

# Drammer: Deterministic Rowhammer Attacks on Mobile Platforms

by

*A bunch of pasty faced sad sack nerds sitting in a basement want to sound cool and tough, like they've just done a tour in 'Nam. [slashdot]*

# Drammer: Deterministic Rowhammer Attacks on Mobile Platforms

*Victor van der Veen<sup>1</sup>, Yanick Fratantonio<sup>2</sup>, Martina Lindorfer<sup>2</sup>,  
Daniel Gruss<sup>3</sup>, Clémentine Maurice<sup>3</sup>, Giovanni Vigna<sup>2</sup>,  
Herbert Bos<sup>1</sup>, Kaveh Razavi<sup>1</sup>, and Cristiano Giuffrida<sup>1</sup>*

<sup>1</sup>Vrije Universiteit Amsterdam, <sup>2</sup>UC Santa Barbara, <sup>3</sup>TU Graz



# Drammer: Deterministic Rowhammer Attacks on Mobile Platforms

Your takeaway message of today

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## Rowhammer on ARM

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Your takeaway message of today

Rowhammer on ARM  
Deterministic exploitation

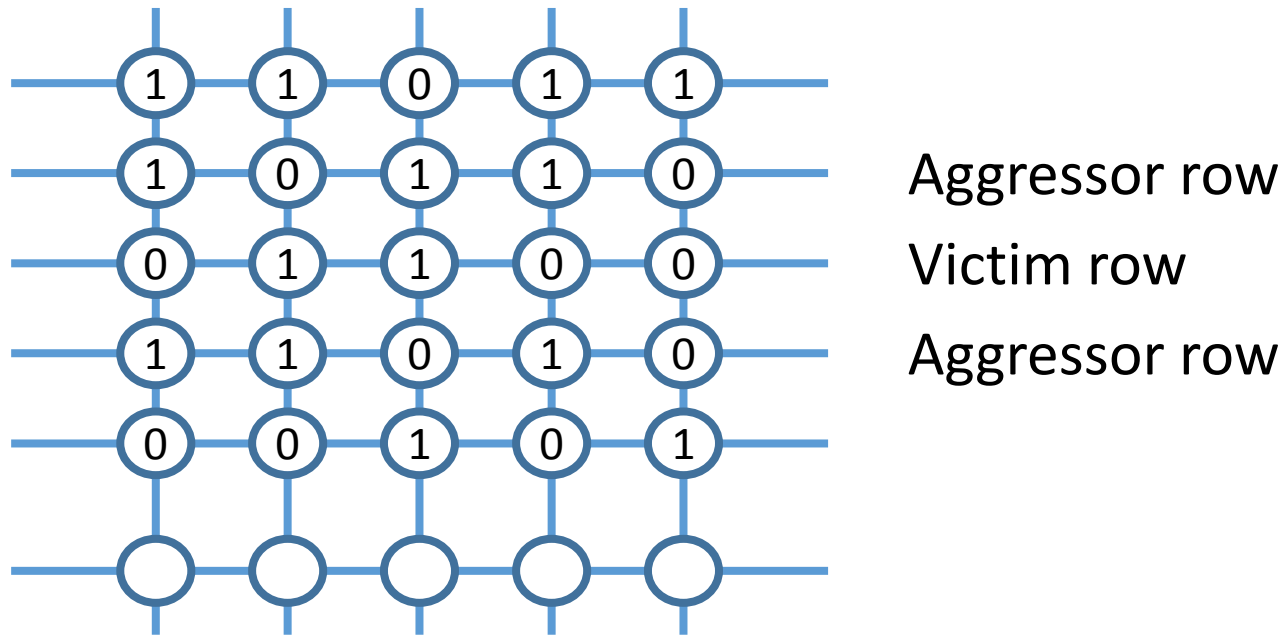
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Your takeaway message of today

Rowhammer on ARM  
Deterministic exploitation  
Works on a Google Pixel

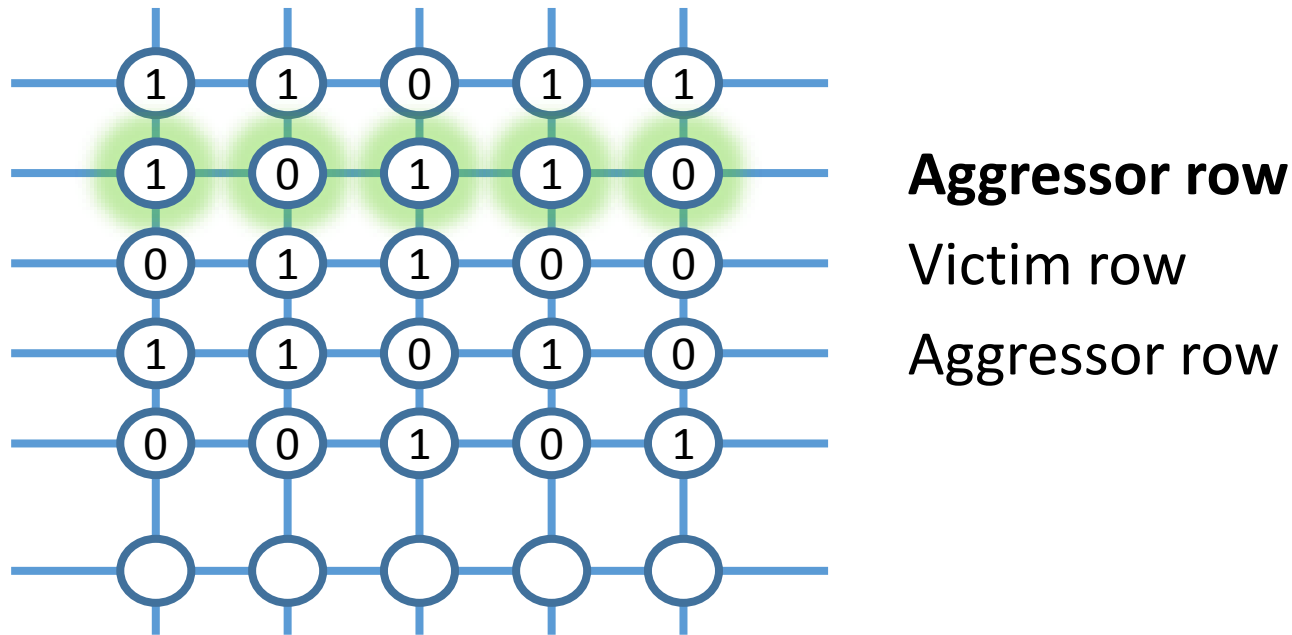
## Flipping bits in memory

DRAM hardware glitch causing disturbance errors



## Flipping bits in memory

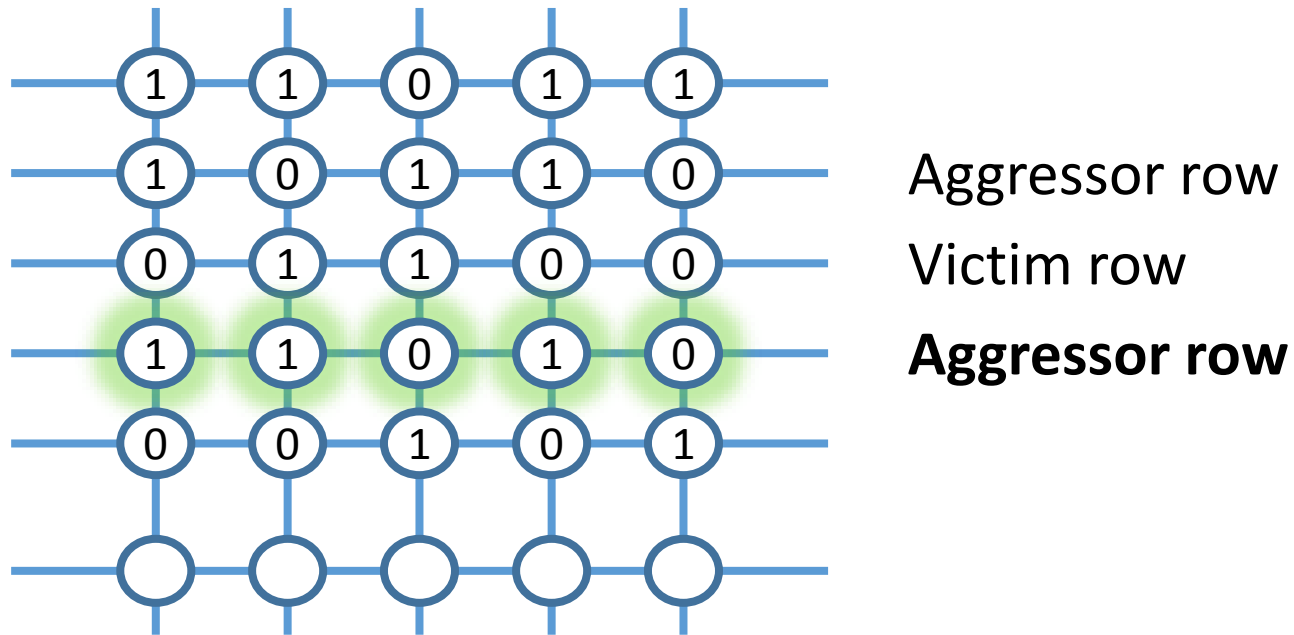
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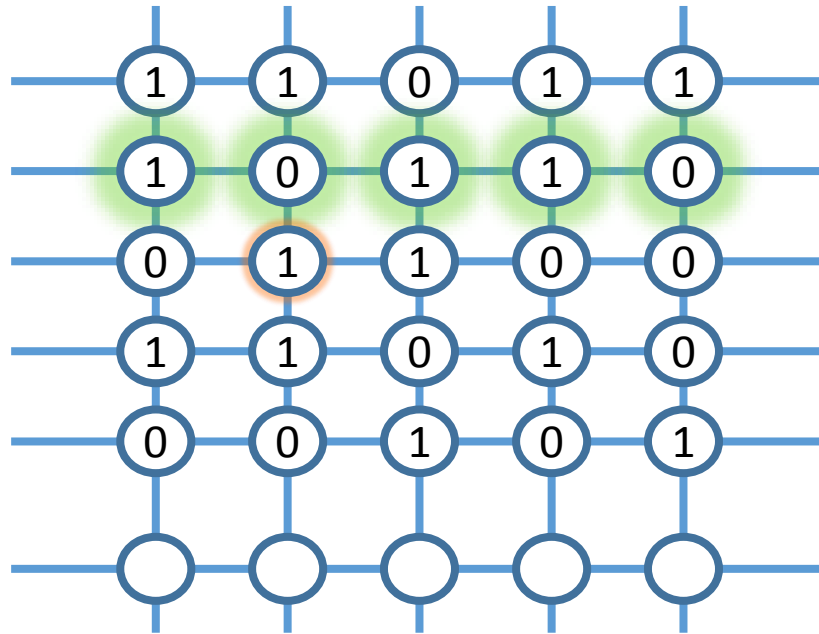
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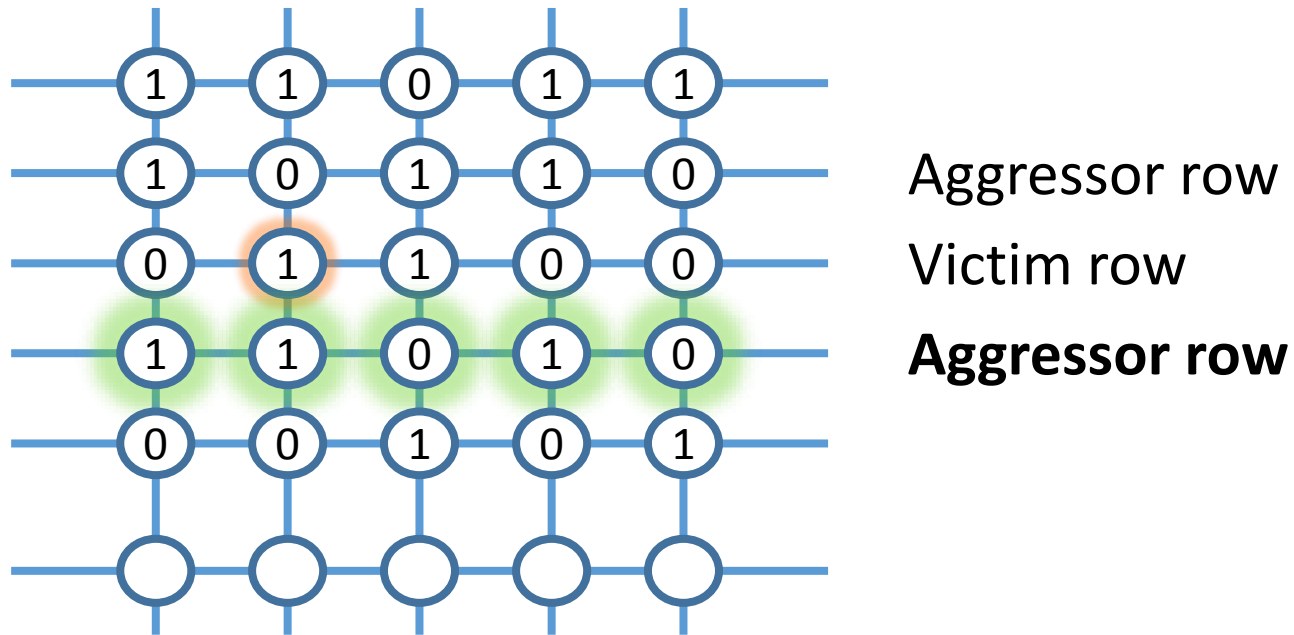
**Aggressor row**

Victim row

Aggressor row

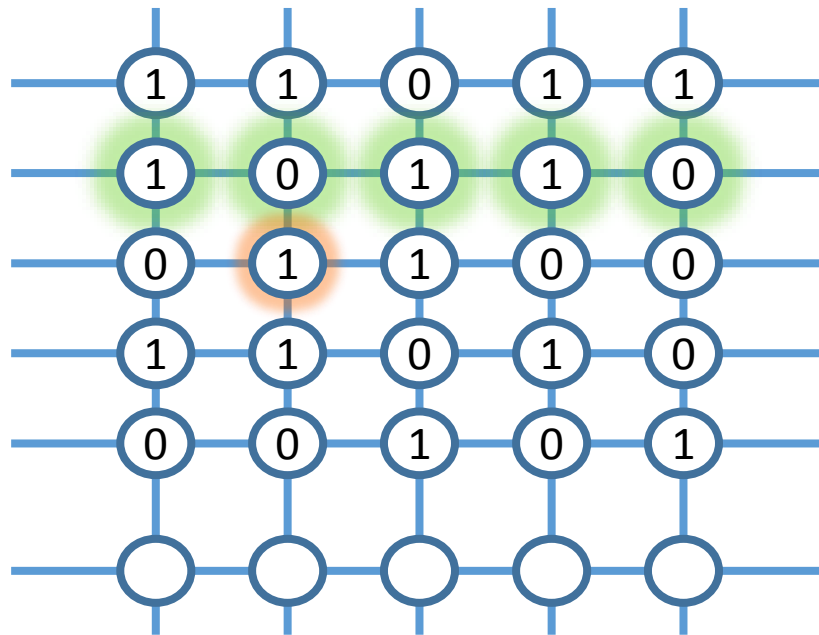
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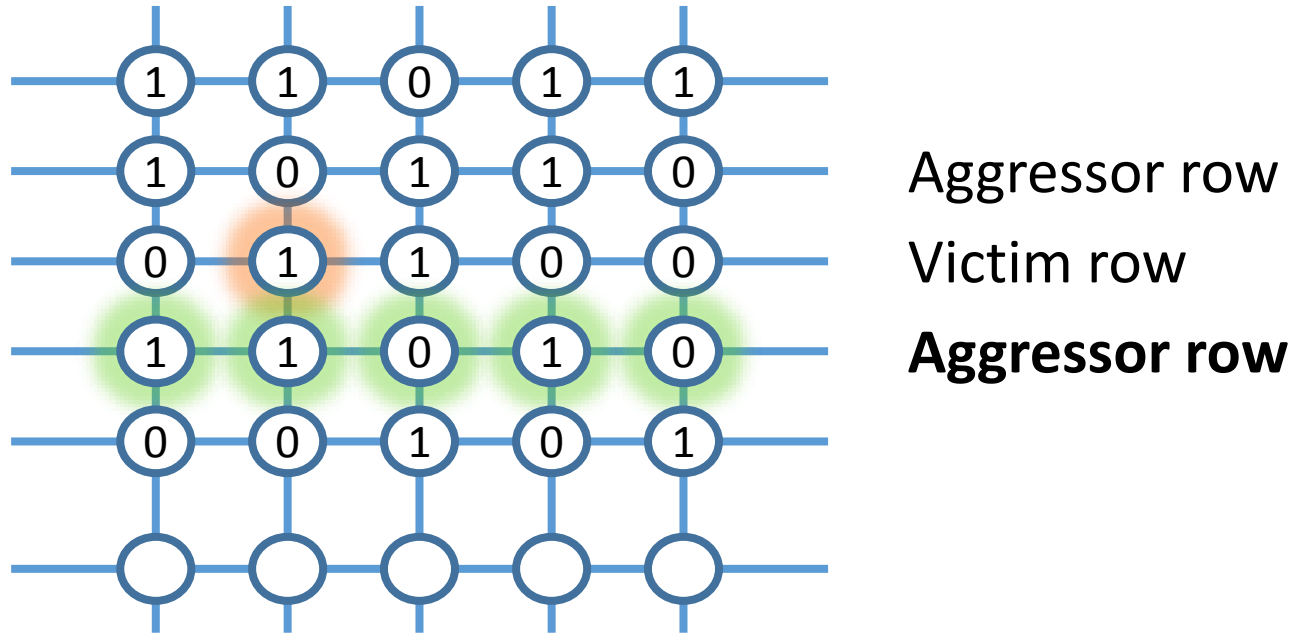
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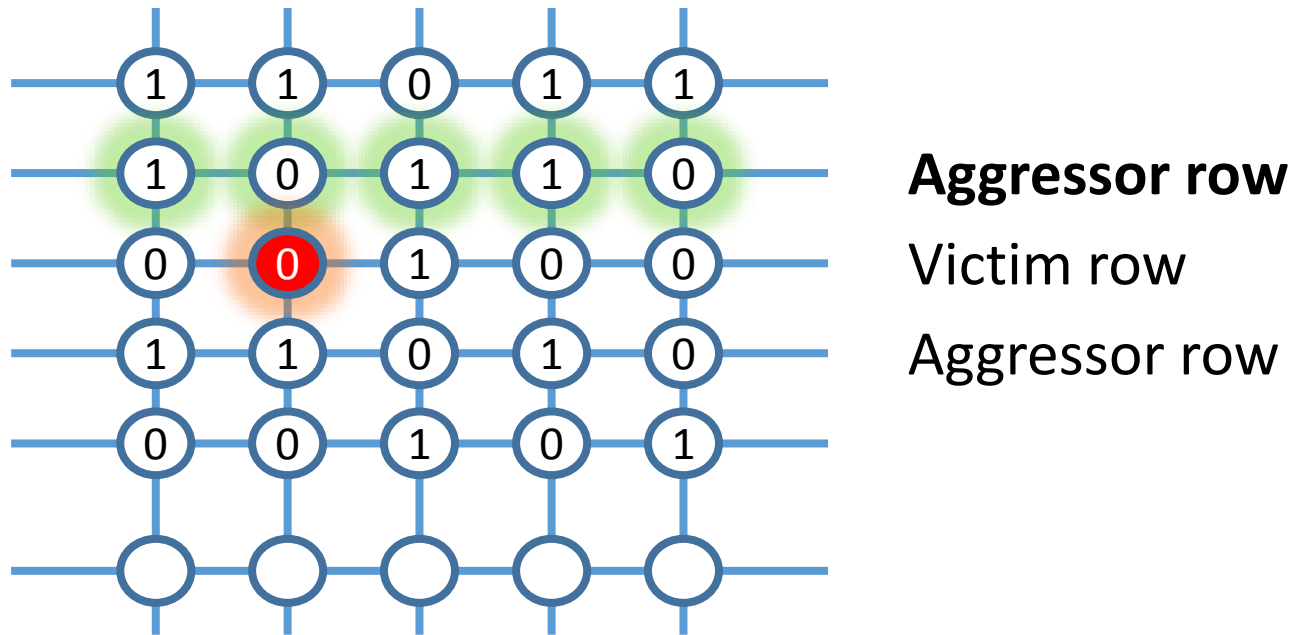
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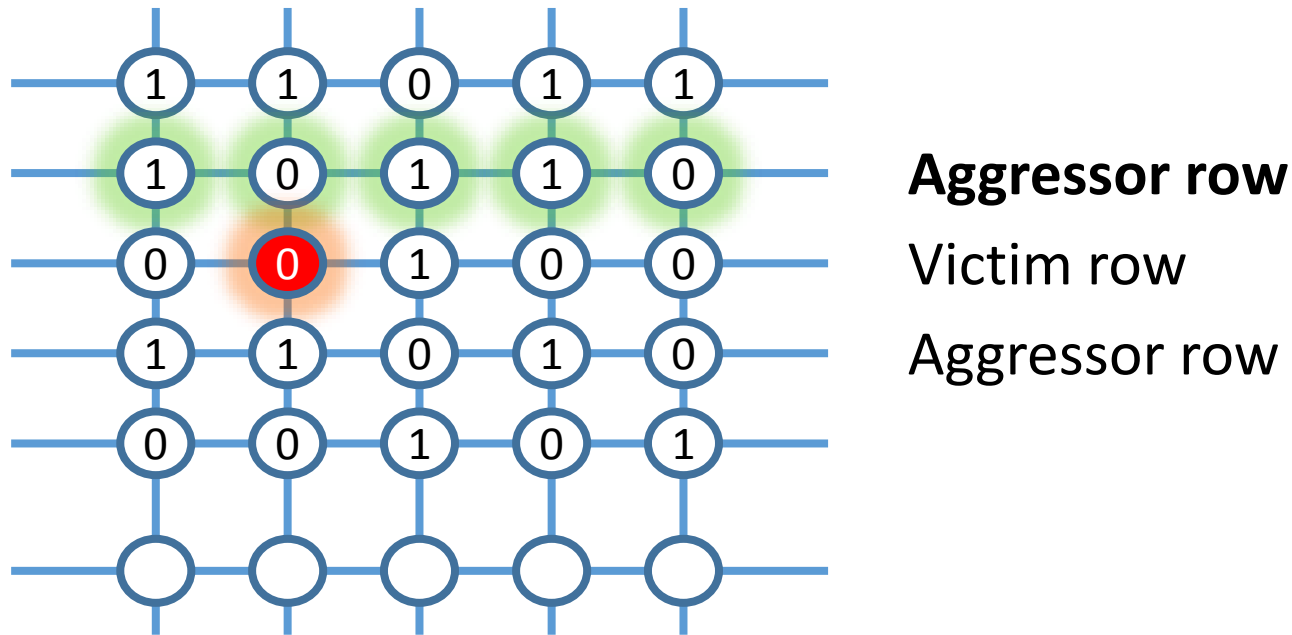
## Flipping bits in memory

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## Flipping bits in memory

DRAM hardware glitch causing disturbance errors



- Not every bit may flip
- Once a bit flips, we can reproduce it

# Overview

1. **Memory Templating**  
Scan memory for useful bit flips



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Store a crucial data structure on a vulnerable page

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- 1. Memory Templating**  
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- 2. Land sensitive data**  
Store a crucial data structure on a vulnerable page
- 3. Reproduce the bit flip**  
Modify the data structure and get root acces

# Overview

1. **Memory Templating**  
Scan memory for useful bit flips

# Templating

## Uncached memory access

- **clflush**
- cache eviction
- non-temporal access instructions

## Determining the physical addresses aggressor/victim rows

- **/proc/self/pagemap**
- 2MB huge pages (relative)

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But does it work on ARM?

Nope

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None of them



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- 2MB huge pages (relative)

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(and we tried)

# Templating on ARM

## DMA

### Direct Memory Access

Android's DMA memory allocator provides everything we need:

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Physical memory:

**DMA ALLOCATED CHUNK**

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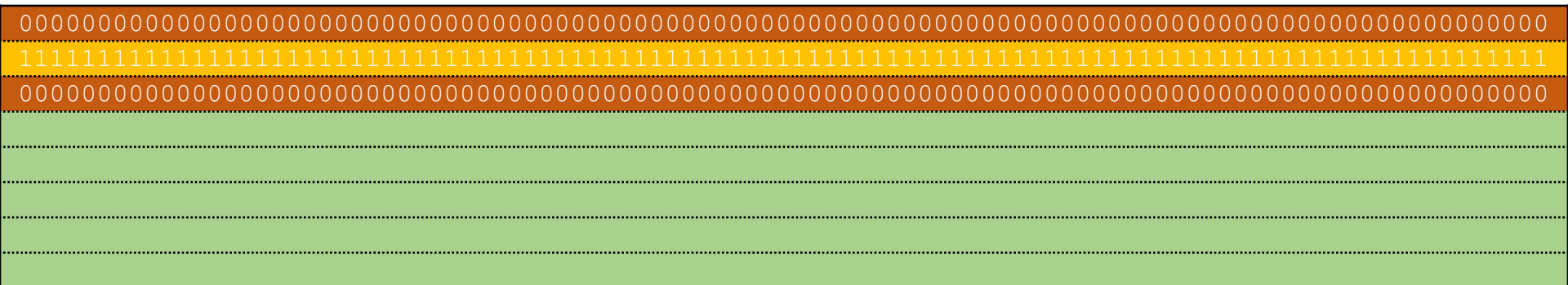
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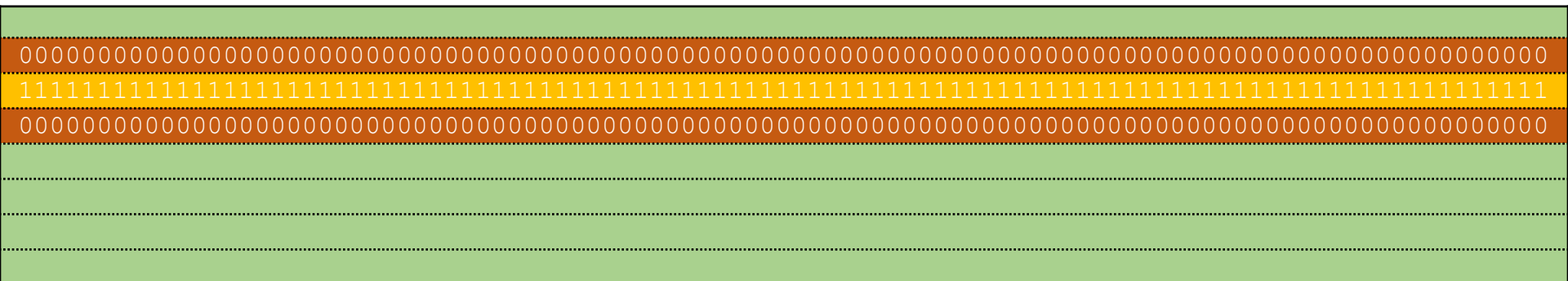
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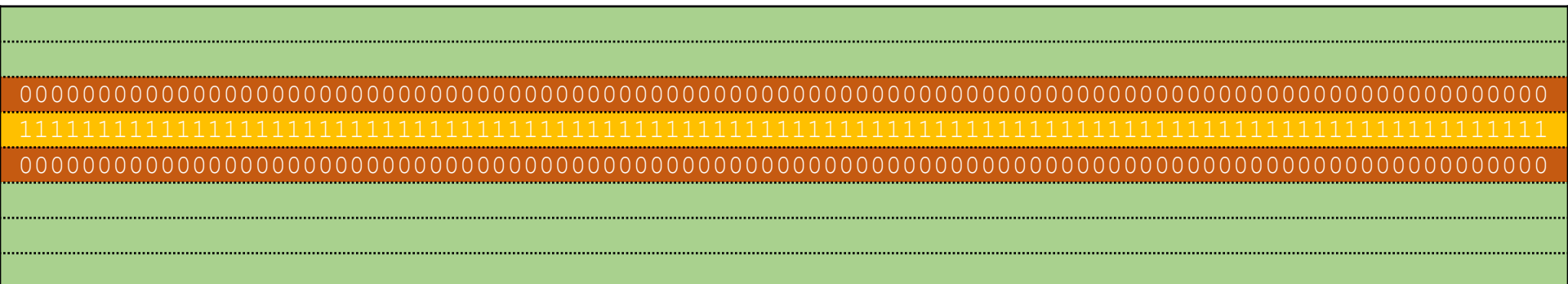
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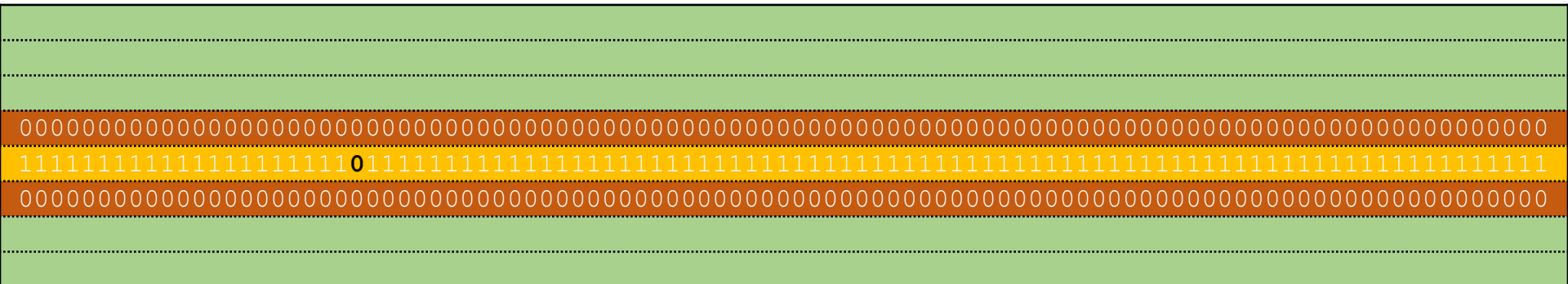
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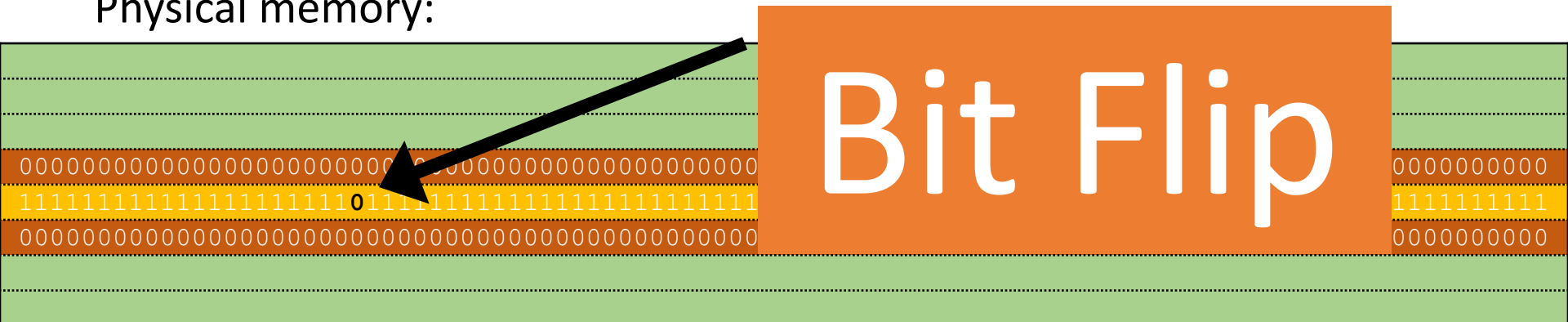
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# Overview

1. Memory Templating  
Scan memory for useful bit flips
2. Land a Page Table  
Store a page table on a vulnerable page

But why?

# Page Tables

Mapping virtual addresses to physical addresses

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Mapping virtual addresses to physical addresses

Example lookup for input virtual address `0xb6a5717f`

1	0	1	1	0	1	1	0	1	0	1	0	0	1	0	1	1	1	0	0	0	1	0	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

# Page Tables

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---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Highest 12 bits: *level 1 table index* (*Translation Table Base Register*)

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Mapping virtual addresses to physical addresses

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1	0	1	1	0	1	1	0	1	0	1	0	0	1	0	1	1	1	0	0	0	1	0	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

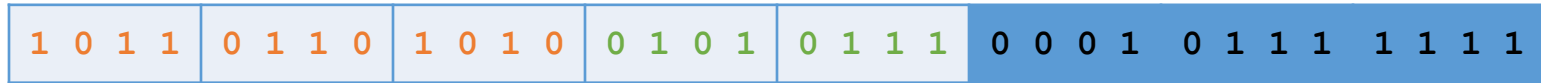
- Highest 12 bits: *level 1 table index* (*Translation Table Base Register*)
- Middle 8 bits: *level 2 table index*



# Page Tables

Mapping virtual addresses to physical addresses

Example lookup for input virtual address `0xb6a5717f`



- Highest 12 bits: *level 1 table index* (*Translation Table Base Register*)
- Middle 8 bits: *level 2 table index*
- Lowest 12 bits: *offset in page*

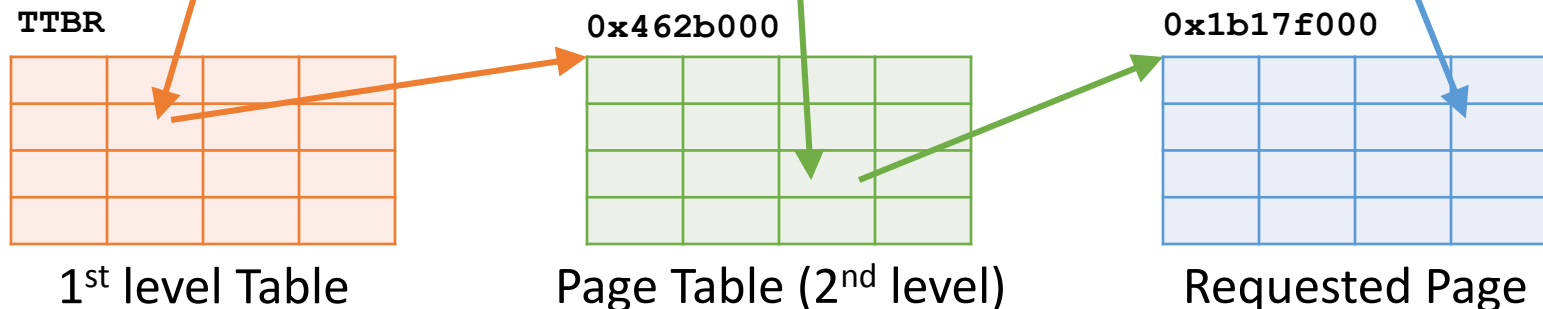
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1	0	1	1	0	1	1	0	1	0	1	0	0	1	0	1	0	1	1	1	0	0	0	1	0	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

- Highest 12 bits: *level 1 table index* (*Translation Table Base Register*)
- Middle 8 bits: *level 2 table index*
- Lowest 12 bits: *offset in page*



# Page Table Entries

Entry in the (2<sup>nd</sup> level) Page Table

0 0 0 1	1 0 1 1	0 0 0 1	0 1 1 1	1 1 1 1	x x x x	x x x x	x x x x
---------	---------	---------	---------	---------	---------	---------	---------

# Page Table Entries

## Entry in the (2<sup>nd</sup> level) Page Table

0 0 0 1	1 0 1 1	0 0 0 1	0 1 1 1	1 1 1 1	x x x x	x x x x	x x x x
---------	---------	---------	---------	---------	---------	---------	---------

- 12 bits of *properties*

# Page Table Entries

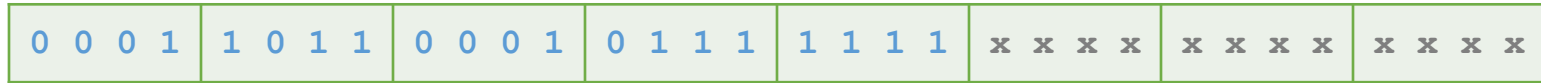
## Entry in the (2<sup>nd</sup> level) Page Table

0 0 0 1	1 0 1 1	0 0 0 1	0 1 1 1	1 1 1 1	x x x x	x x x x	x x x x
---------	---------	---------	---------	---------	---------	---------	---------

- 12 bits of *properties*
- 20 bits for the *page base address*

# Page Table Entries

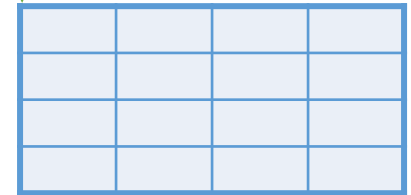
## Entry in the (2<sup>nd</sup> level) Page Table



$0x1b17f \ll 12$

- 12 bits of *properties*
- 20 bits for the *page base address*

$0x1b17f000$

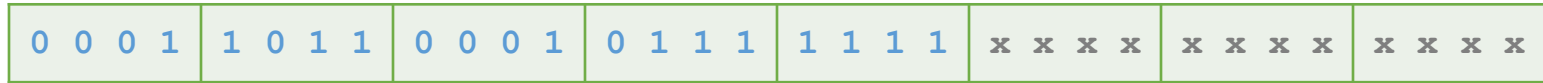


mapped page

What if we flip a bit in the entry?

# Rowhammer Attacks on Page Table Entries

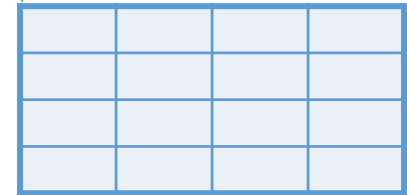
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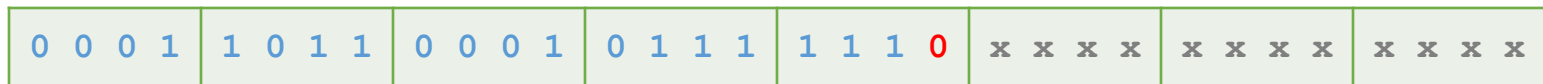
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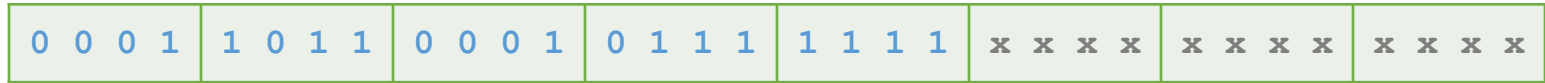


mapped page



# Rowhammer Attacks on Page Table Entries

Entry in the (2<sup>nd</sup> level) Page Table

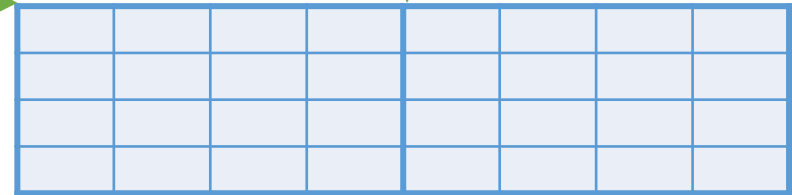


$0x1b17f \ll 12$

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0x1b17e000

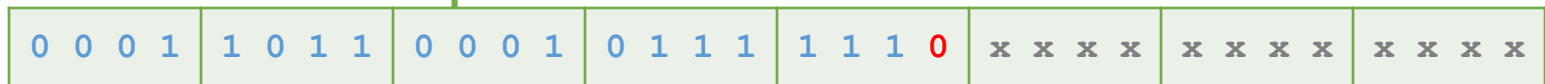
0x1b17f000



mapped page

mapped page

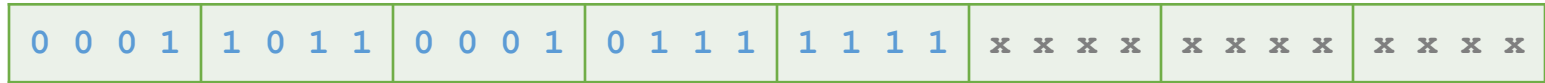
$0x1b17e \ll 12$





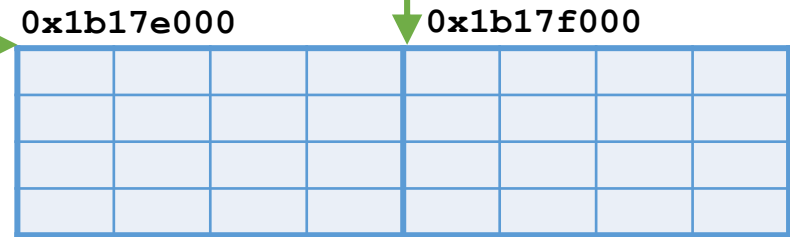
# Rowhammer Attacks on Page Table Entries

## Entry in the (2<sup>nd</sup> level) Page Table



$0x1b17f \ll 12$

- 12 bits of *properties*
- 20 bits for the *page base address*



mapped page

~~mapped page~~

$0x1b17e \ll 12$



A 1-to-0 flip moves the mapping 'to the left'

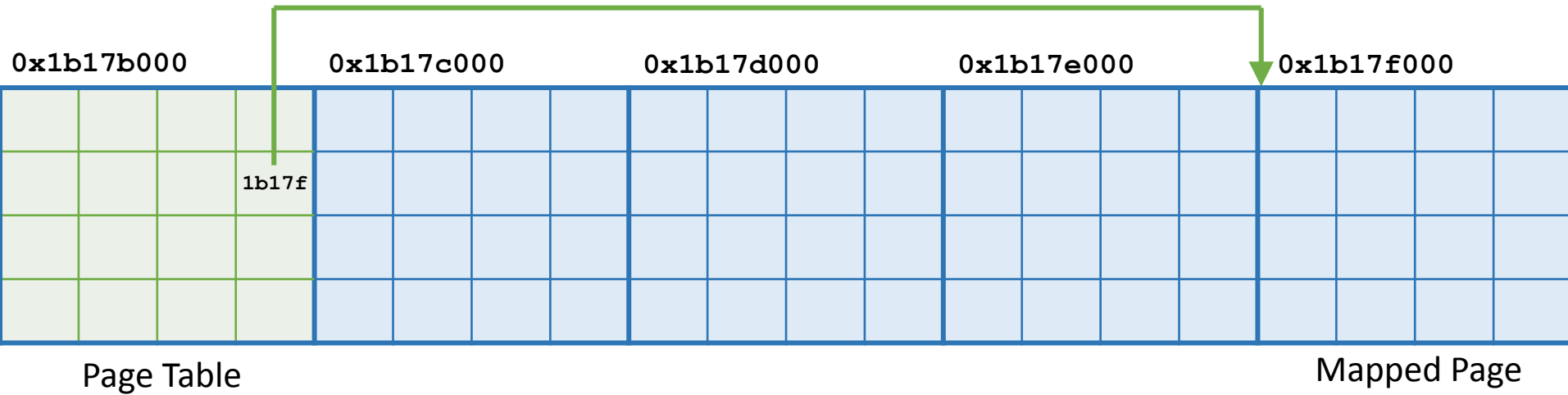
- Flip offset 0: -1 page
- Flip offset 1: -2 pages
- Flip offset 2: -4 pages
- Flip offset  $n$ :  $-2^n$  pages

Drammer: **Deterministic** Rowhammer **Attacks on** Mobile Platforms  
Page Table Entries

1. Map a page 4 pages 'away' from its page table

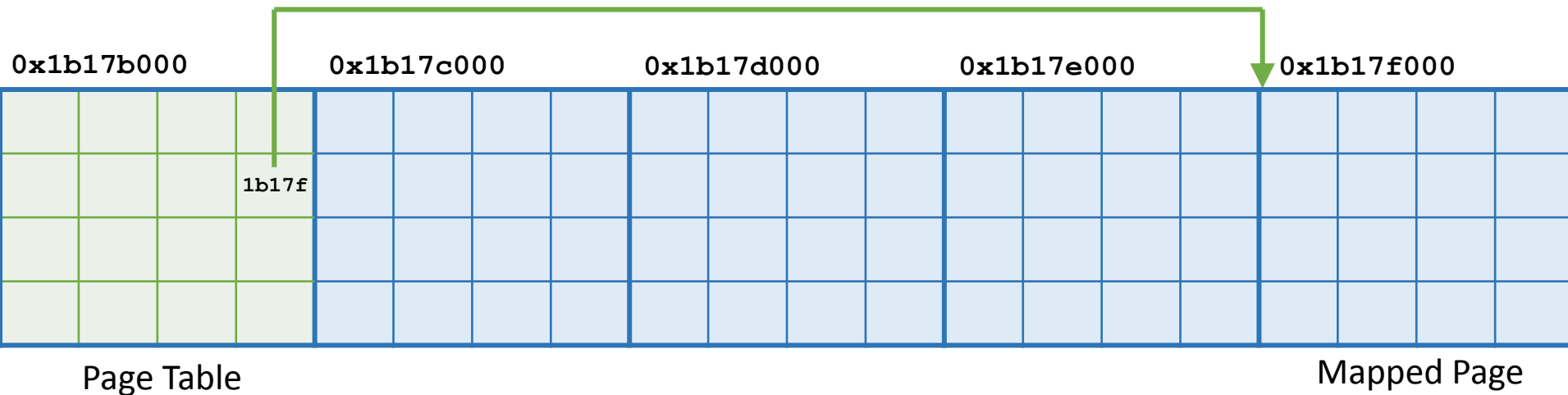
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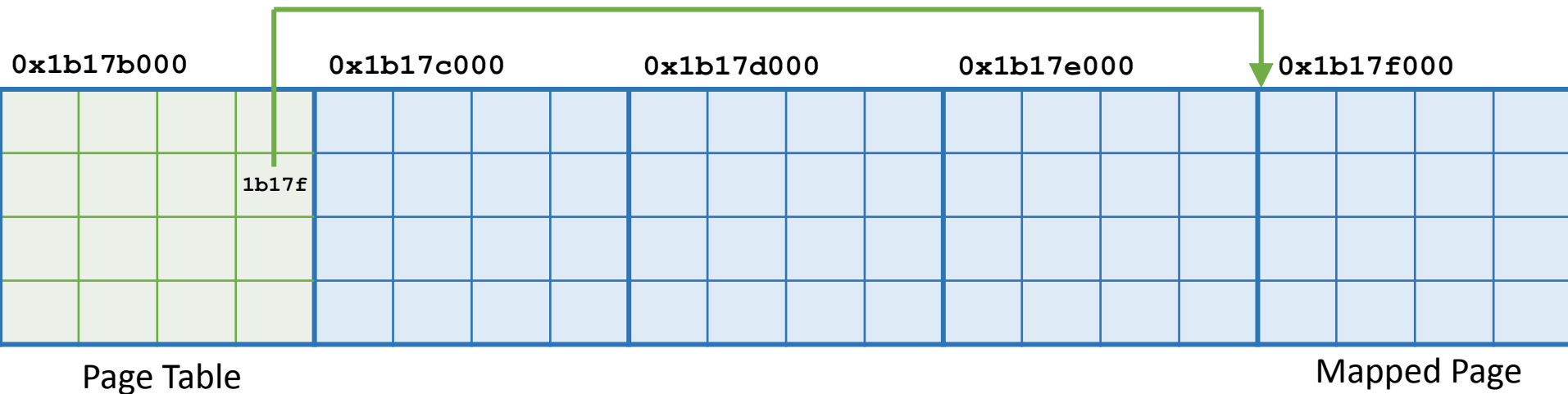
Virtual address 0xb6a57000 maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page **0x1b17f000**

# Deterministic Attacks on Page Table Entries

1. Map a page 4 pages 'away' from its page table
2. Flip bit 2 in the page table entry



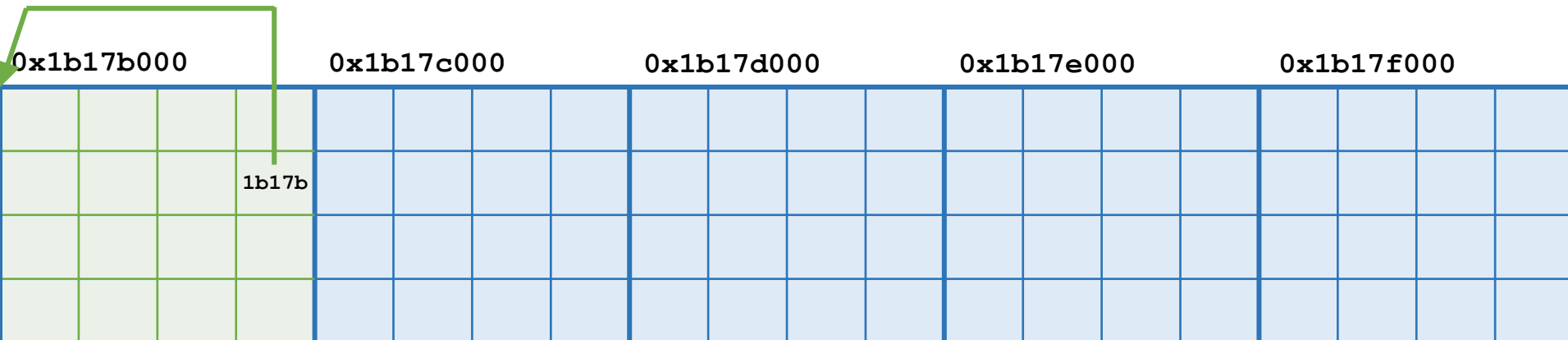
Virtual address 0xb6a57000 maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page **0x1b17f000**

# Deterministic Attacks on Page Table Entries

1. Map a page 4 pages 'away' from its page table
2. Flip bit 2 in the page table entry



Mapped Page Table

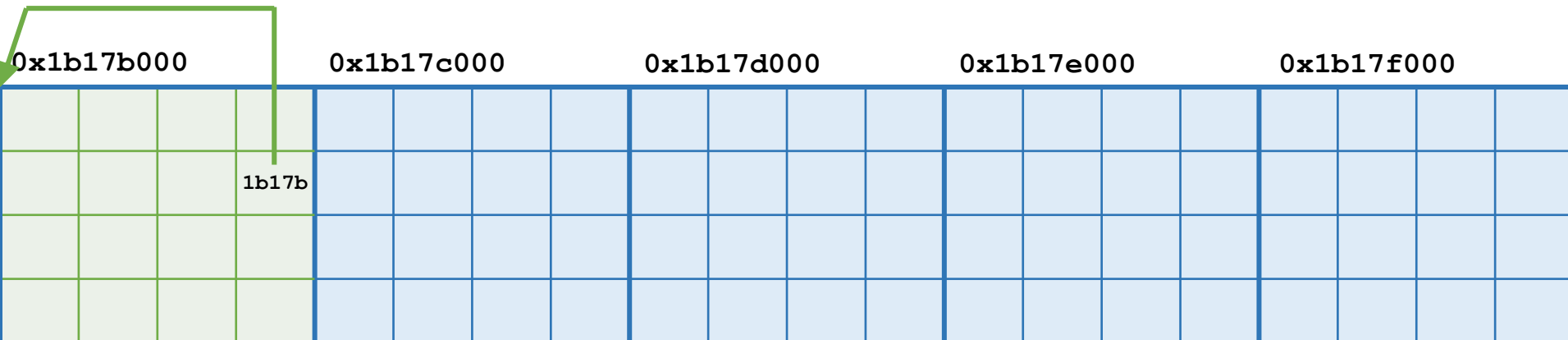
Virtual address `0xb6a57000` maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	0	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page `0x1b17b000`

# Deterministic Attacks on Page Table Entries

1. Map a page 4 pages 'away' from its page table
2. Flip bit 2 in the page table entry
3. Write page table entries



Mapped Page Table

Virtual address 0xb6a57000 maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	0	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page **0x1b17b000**

# Deterministic Attacks on Page Table Entries

1. Map a page 4 pages 'away' from its page table
2. Flip bit 2 in the page table entry
3. Write page table entries

0x1b17b000				0x1b17c000				0x1b17d000				0x1b17e000				0x1b17f000			
3ac90	3ac91	3ac92	3ac93																
3ac94	3ac95	3ac96	1b17b																
3ac97	3ac98	3ac99	3ac9a																
3ac9b	3ac9c	3ac9d	3ac9e																

Mapped Page Table

Virtual address 0xb6a57000 maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	0	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page 0x1b17b000



# Deterministic Attacks on Page Table Entries

1. Map a page 4 pages 'away' from its page table
2. Flip bit 2 in the page table entry
3. Write page table entries
4. Read/write kernel memory

0x1b17b000				0x1b17c000				0x1b17d000				0x1b17e000				0x1b17f000			
3ac90	3ac91	3ac92	3ac93																
3ac94	3ac95	3ac96	1b17b																
3ac97	3ac98	3ac99	3ac9a																
3ac9b	3ac9c	3ac9d	3ac9e																

Mapped Page Table

Virtual address 0xb6a57000 maps to Page Table Entry:

0	0	0	1	1	0	1	1	0	0	0	1	0	1	1	1	1	1	0	1	x	x	x	x	x	x	x	x	x	x	x	x	x	x
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

which translates to physical page 0x1b17b000

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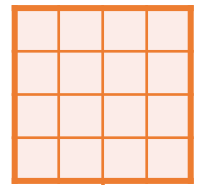
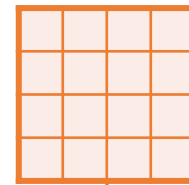
0x1b17b000				0x1b17c000				0x1b17d000				0x1b17e000				0x1b17f000			
3ac90	3ac91	3ac92	3ac93																
3ac94	3ac95	3ac96	1b17b																
3ac97	3ac98	3ac99	3ac9a																
3ac9b	3ac9c	3ac9d	3ac9e																

Mapped Page Table

Virtual address 0xb6a57000 maps to 0x1b17b000

Virtual address 0xb6a58000 maps to 0x3ac97000

Virtual address 0xb6a59000 maps to 0x3ac98000



# Overview

1. Memory Templating  
Scan memory for useful bit flips
2. Land a Page Table  
Store a page table on a vulnerable page

But how?

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

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- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

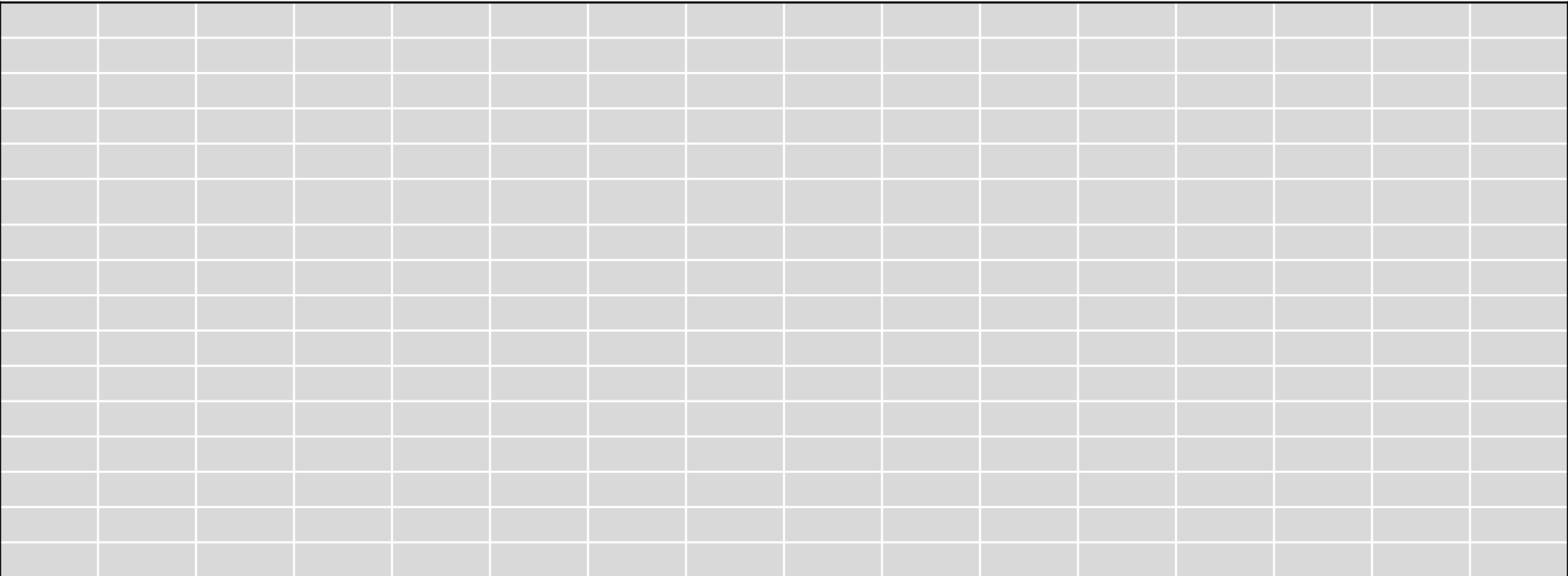
## Phys Feng Shui

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:

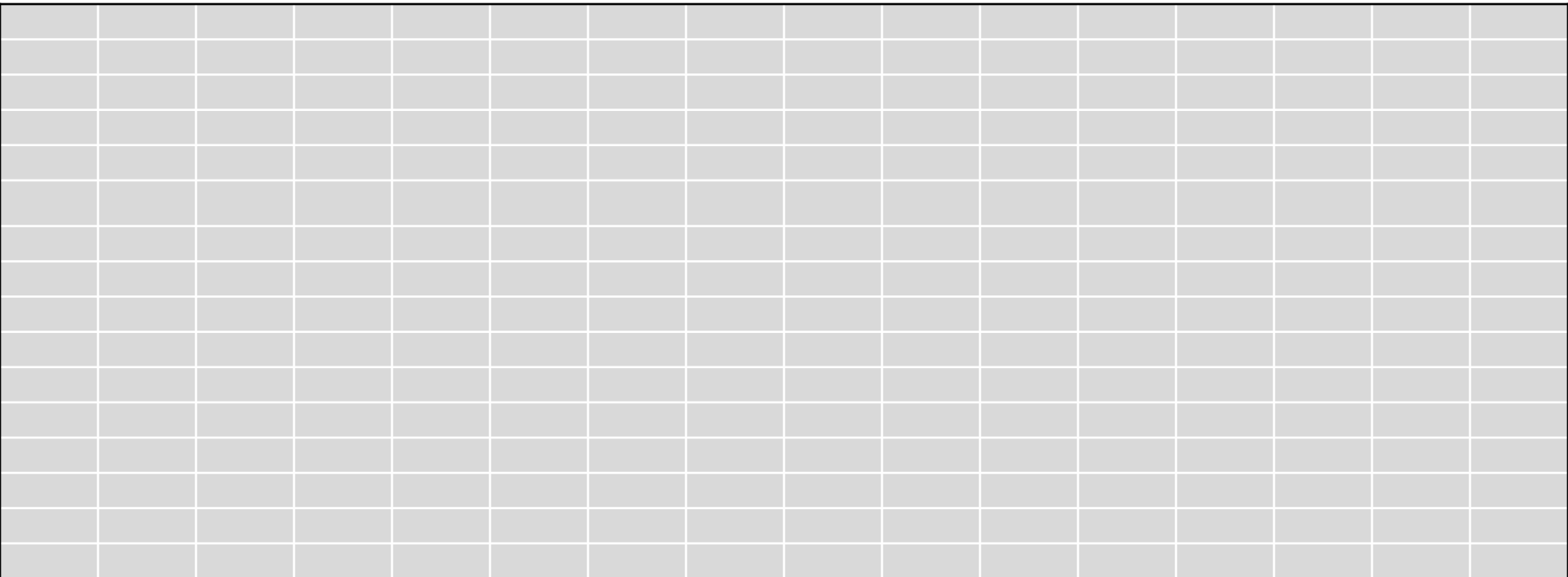


## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



Exhaust all memory

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



Exhaust all memory

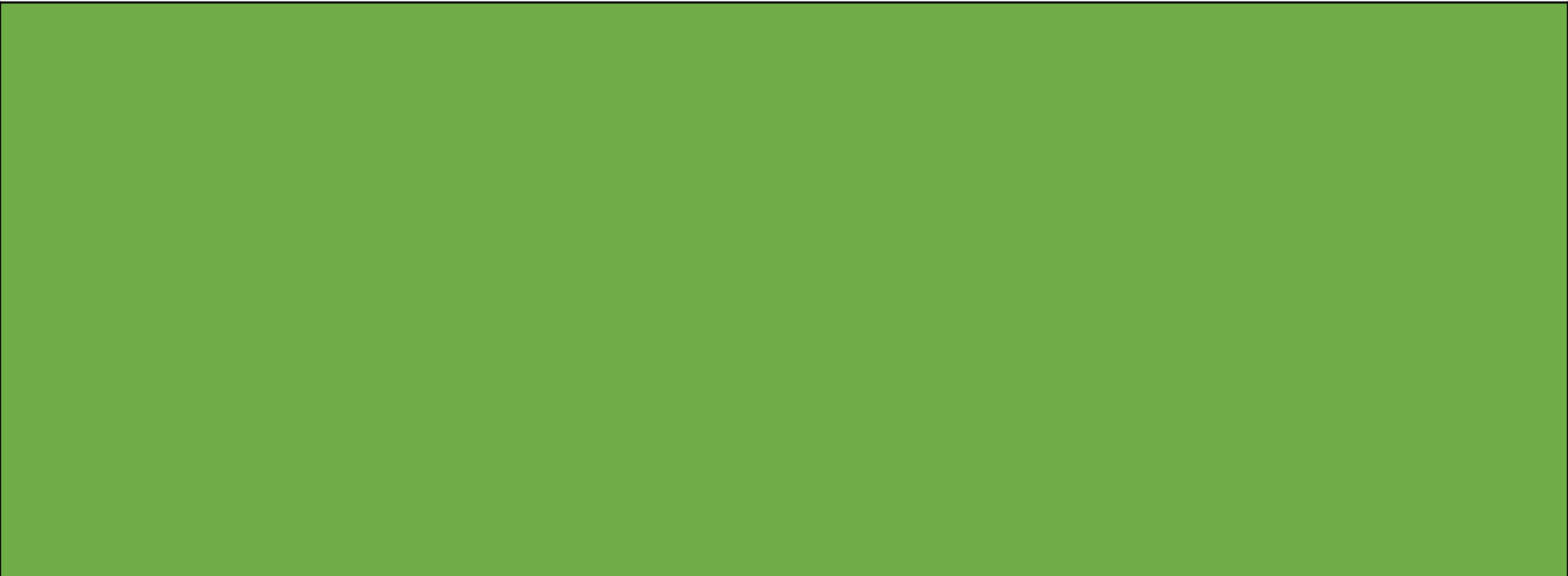


## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



Release the vulnerable page

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



Release the vulnerable page

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



Trigger a Page Table Allocation

## Landing a Page Table

- No access to `pagemap` (virtual – physical address mapping)
- No fancy memory management features (deduplication)

## Phys Feng Shui

Physical memory:



## Trigger a Page Table Allocation

# Phys Feng Shui

Exploit the predictable behavior of the **Buddy Allocator**

Physical Memory



← 16 \* 4KB pages = 64 KB rows →

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

Physical Memory



← 16 \* 4KB pages = 64 KB rows →

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)



The diagram consists of two large, light gray rectangular blocks stacked vertically, separated by a thin black horizontal line. The top block is larger than the bottom block. The text '1024KB' is centered within the top block, and '512KB' is centered within the bottom block. This represents memory buddies of different sizes.

1024KB

512KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X1 = __get_free_pages(flags, 6); // get  $2^6 = 64\text{KB}$  of memory
```



1024KB

A large gray rectangular area representing a memory block of 1024KB. The text '1024KB' is centered within this area.



512KB

A smaller gray rectangular area representing a memory block of 512KB. The text '512KB' is centered within this area.



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X1 = __get_free_pages(flags, 6); // get  $2^6 = 64\text{KB}$  of memory
```



1024KB

256KB

256KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X1 = __get_free_pages(flags, 6); // get  $2^6 = 64\text{KB}$  of memory
```

1024KB

128KB

128KB

256KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X1 = __get_free_pages(flags, 6); // get  $2^6 = 64\text{KB}$  of memory
```

1024KB

64KB

64KB

128KB

256KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X1 = __get_free_pages(flags, 6); // get  $2^6 = 64\text{KB}$  of memory
```

1024KB

X1

64KB

128KB

256KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X2 = __get_free_pages(flags, 3); // get  $2^3 = 8$ KB of memory
```

1024KB

X1

64KB

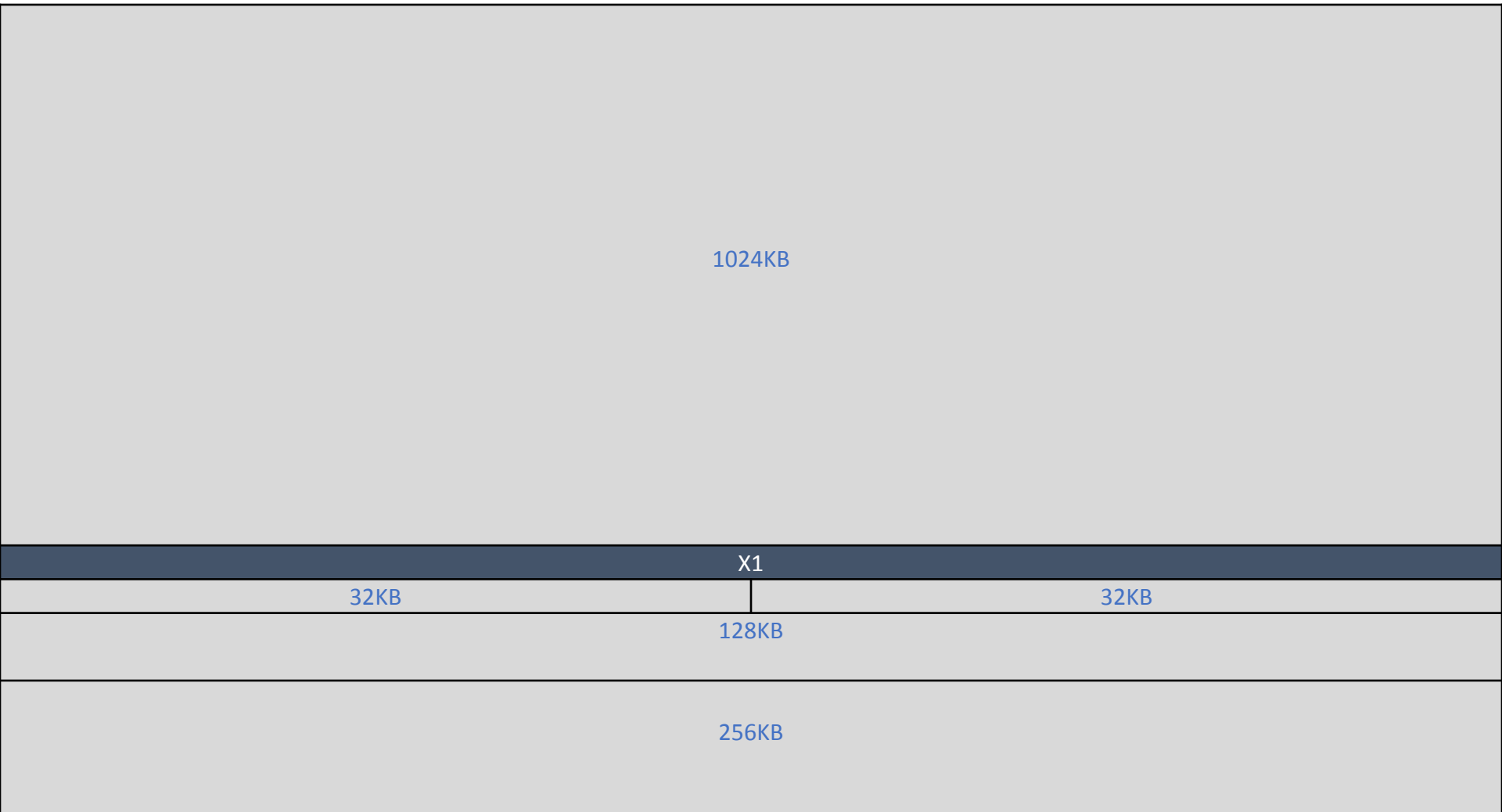
128KB

256KB

# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

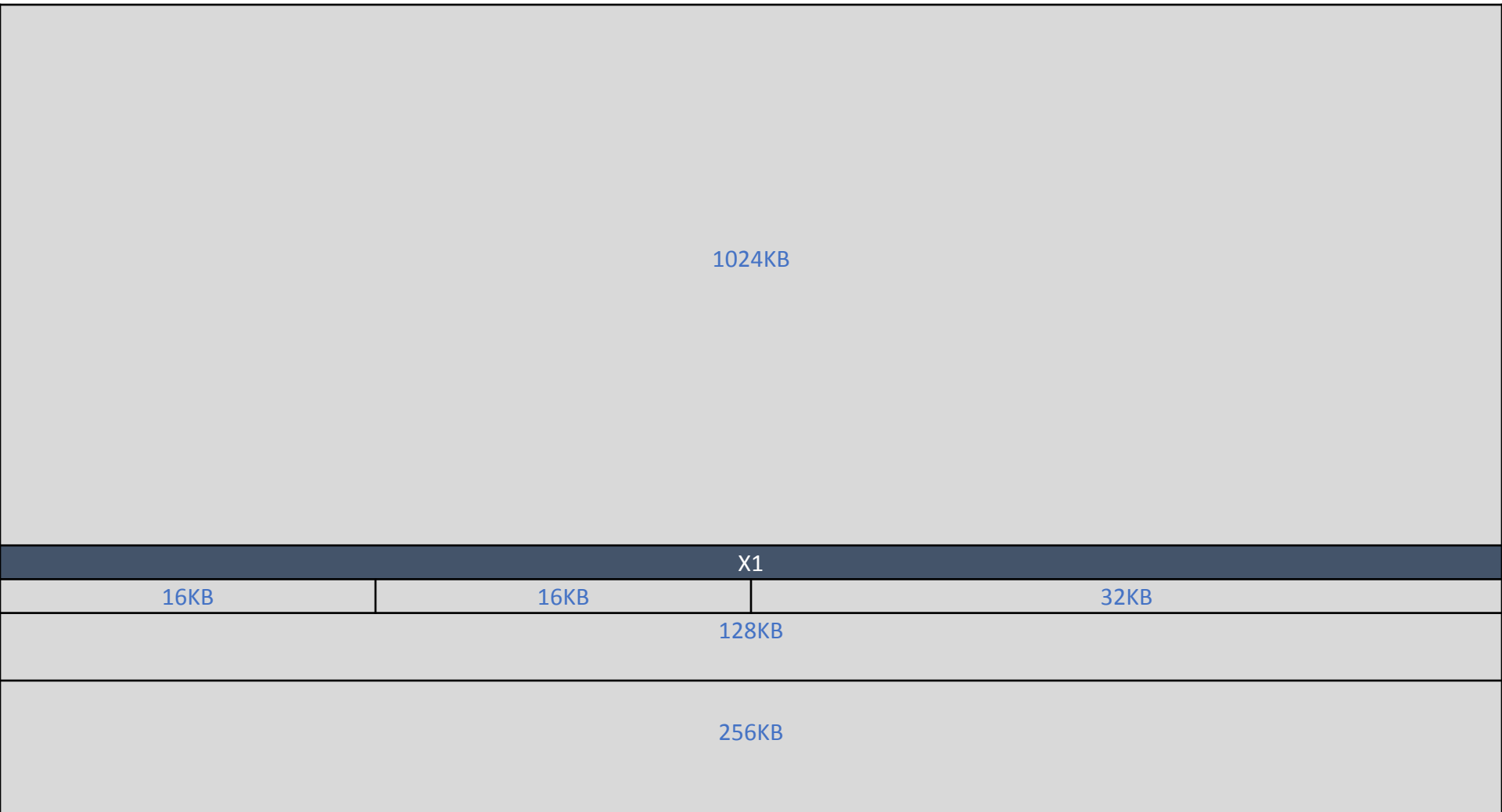
```
X2 = __get_free_pages(flags, 3); // get  $2^3 = 8$ KB of memory
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

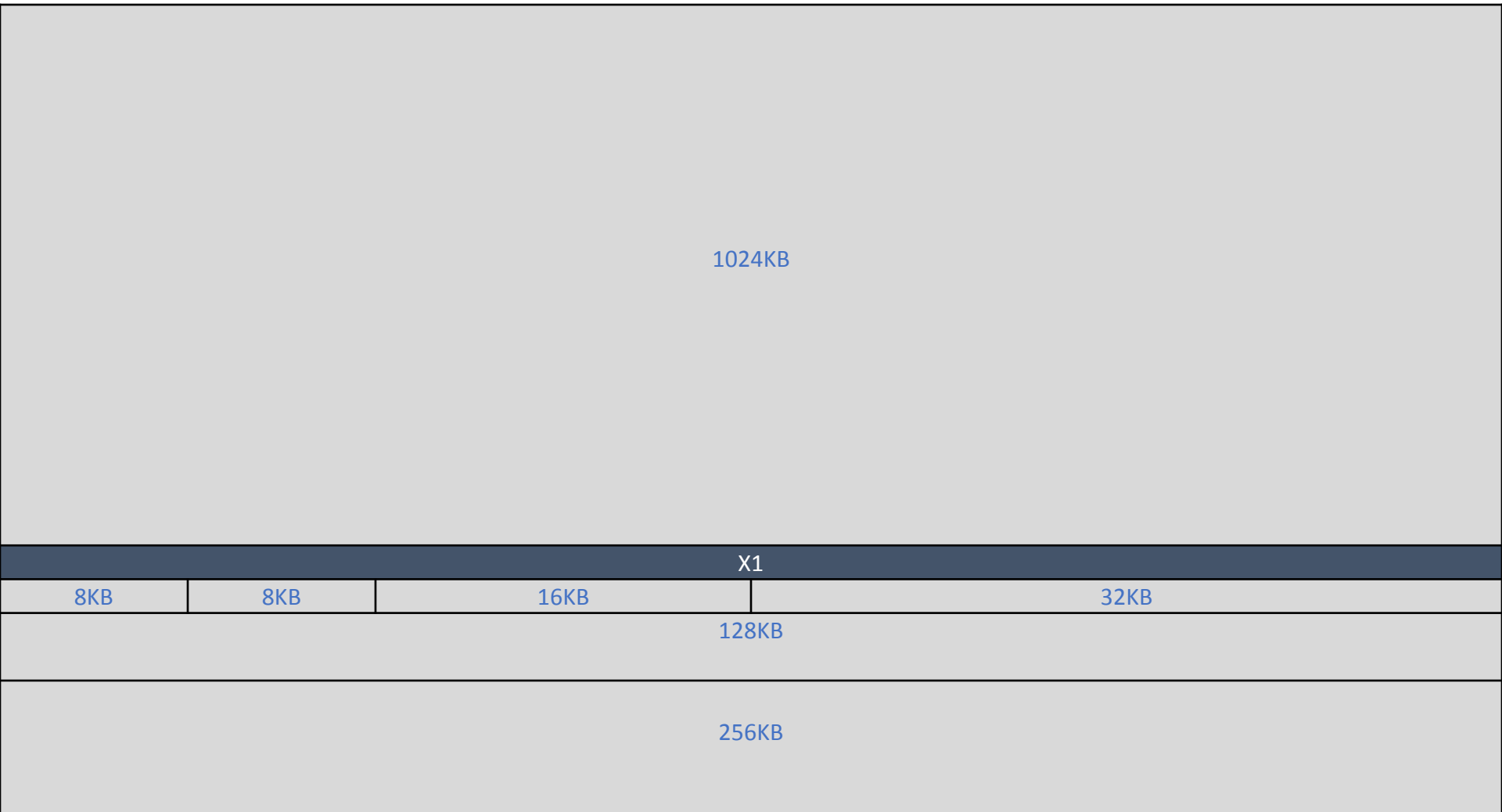
```
X2 = __get_free_pages(flags, 3); // get  $2^3 = 8$ KB of memory
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
X2 = __get_free_pages(flags, 3); // get  $2^3 = 8\text{KB}$  of memory
```

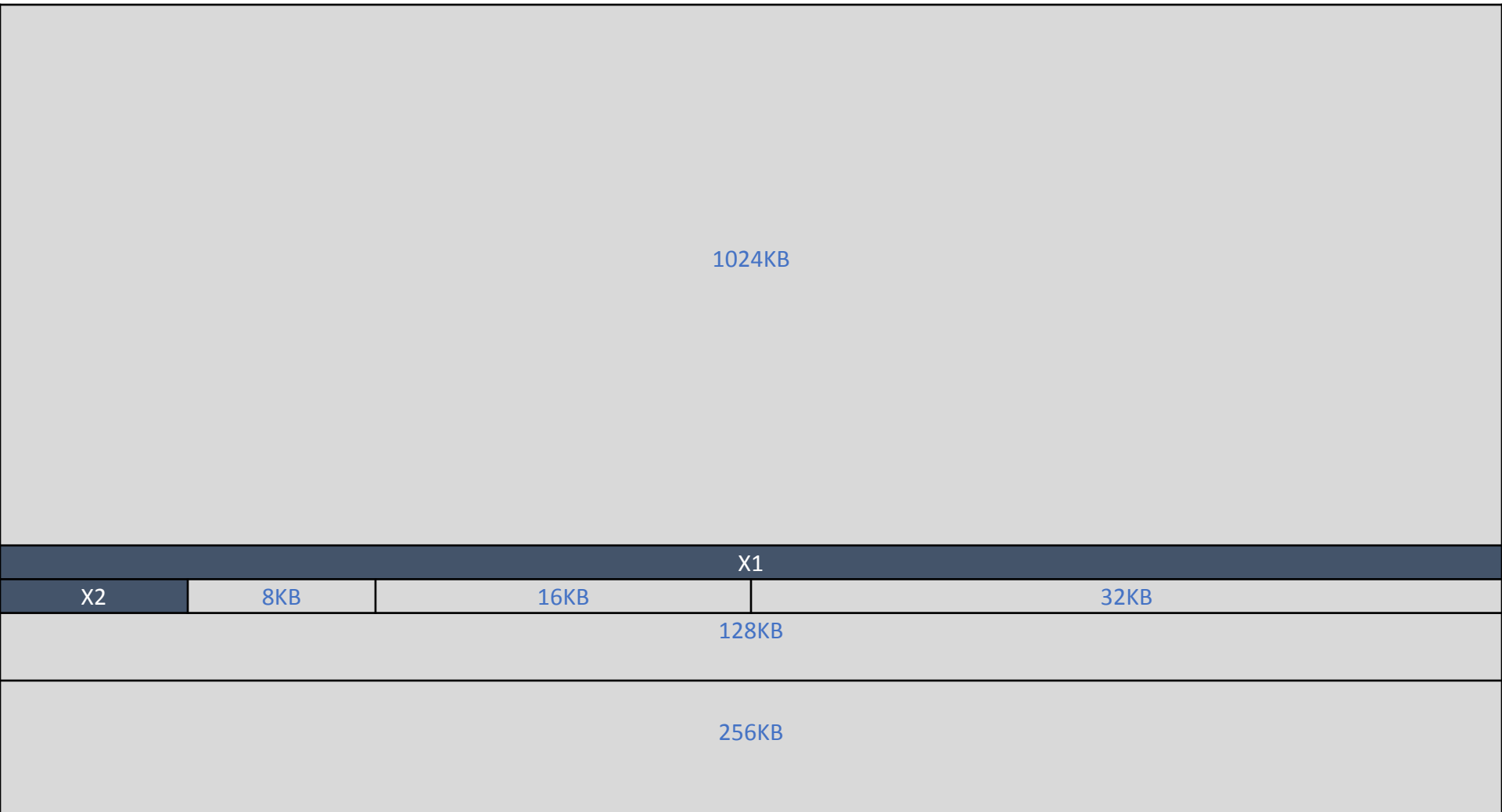




# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

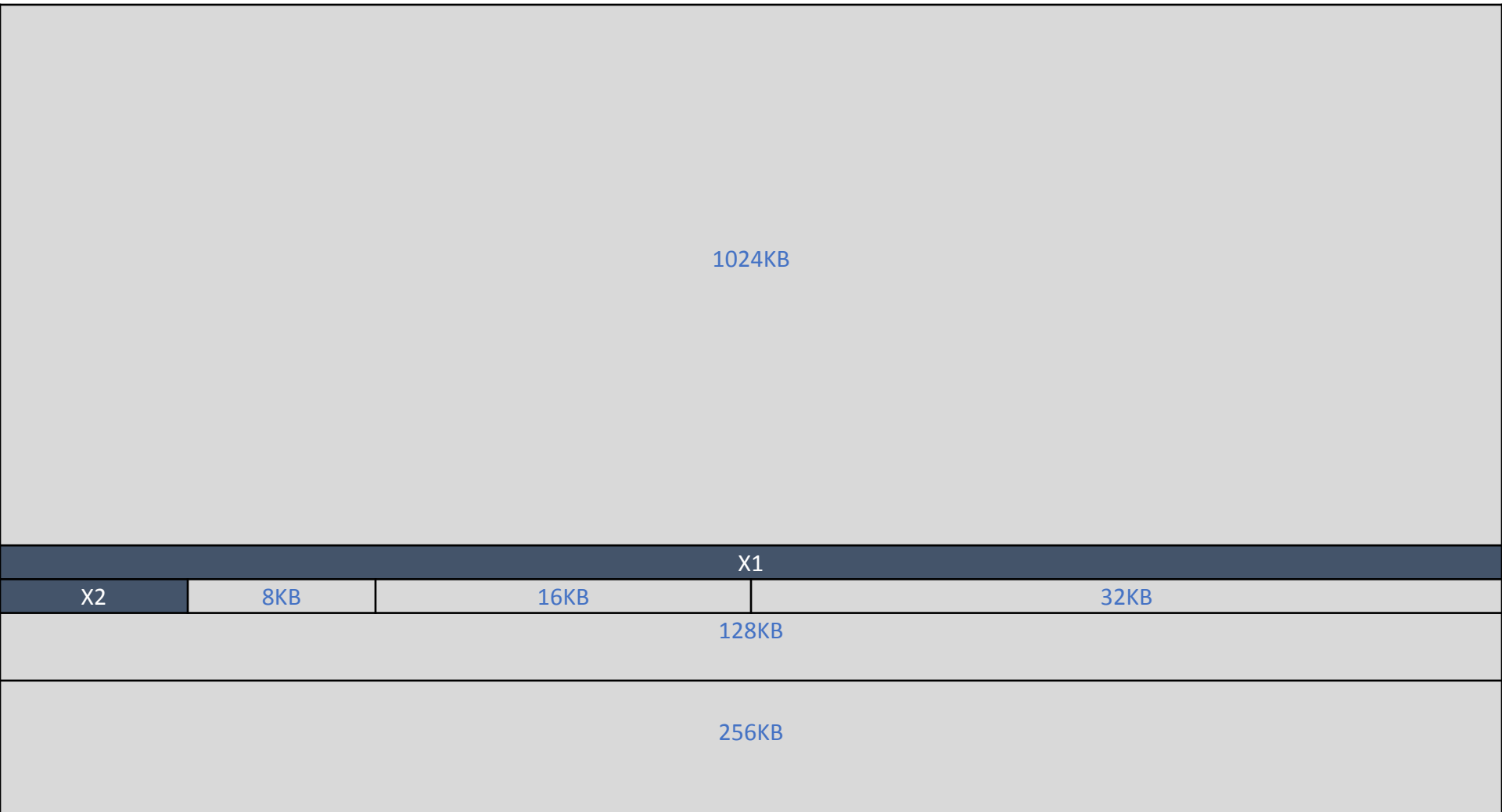
```
X2 = __get_free_pages(flags, 3); // get  $2^3 = 8\text{KB}$  of memory
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

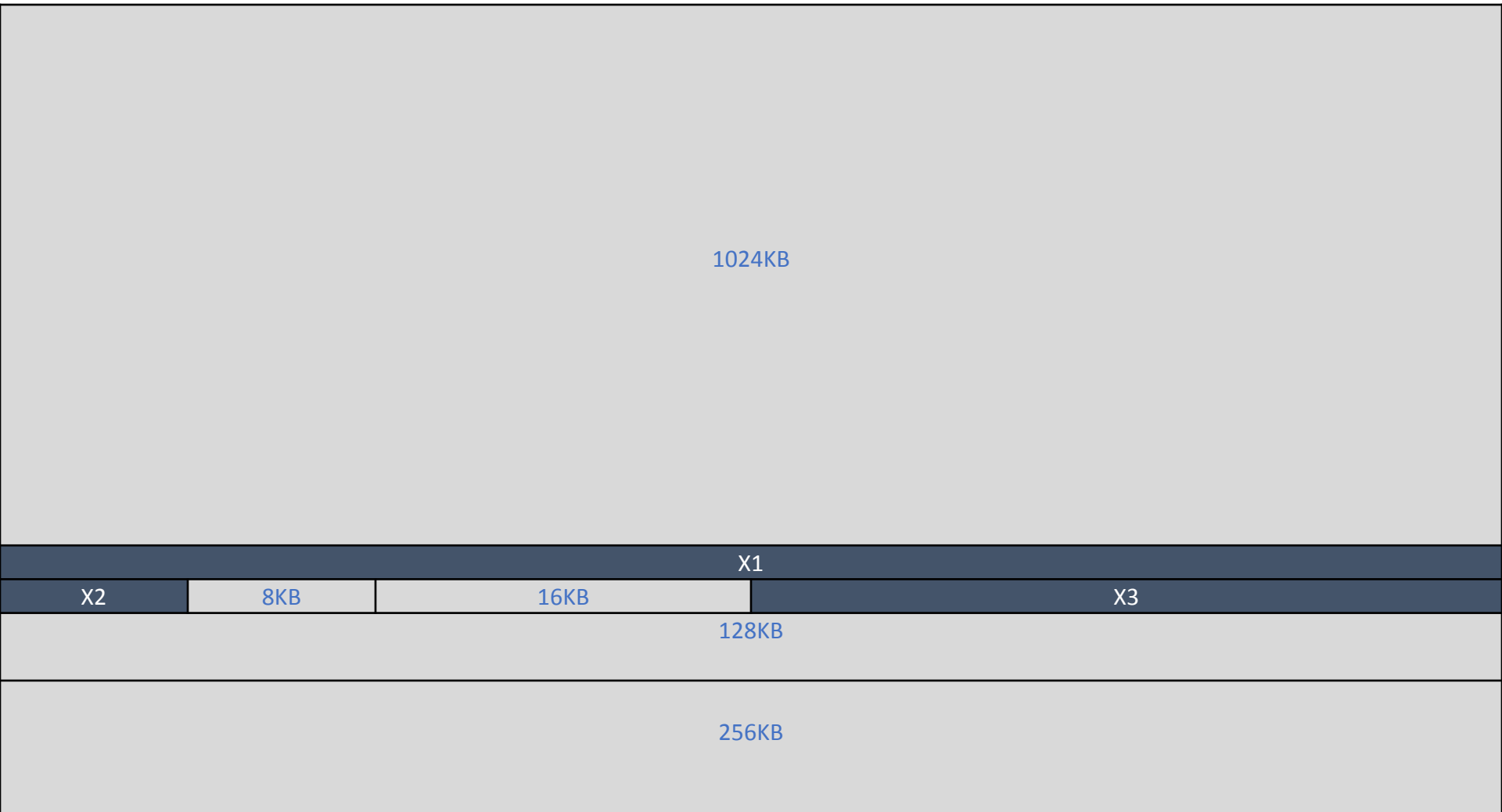
```
X3 = __get_free_pages(flags, 5); // get  $2^3 = 32\text{KB}$  of memory
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

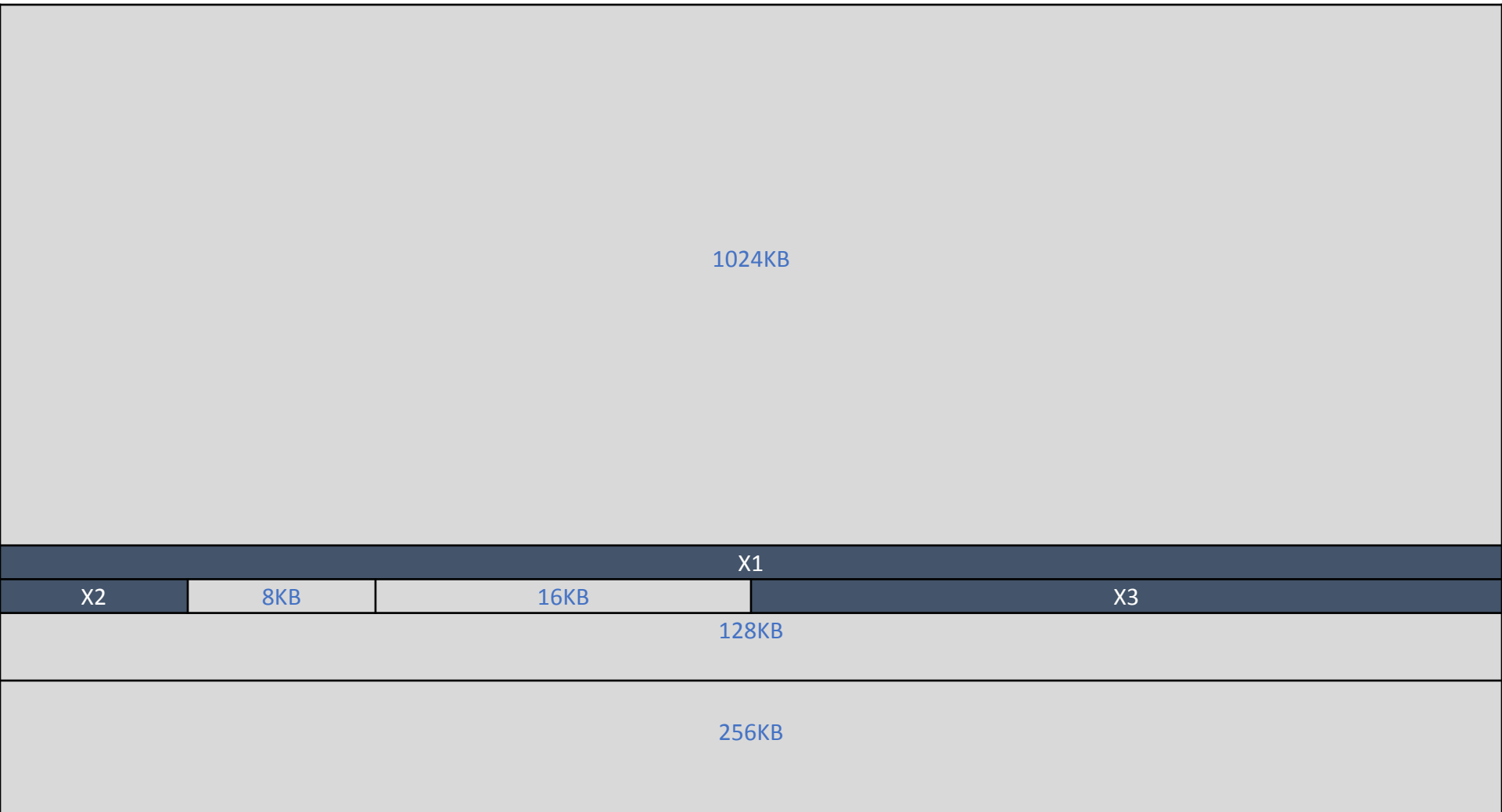
```
P3 = __get_free_pages(flags, 5); // get  $2^3 = 32\text{KB}$  of memory
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

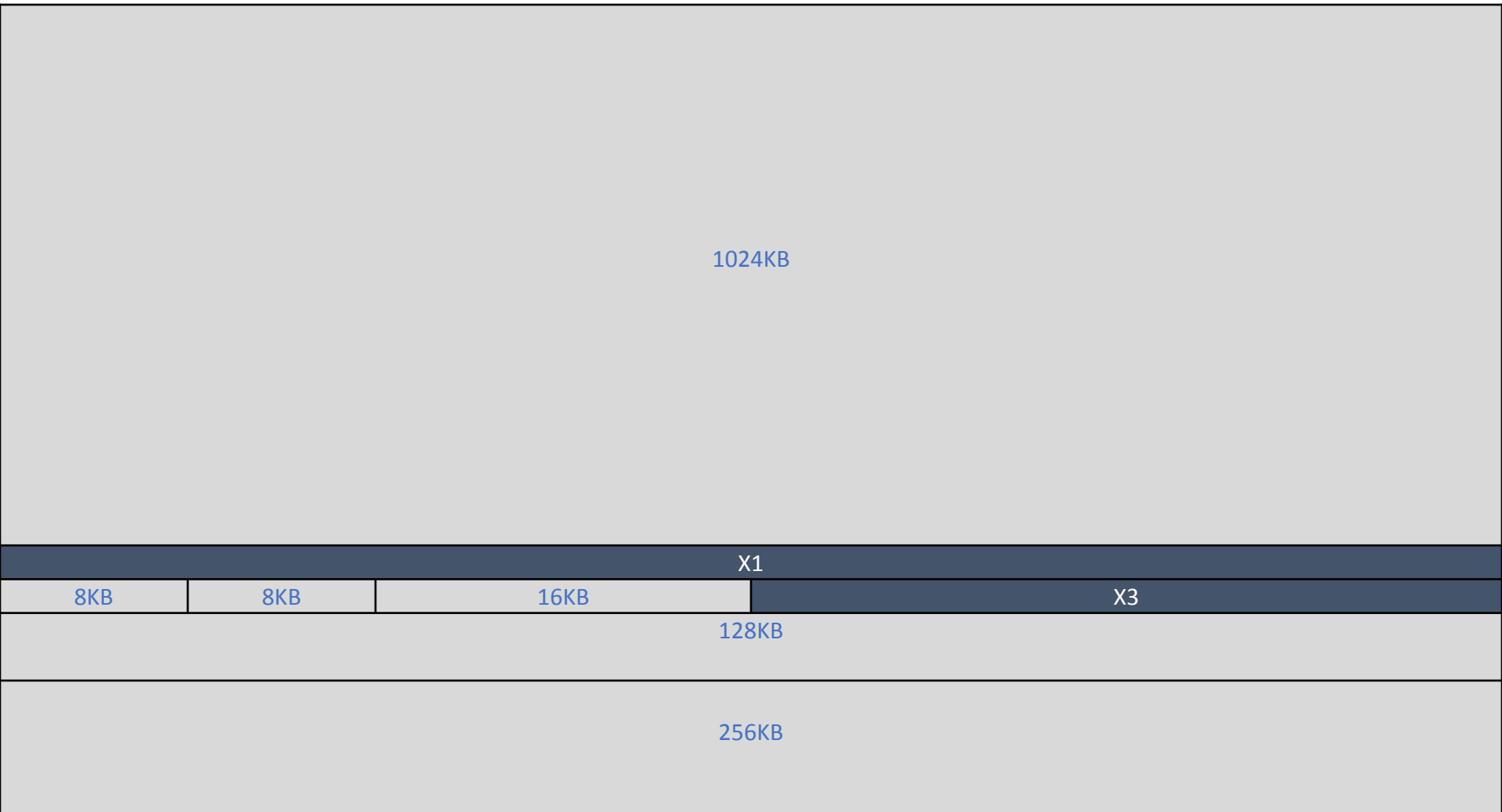
```
free_pages(x2, 3); // free x2
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

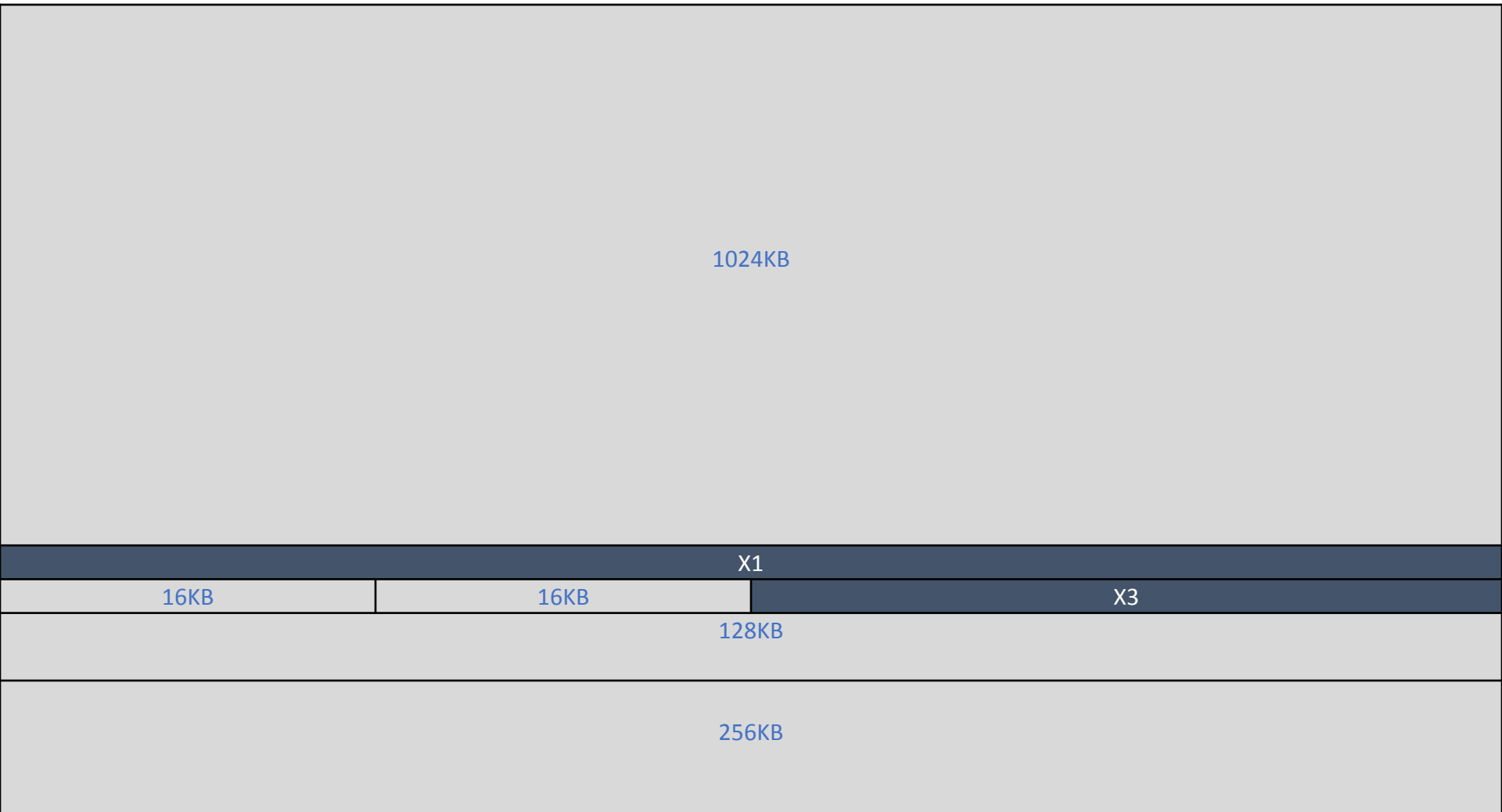
```
free_pages(x2, 3); // free x2
```



# Phys Feng Shui – Buddy Allocator

Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

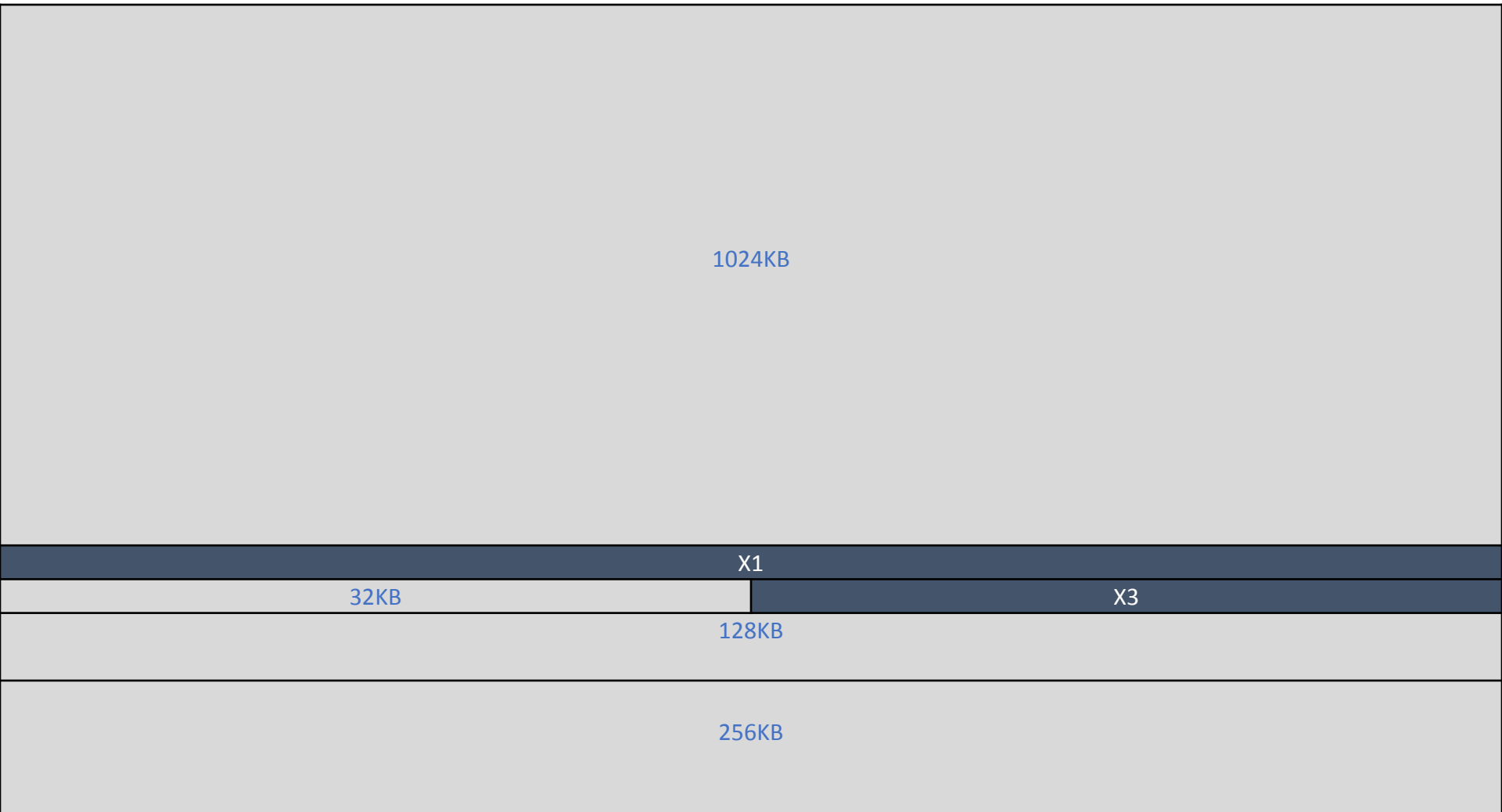
```
free_pages(x2, 3); // free x2
```



# Phys Feng Shui – Buddy Allocator

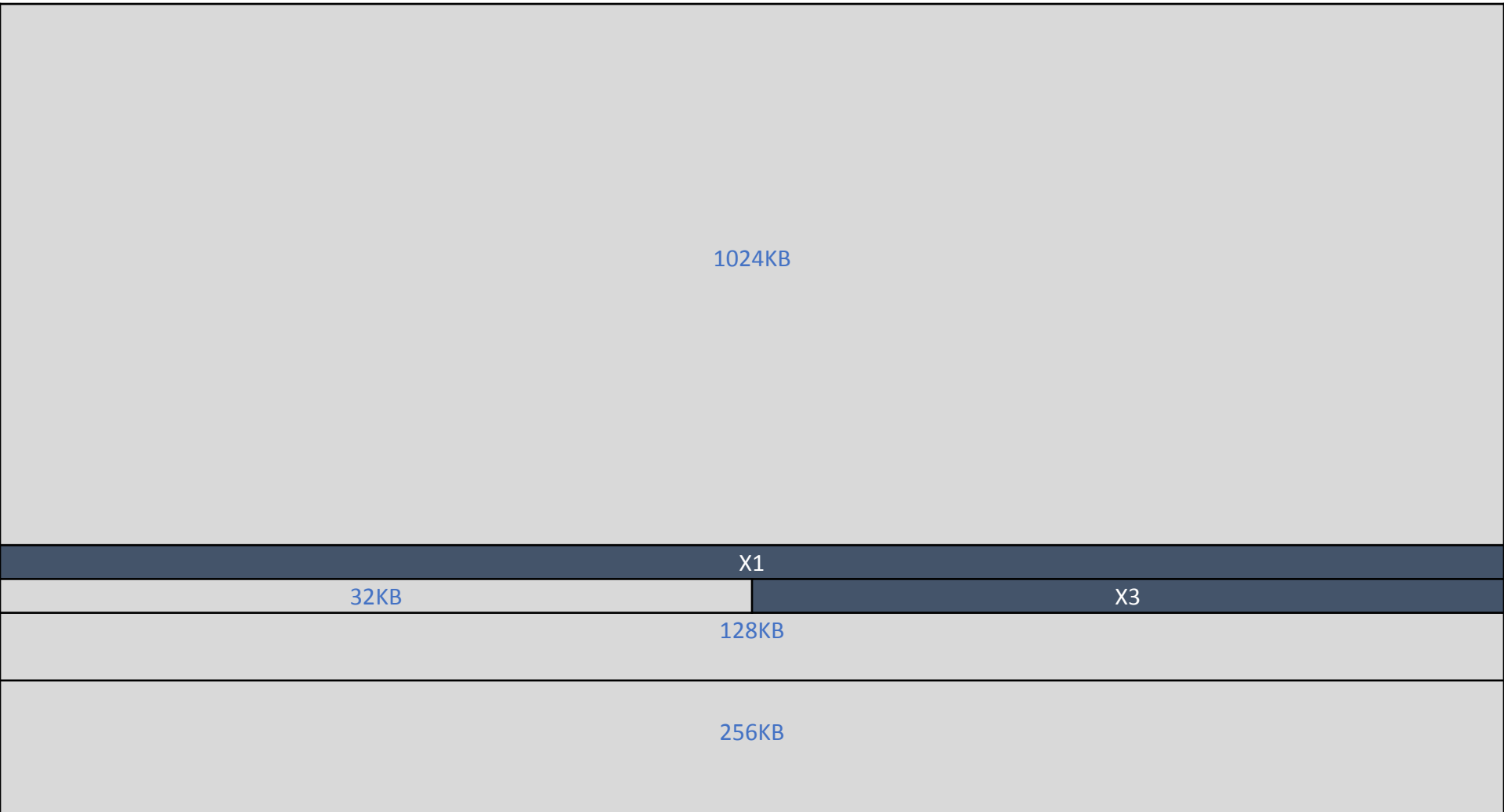
Avoid fragmentation by keeping track of same-size memory chunks (*buddies*)

```
free_pages(x2, 3); // free x2
```



# Phys Feng Shui

## Deterministic Rowhammer exploitation in 8 steps

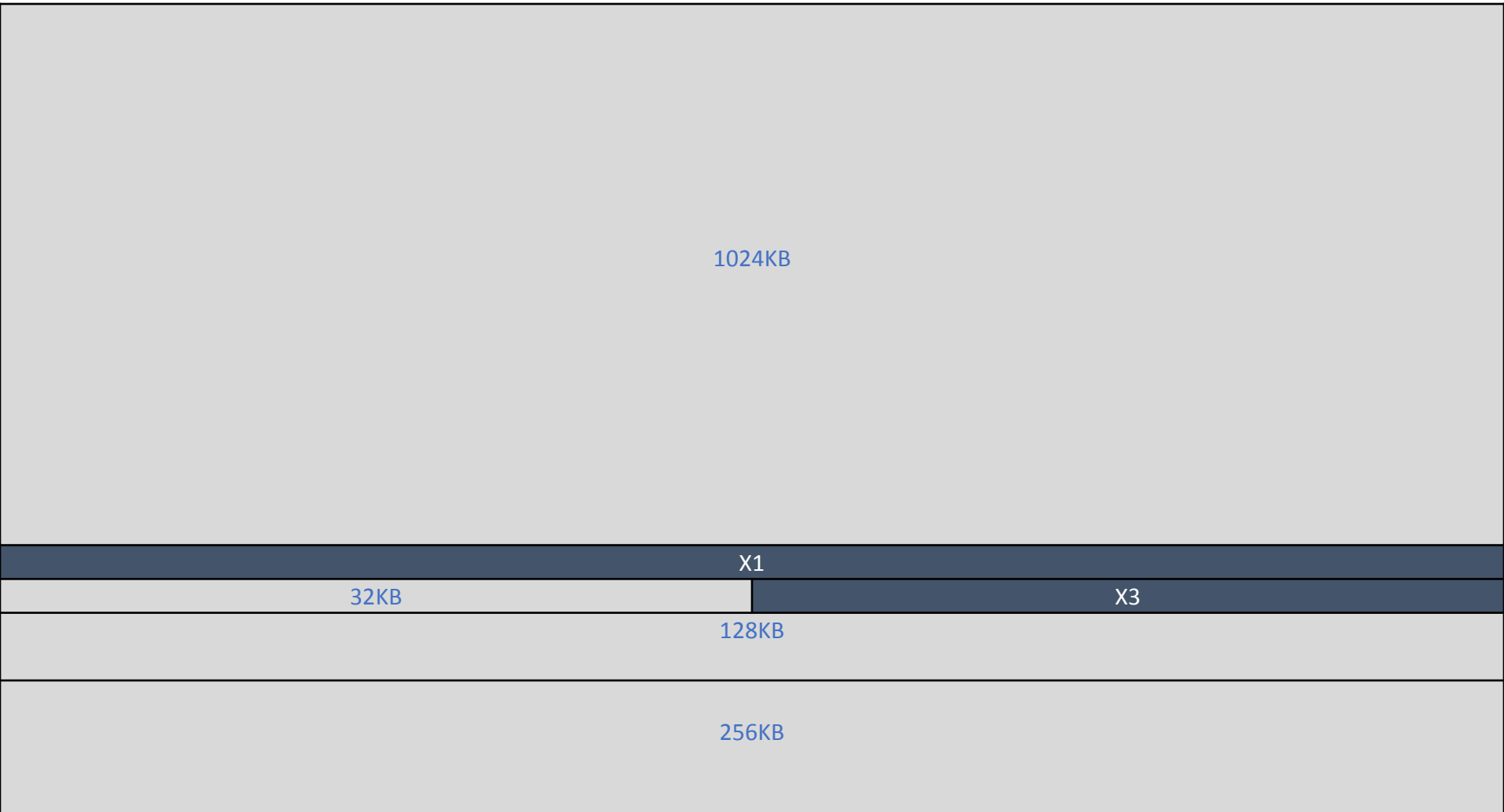




# Phys Feng Shui step 1/8

Exhaust + Template *Large* chunks

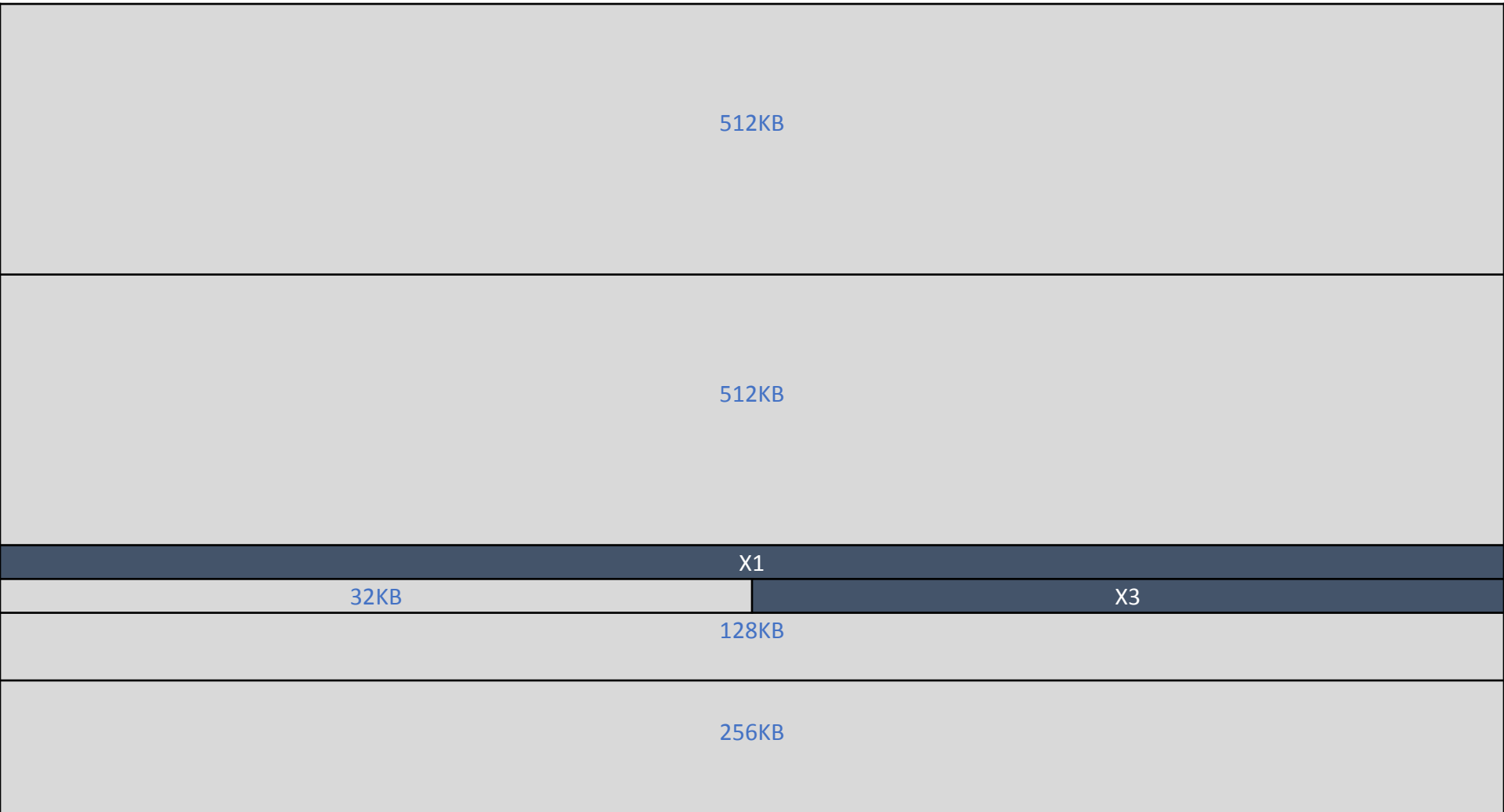
**L1, L2, ..., Ln = exhaust(L) ;**



# Phys Feng Shui step 1/8

Exhaust + Template *Large* chunks

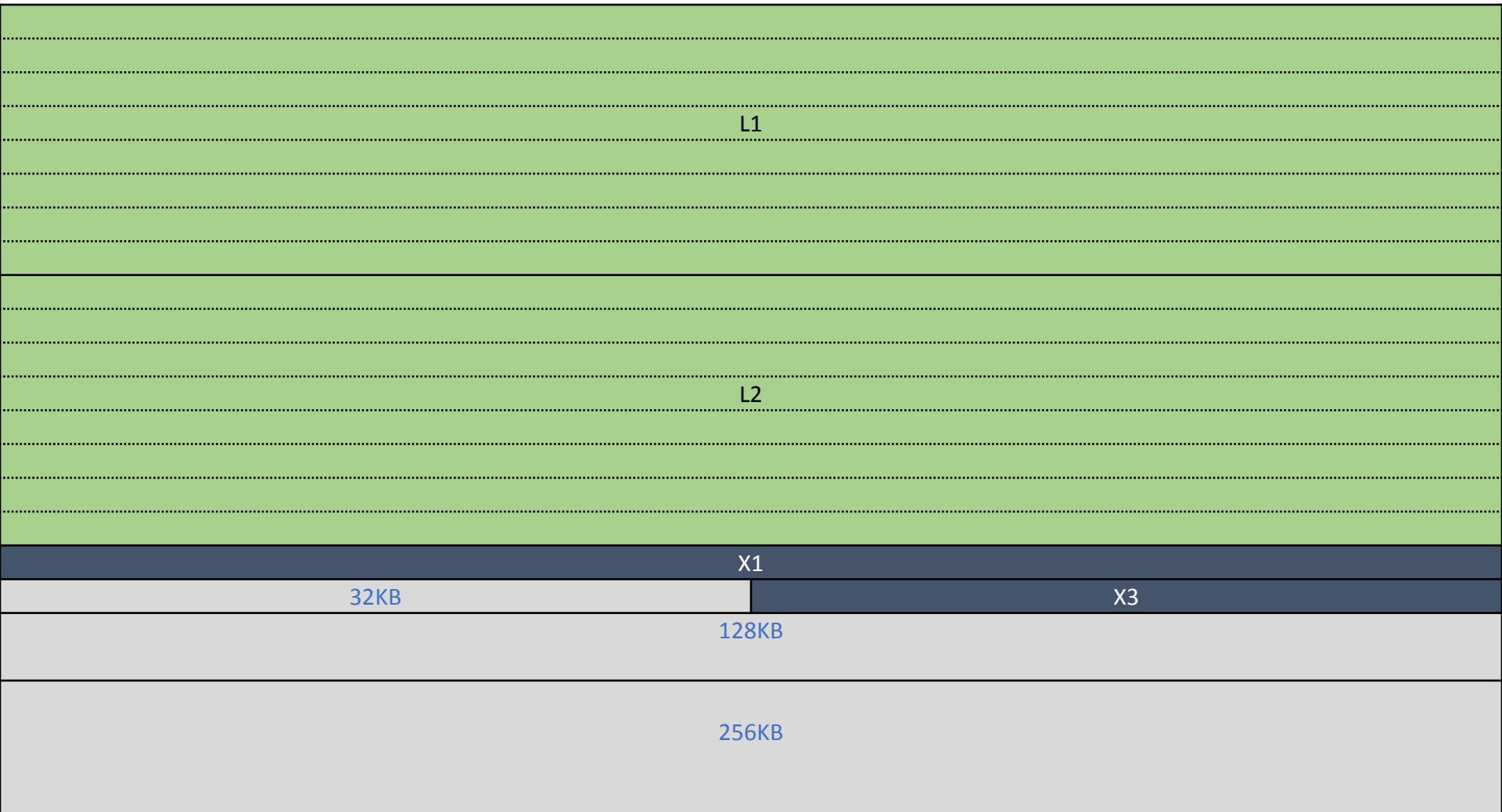
```
L1, L2, ..., Ln = exhaust(9); // get all 2^9 = 512KB chunks
```



# Phys Feng Shui step 1/8

Exhaust + Template *Large* chunks

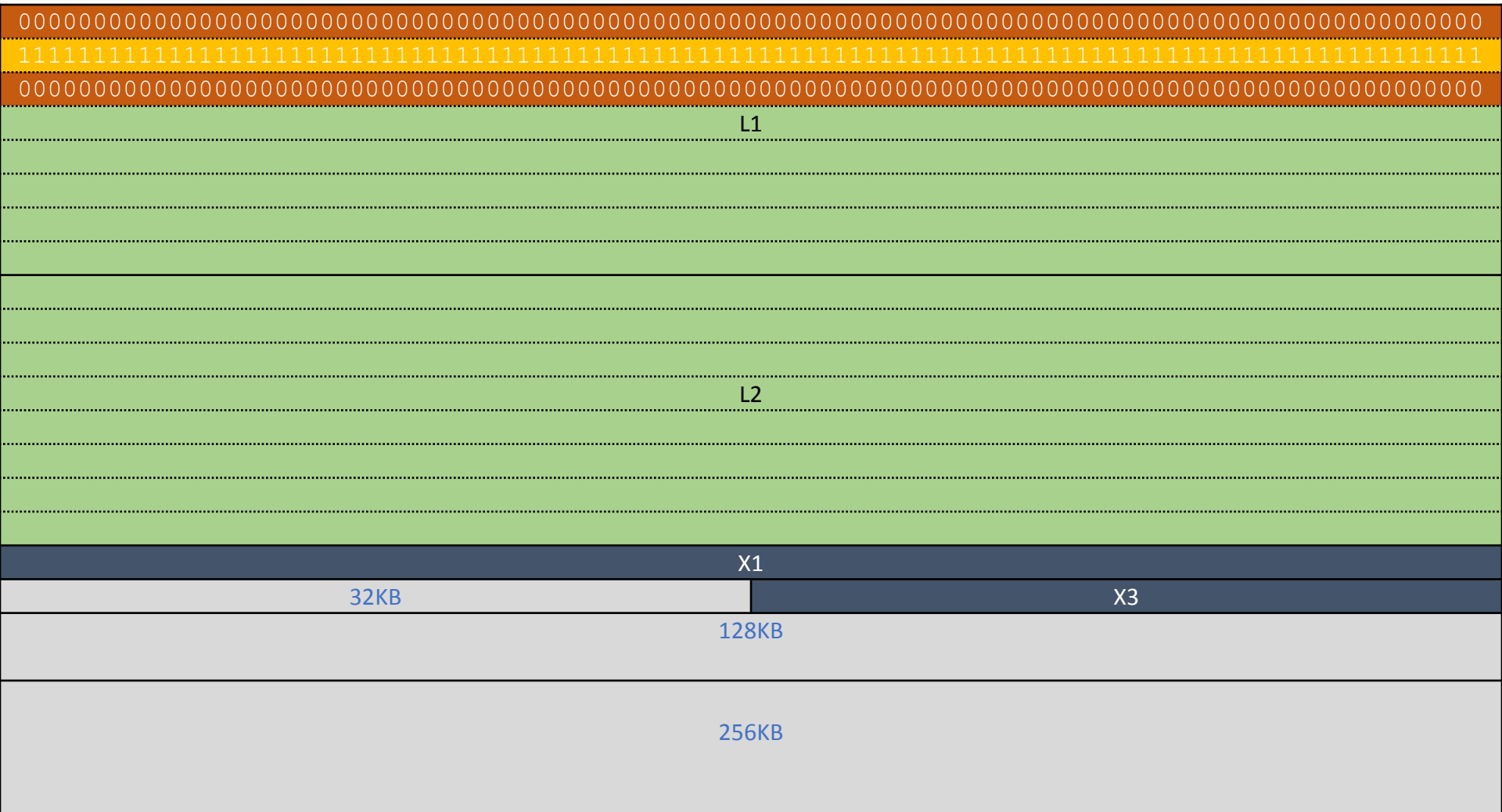
```
L1, L2, ..., Ln = exhaust(L); // get all 2^9 = 512KB chunks
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

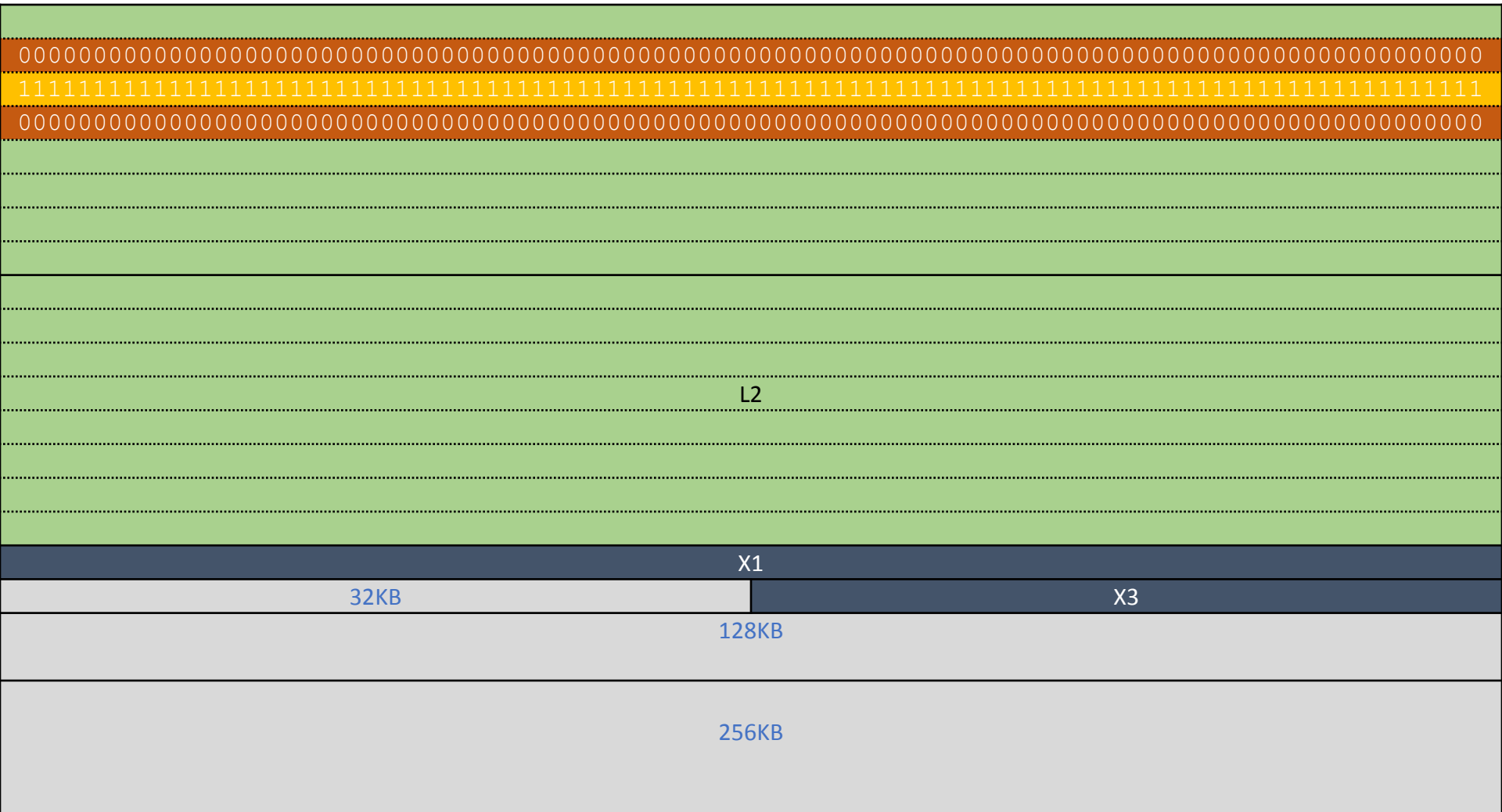
```
Hammer(L1, 2); // hammer row 2 of chunk L1
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

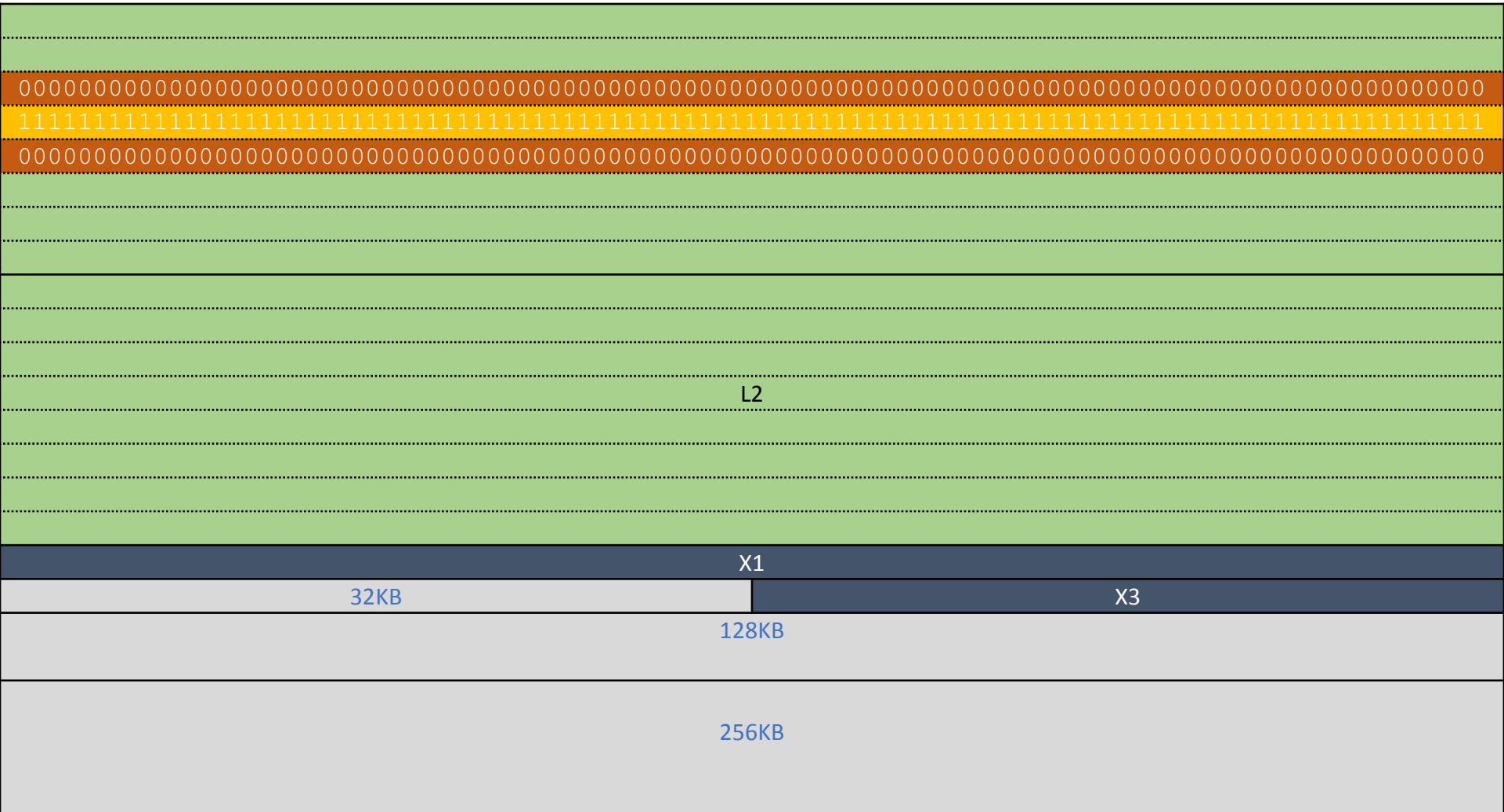
```
Hammer(L1, 3); // hammer row 3 of chunk L1
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

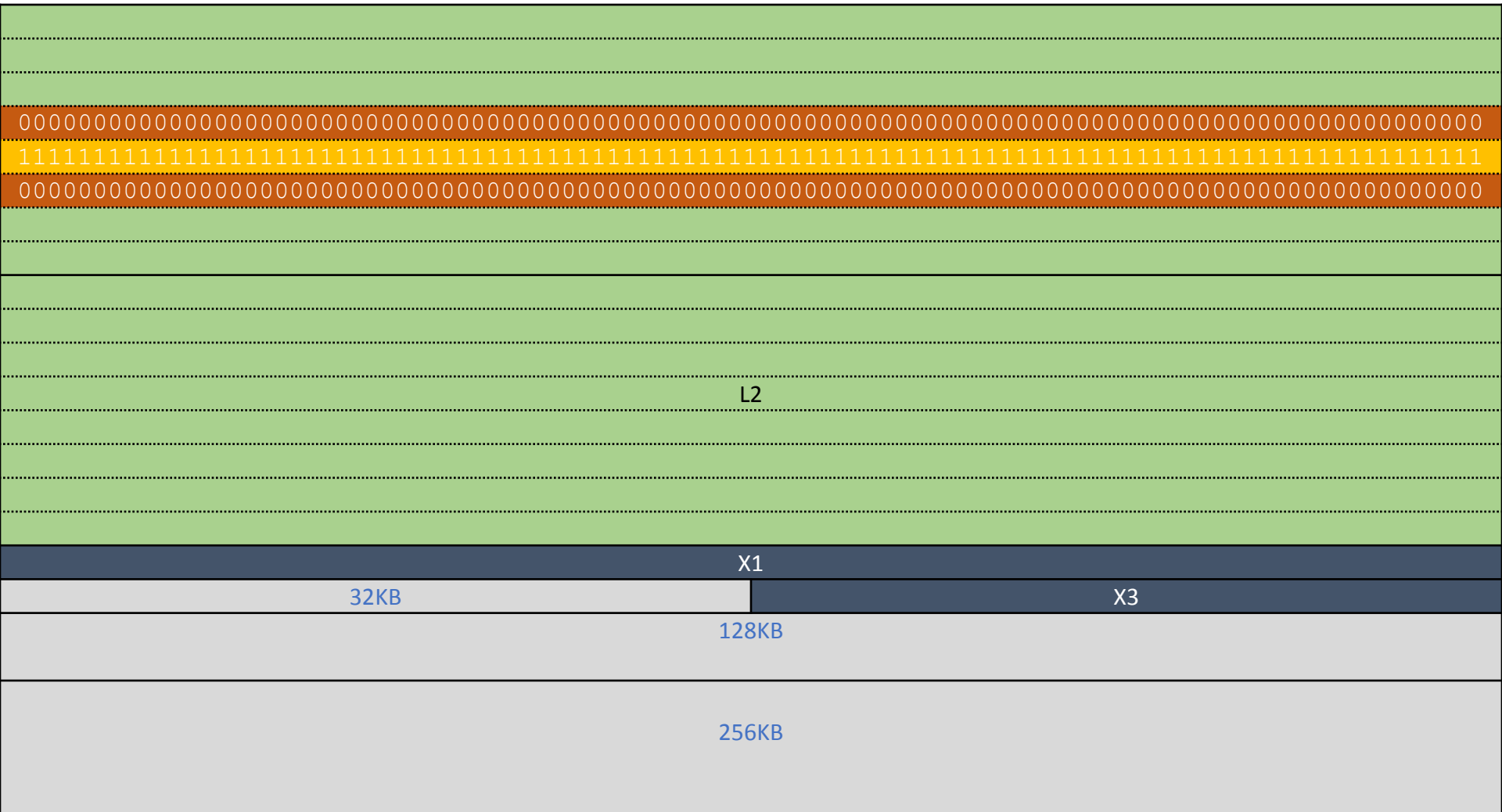
```
Hammer(L1, 4); // hammer row 4 of chunk L1
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

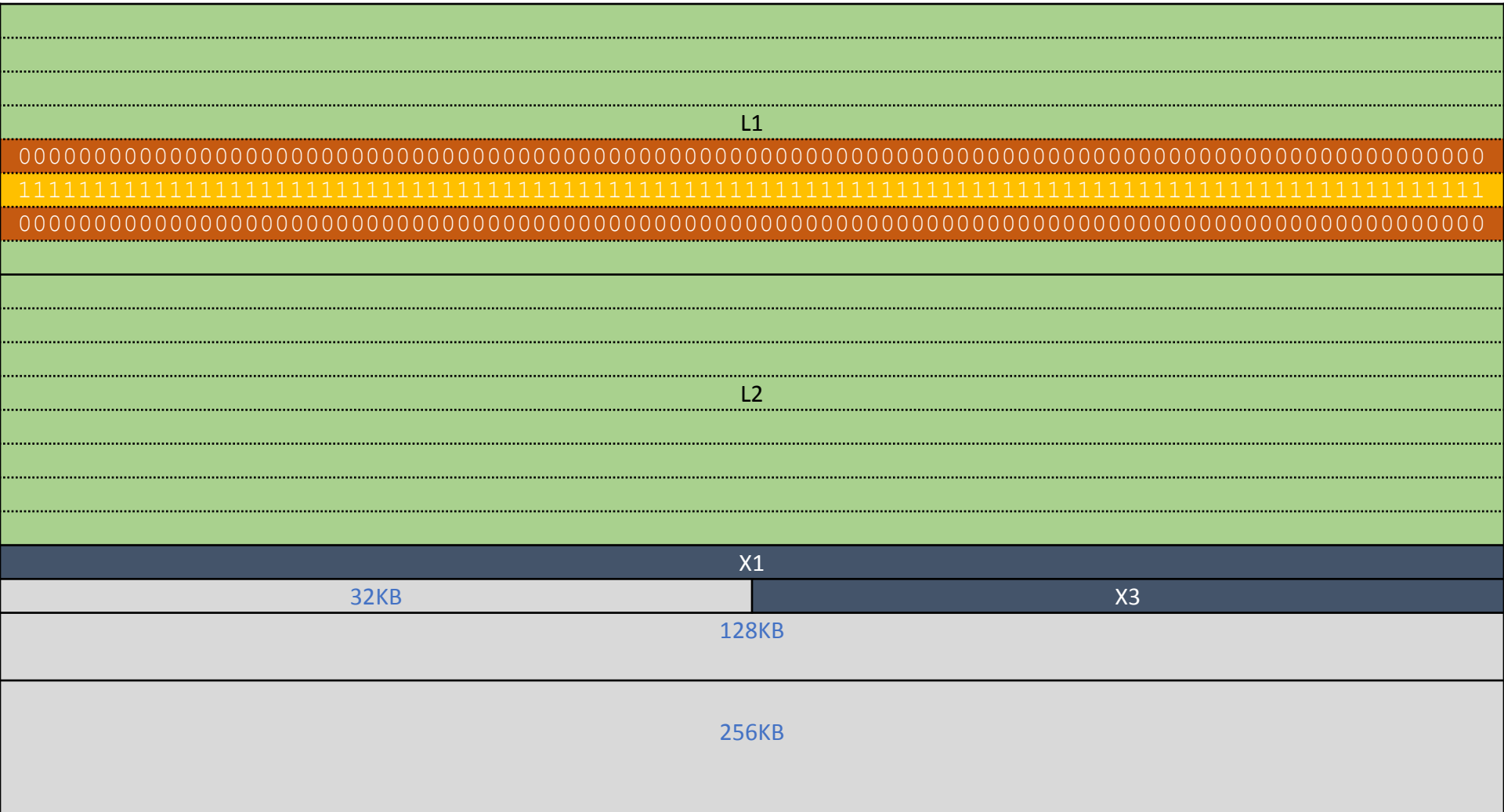
```
Hammer(L1, 5); // hammer row 5 of chunk L1
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

```
Hammer(L1, 6); // hammer row 6 of chunk L1
```

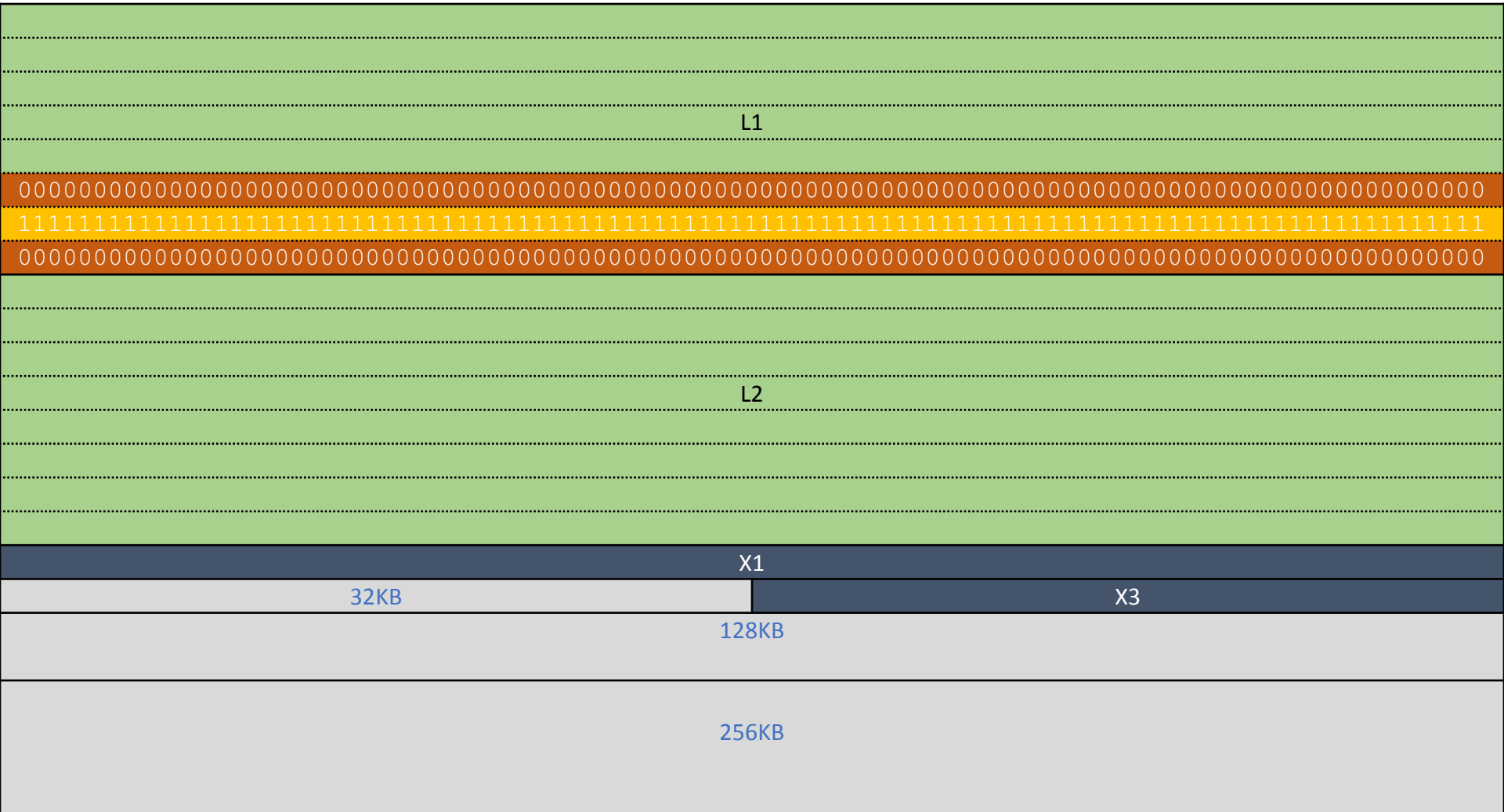




# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

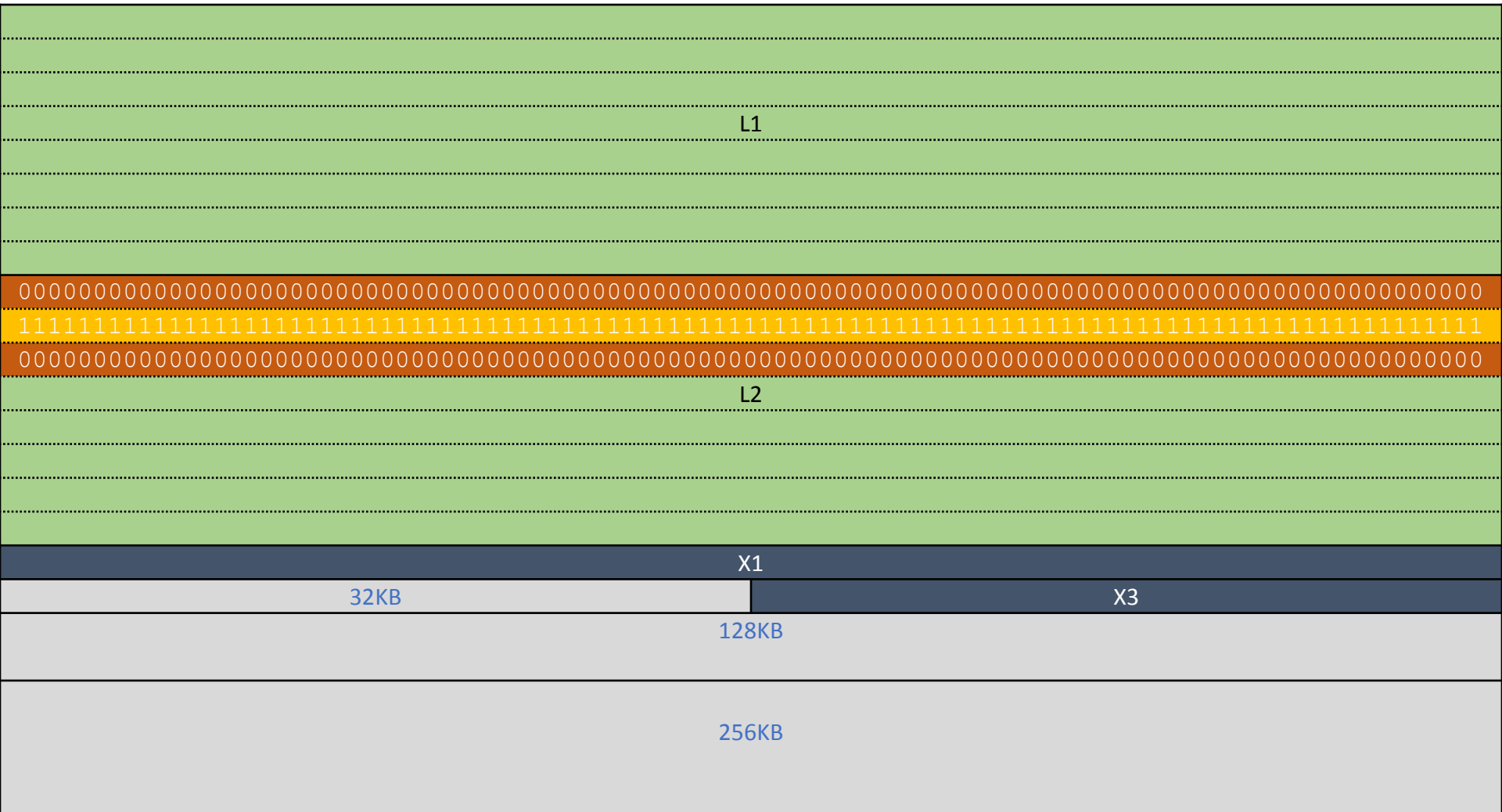
```
Hammer(L1, 7); // hammer row 7 of chunk L1
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

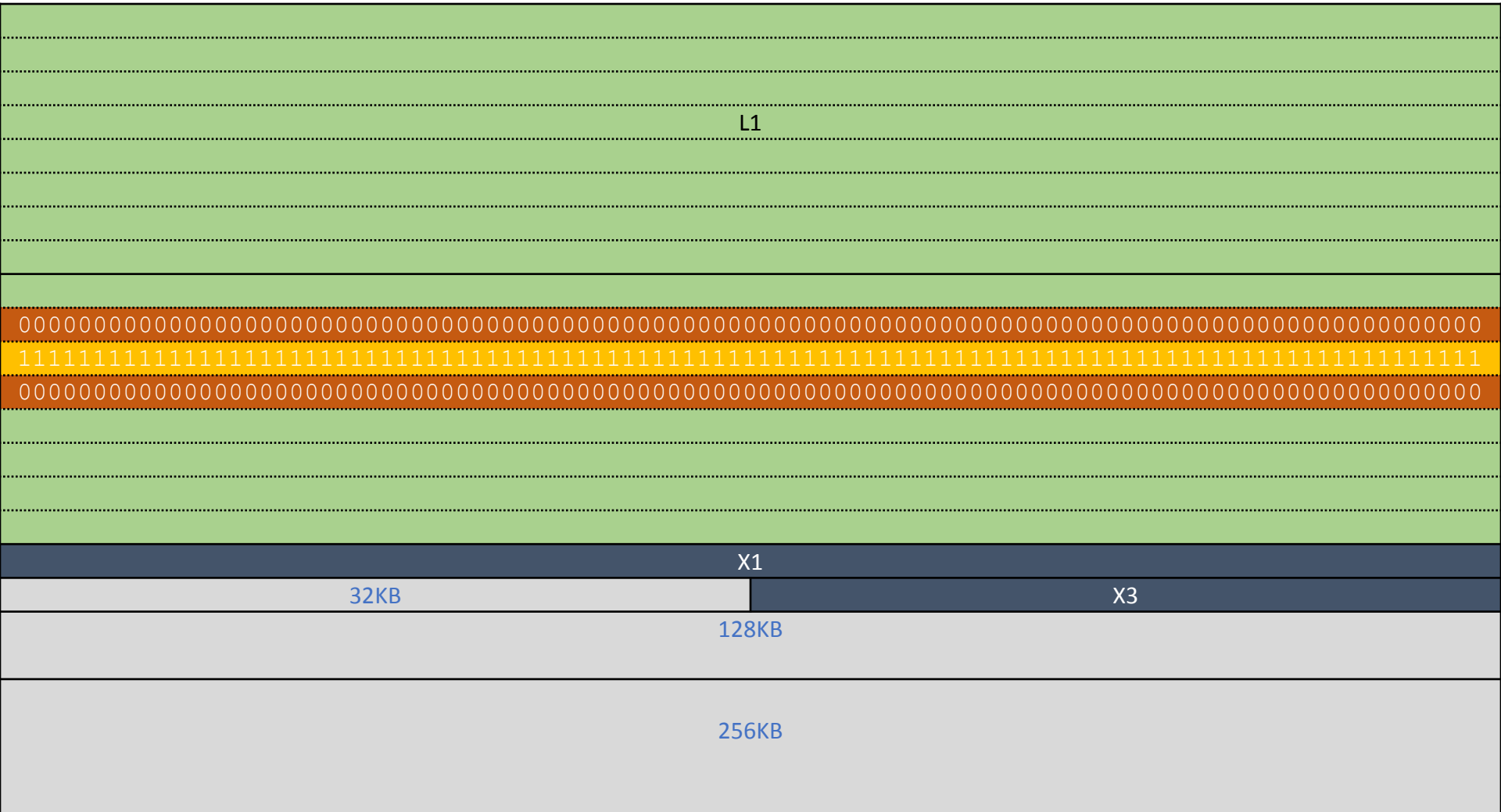
```
Hammer (L2, 2); // hammer row 2 of chunk L2
```



# Phys Feng Shui step 1/8

Exhaust + **Template** *Large* chunks

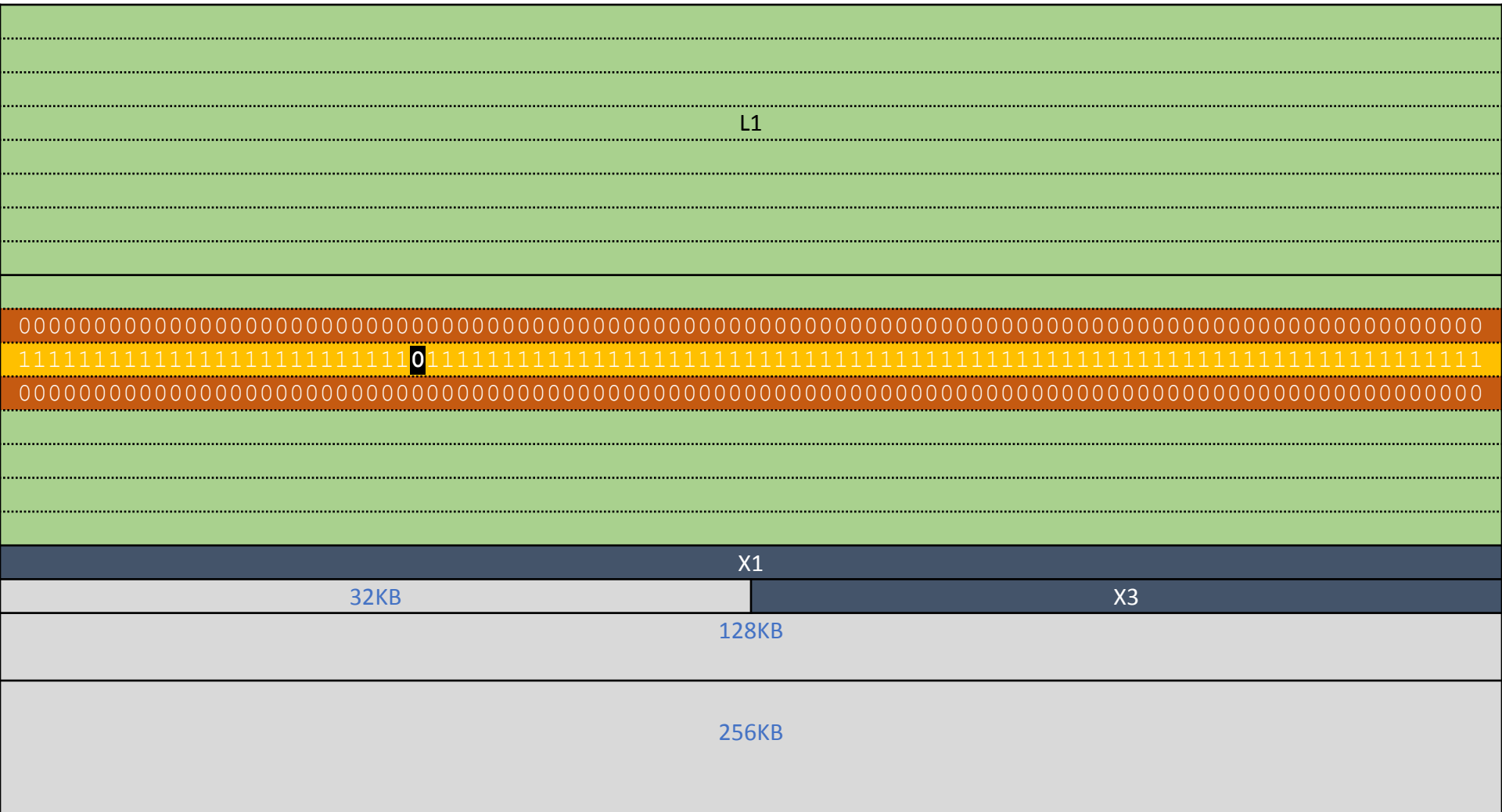
```
Hammer (L2, 3); // hammer row 3 of chunk L2
```



# Phys Feng Shui step 1/8

## Exhaust + **Template** *Large* chunks

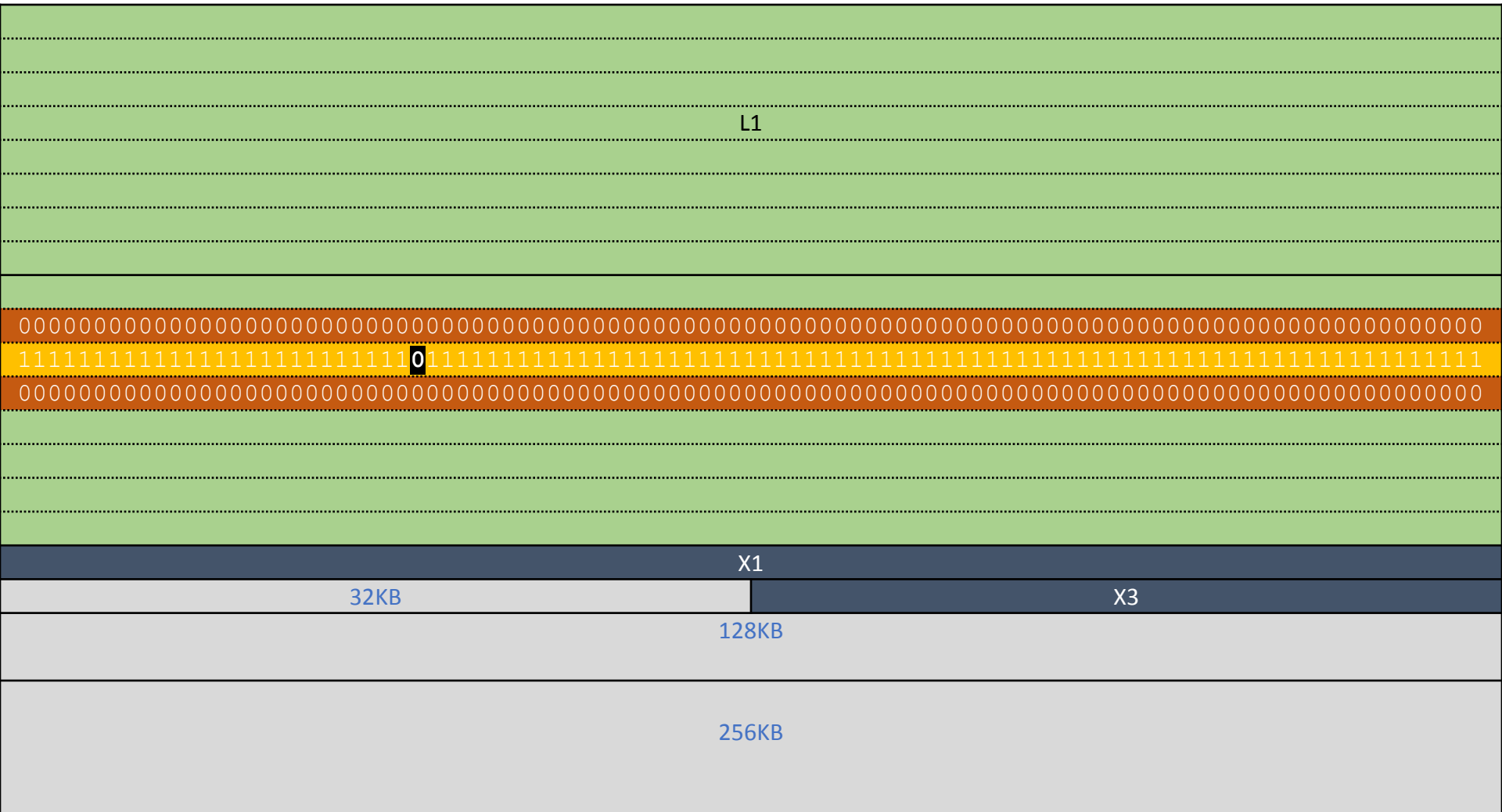
“exploitable flip found in page 5 of virtual row 3 of L2!”



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

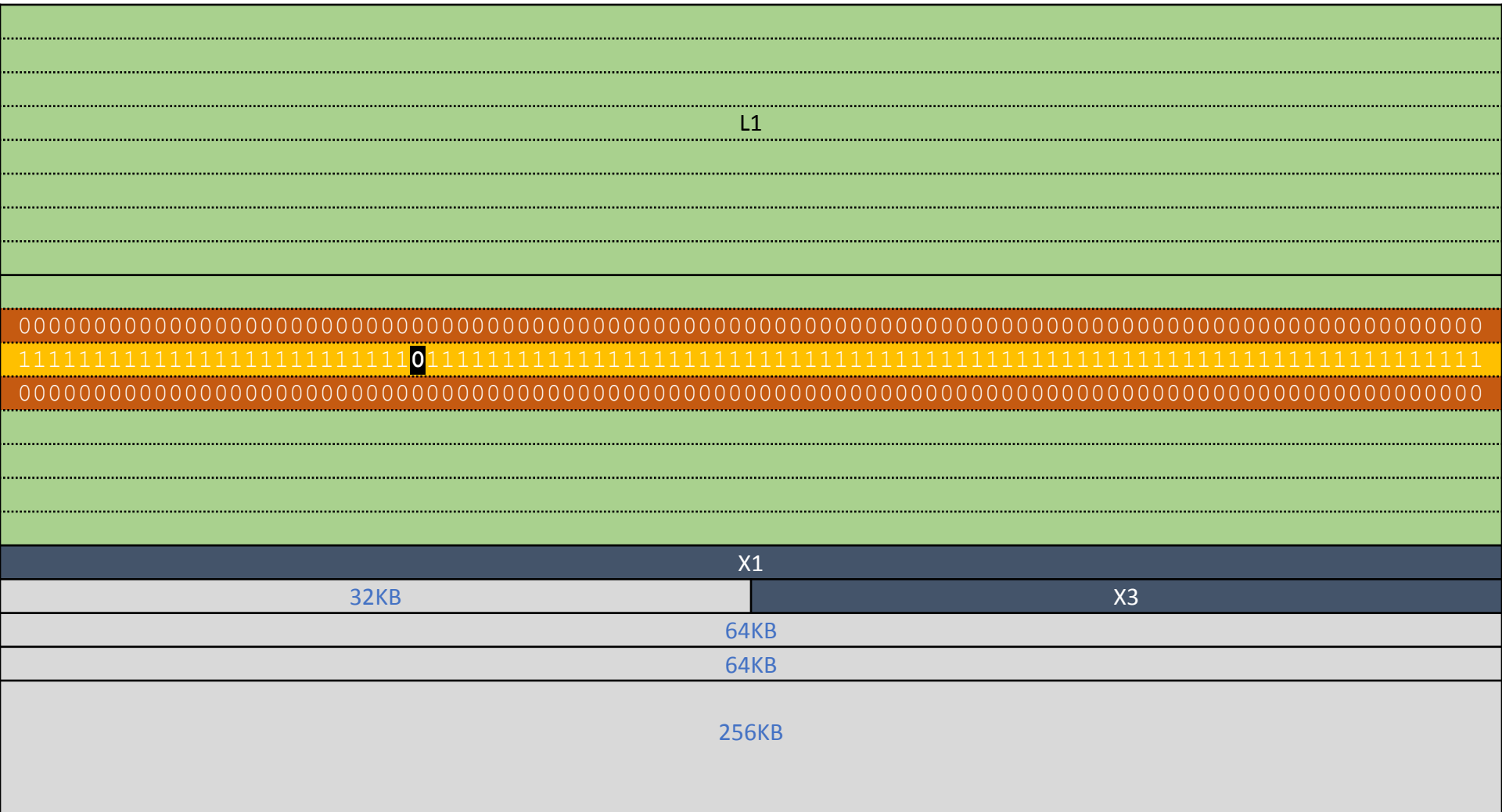
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

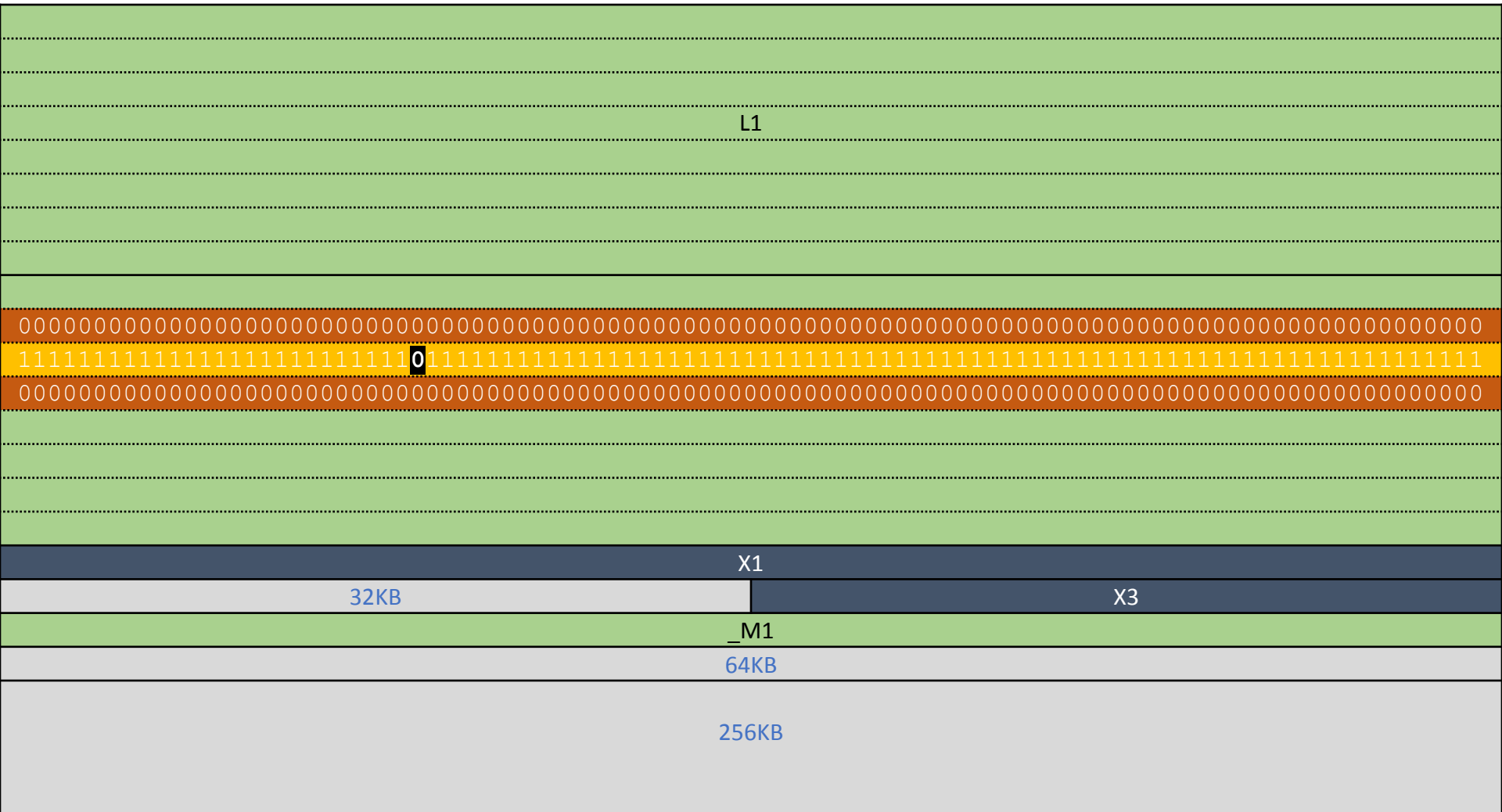
```
_M1, _M2, ..., _Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

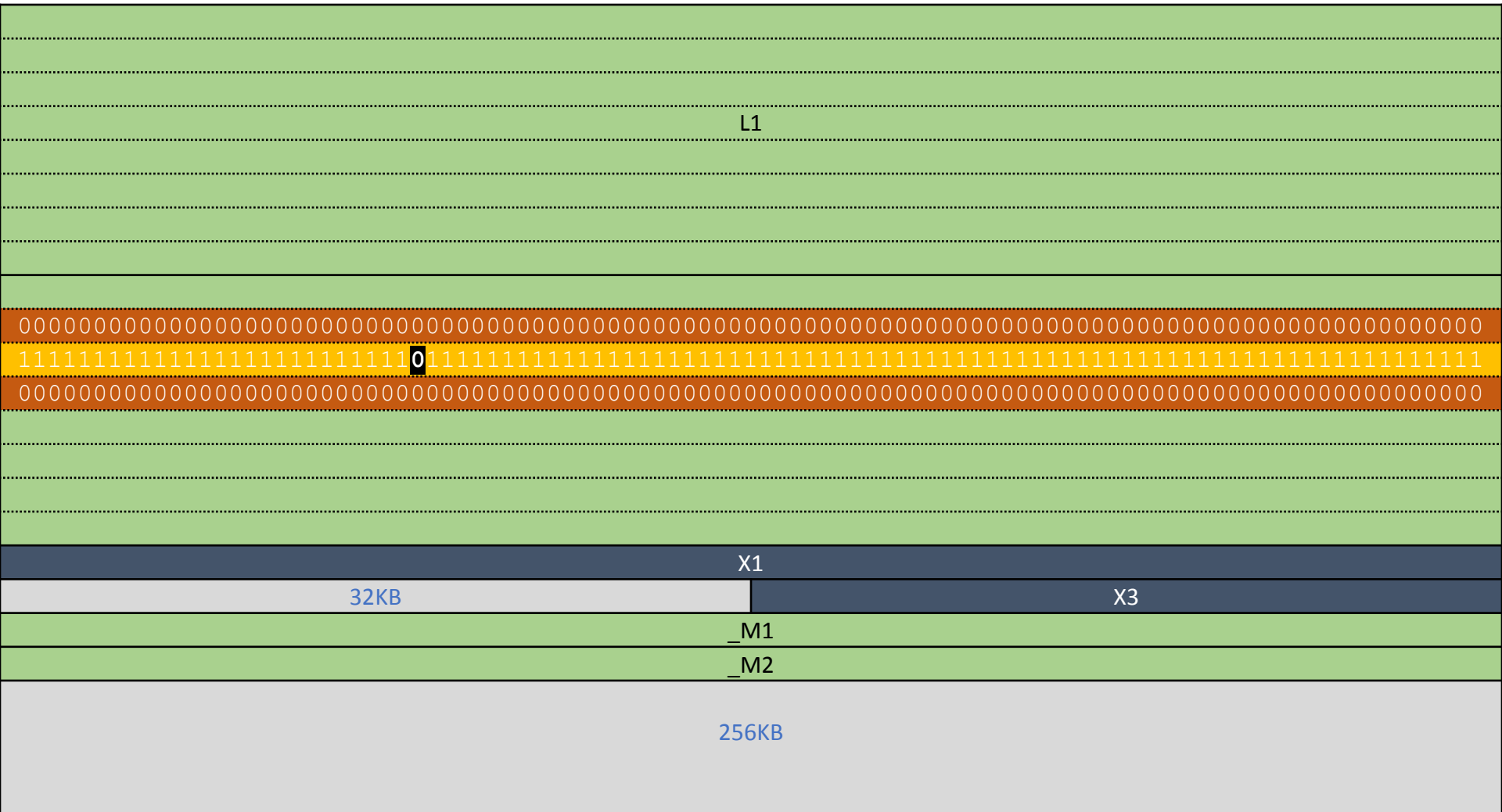
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



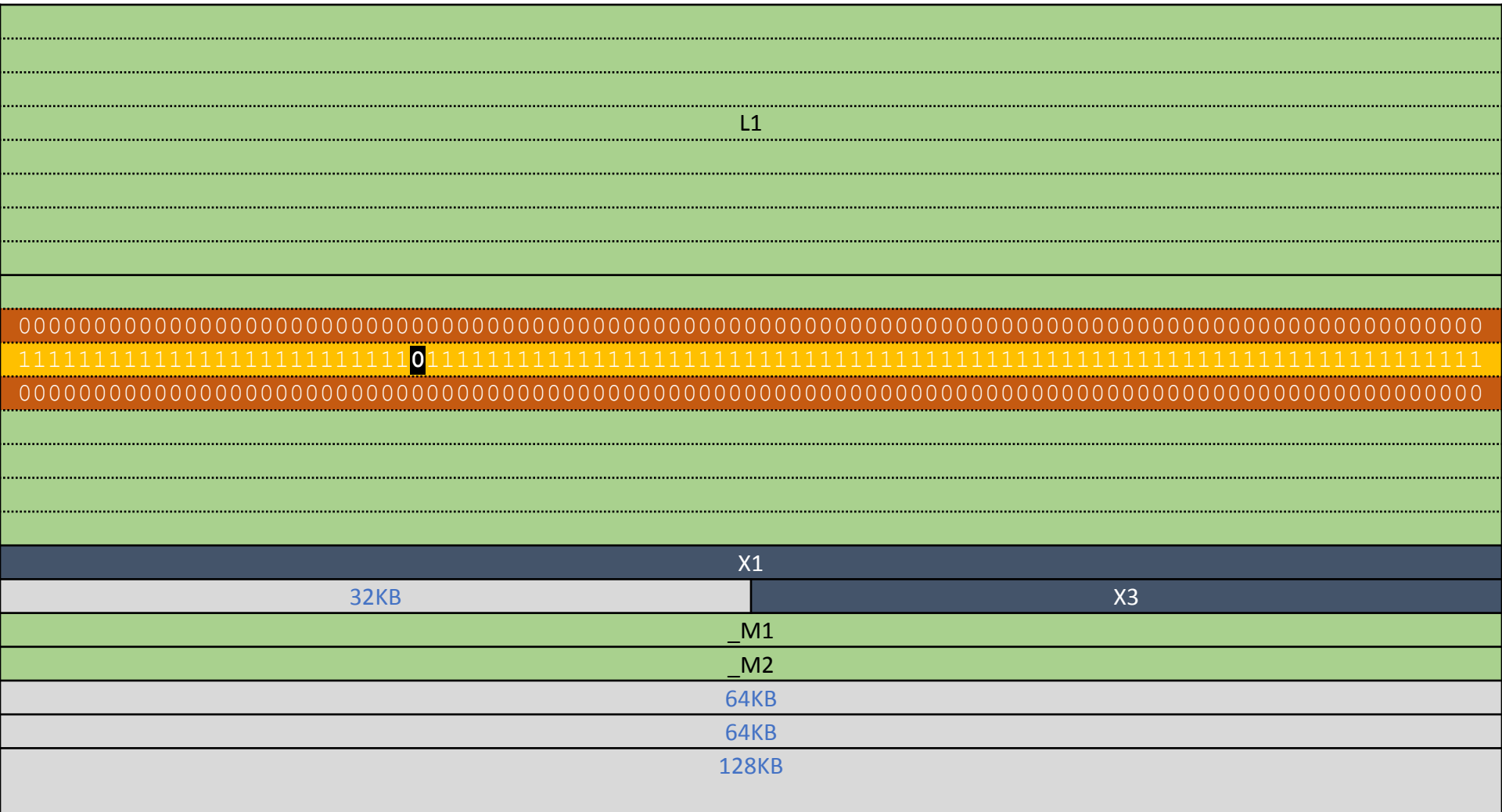




# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

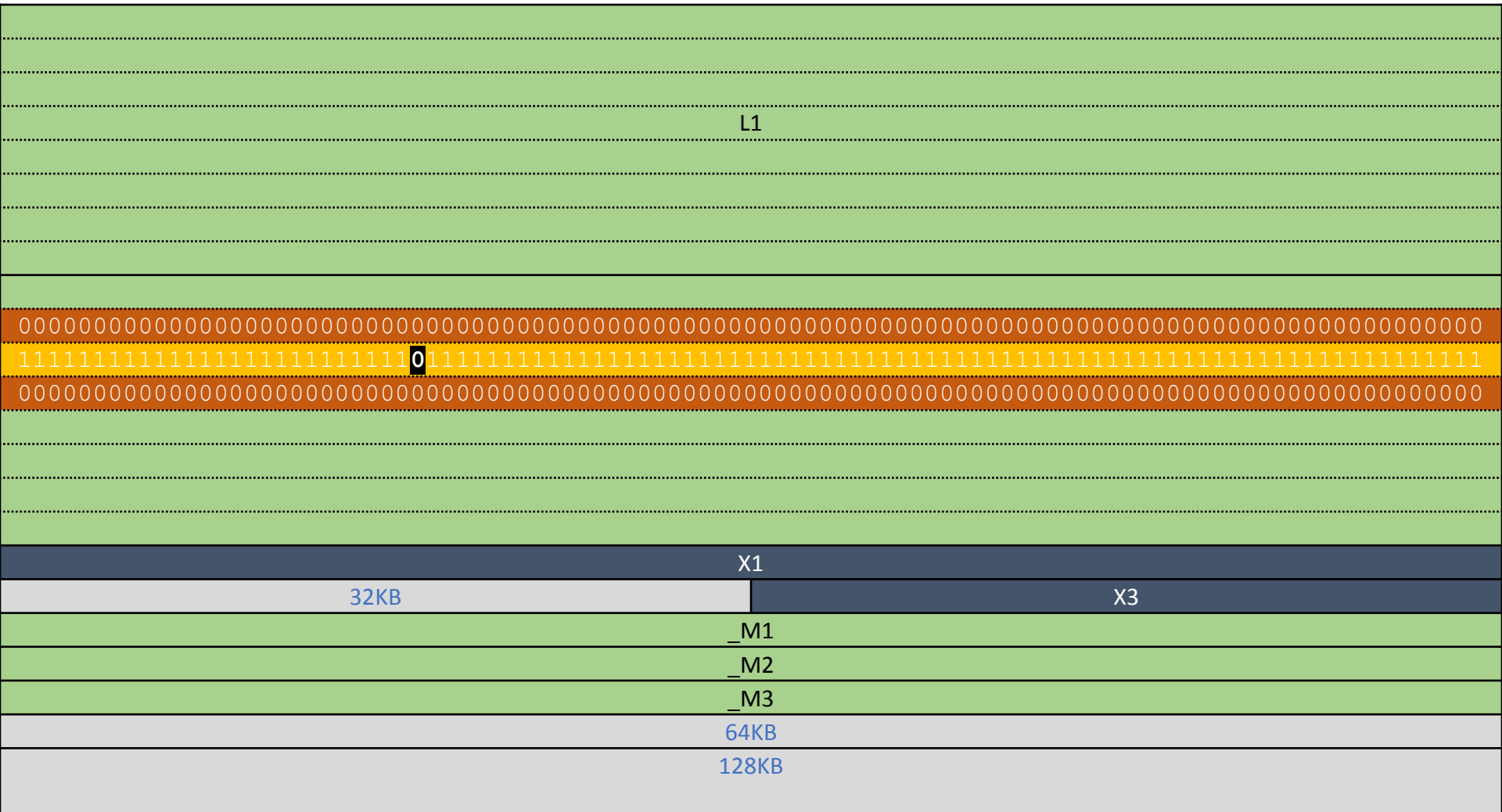
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

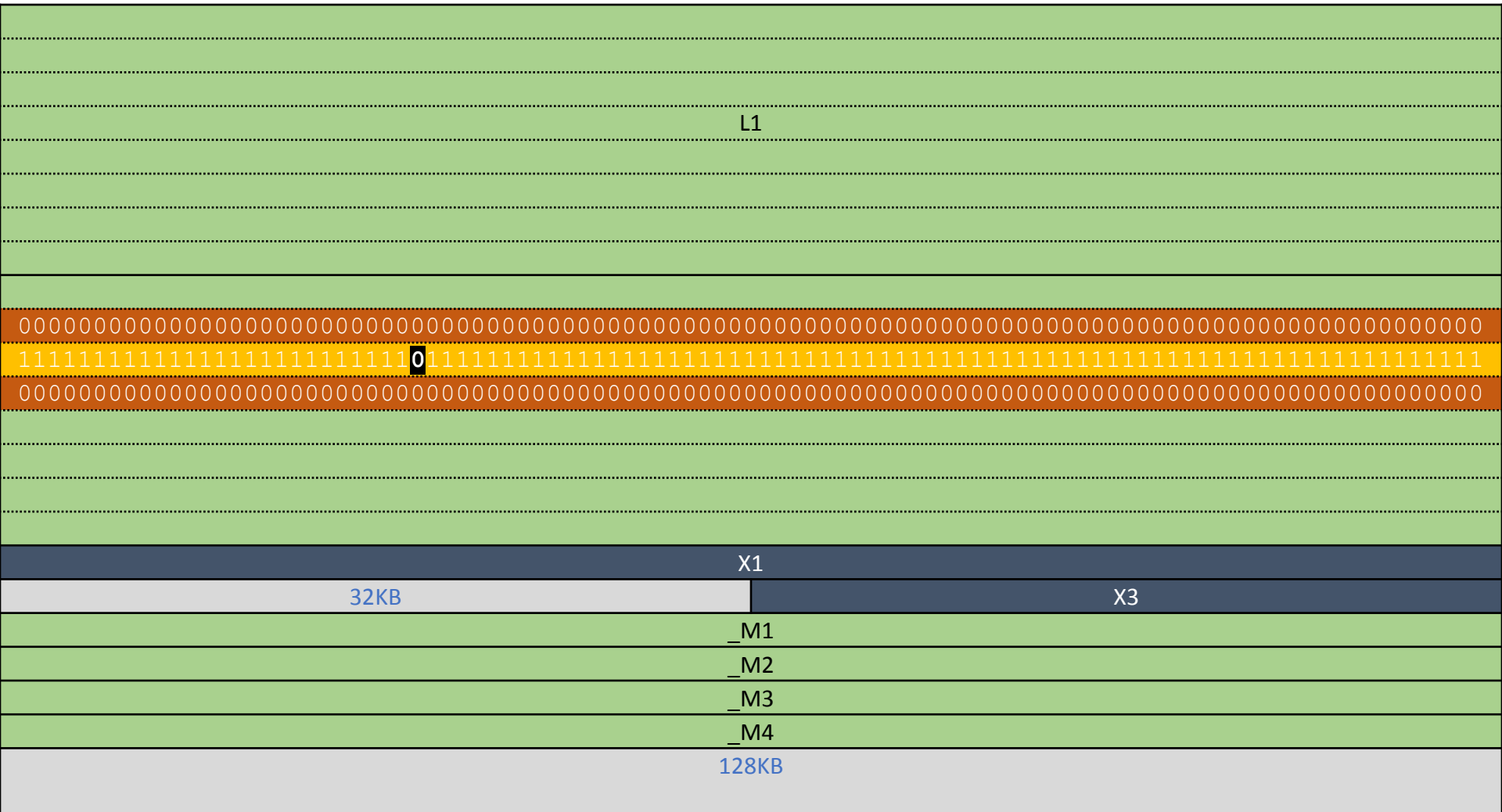
```
_M1, _M2, ..., _Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

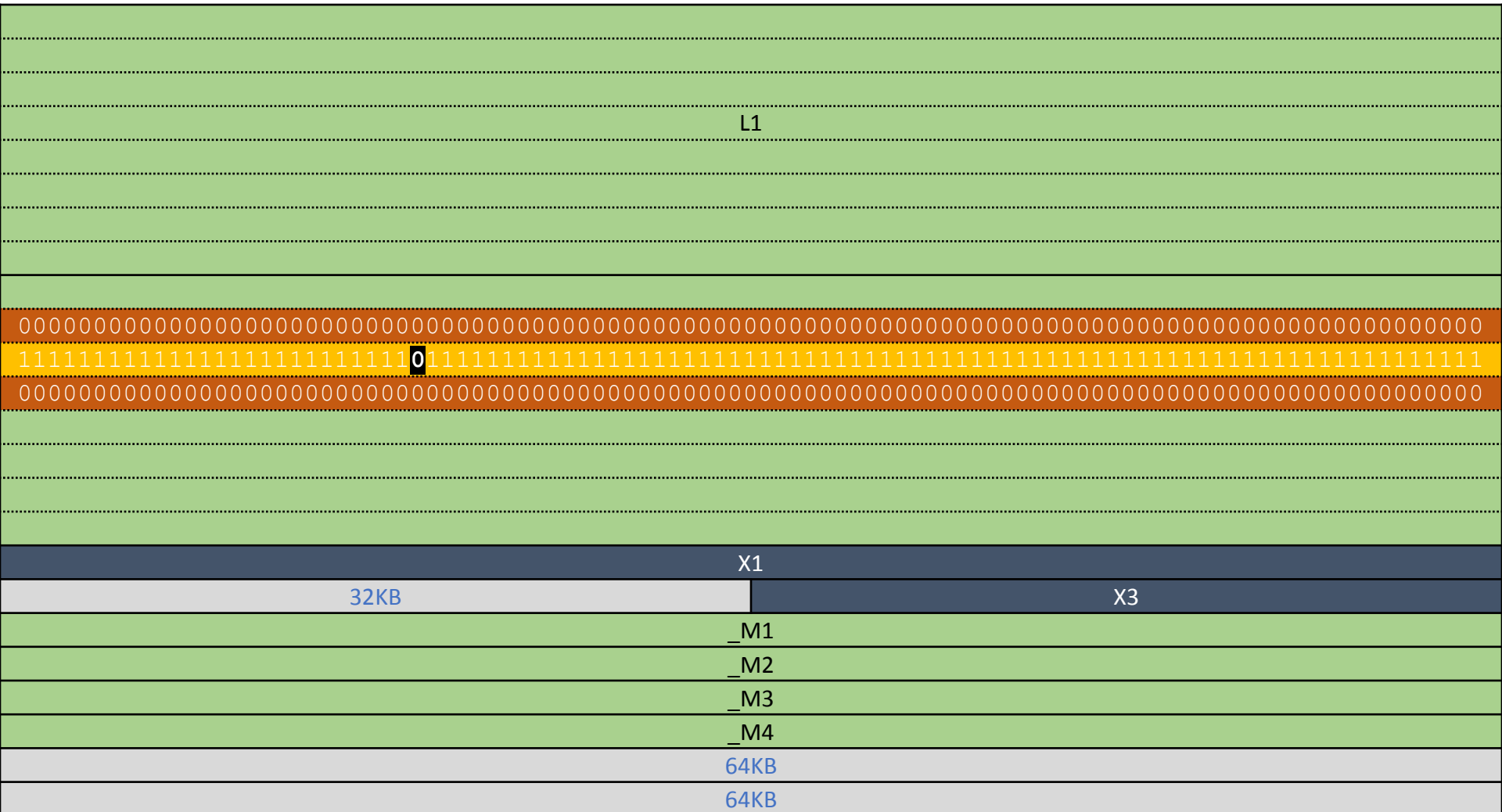
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

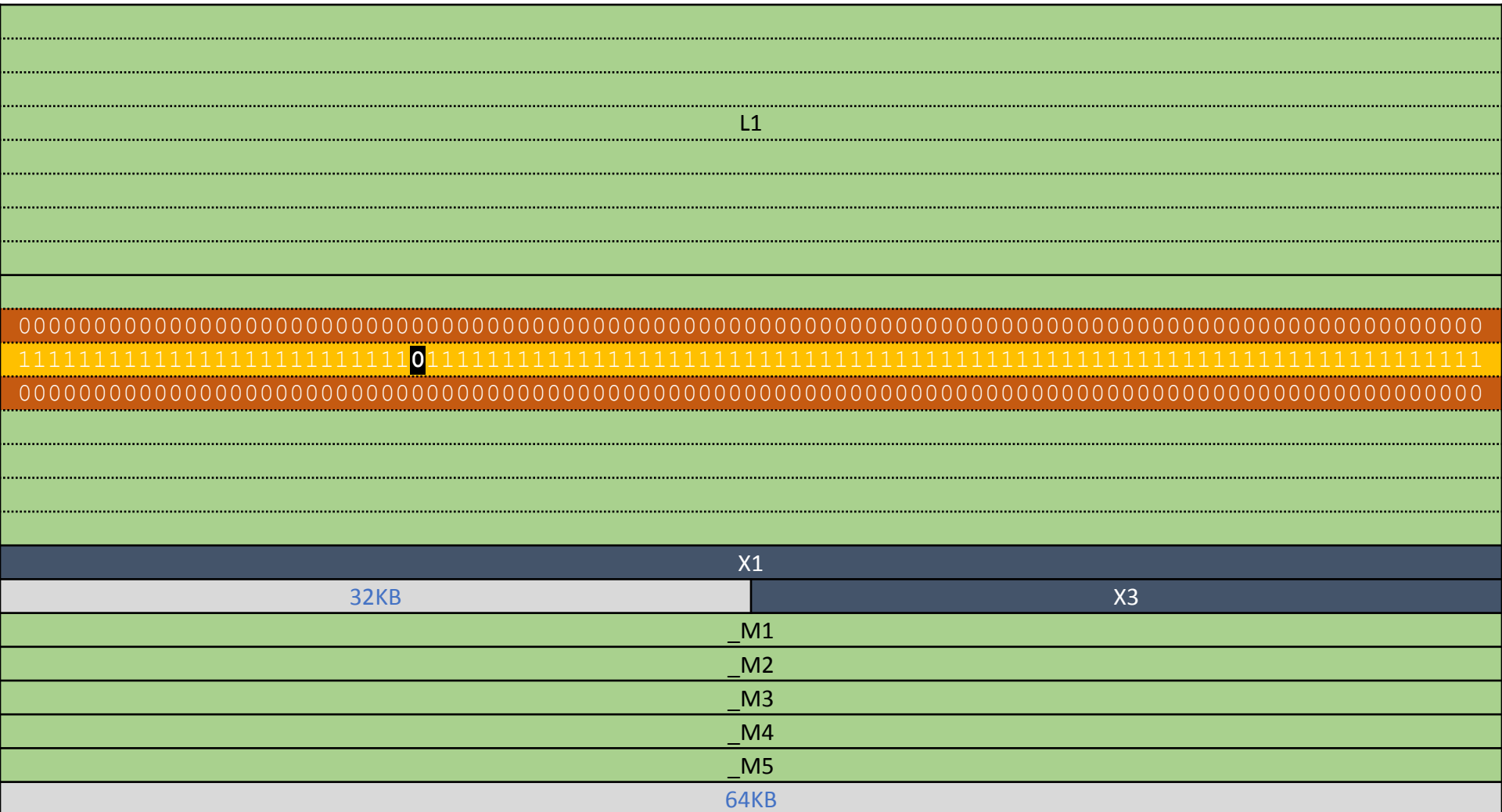
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

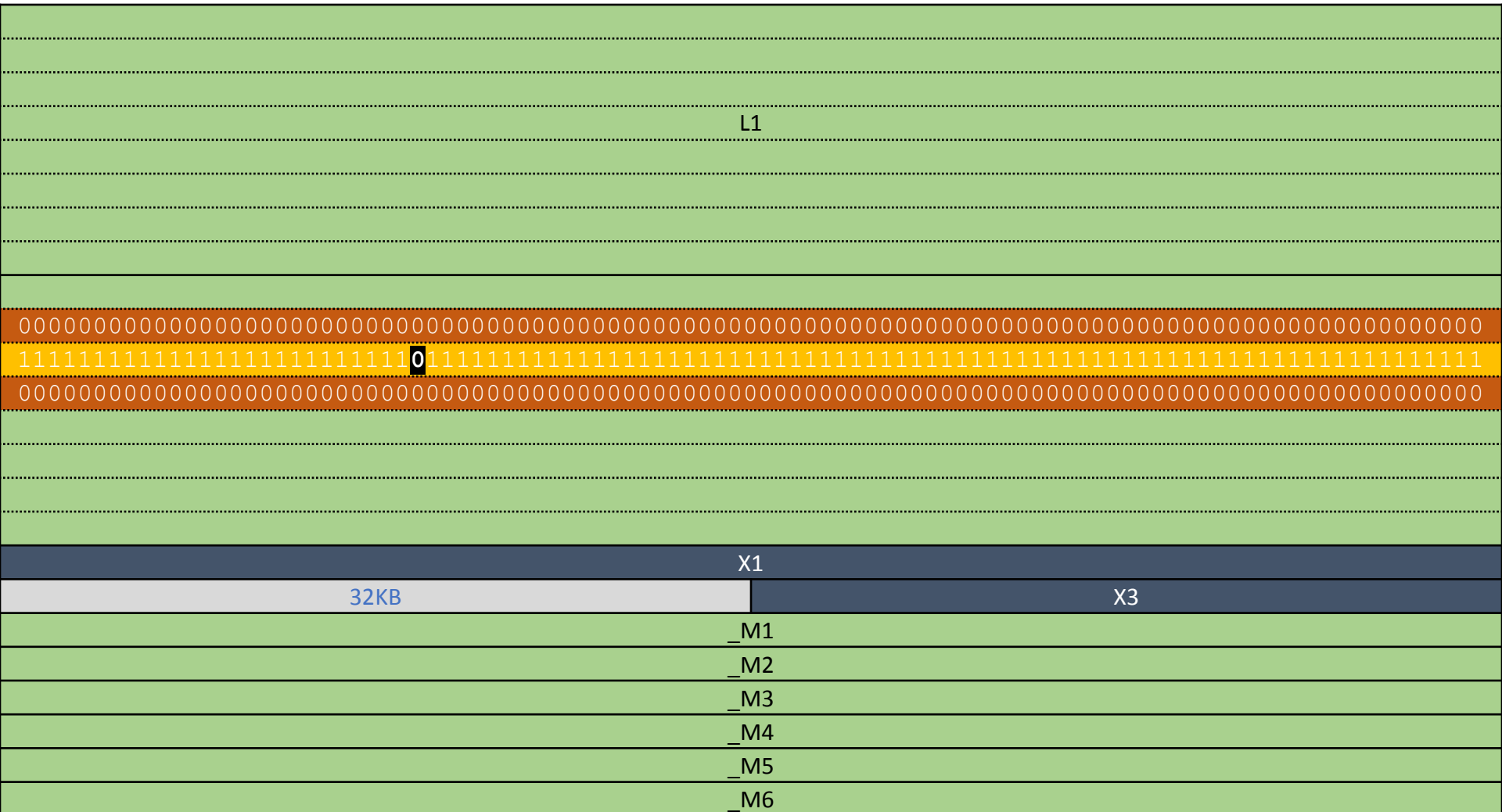
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 2/8

Exhaust *Medium-sized* chunks

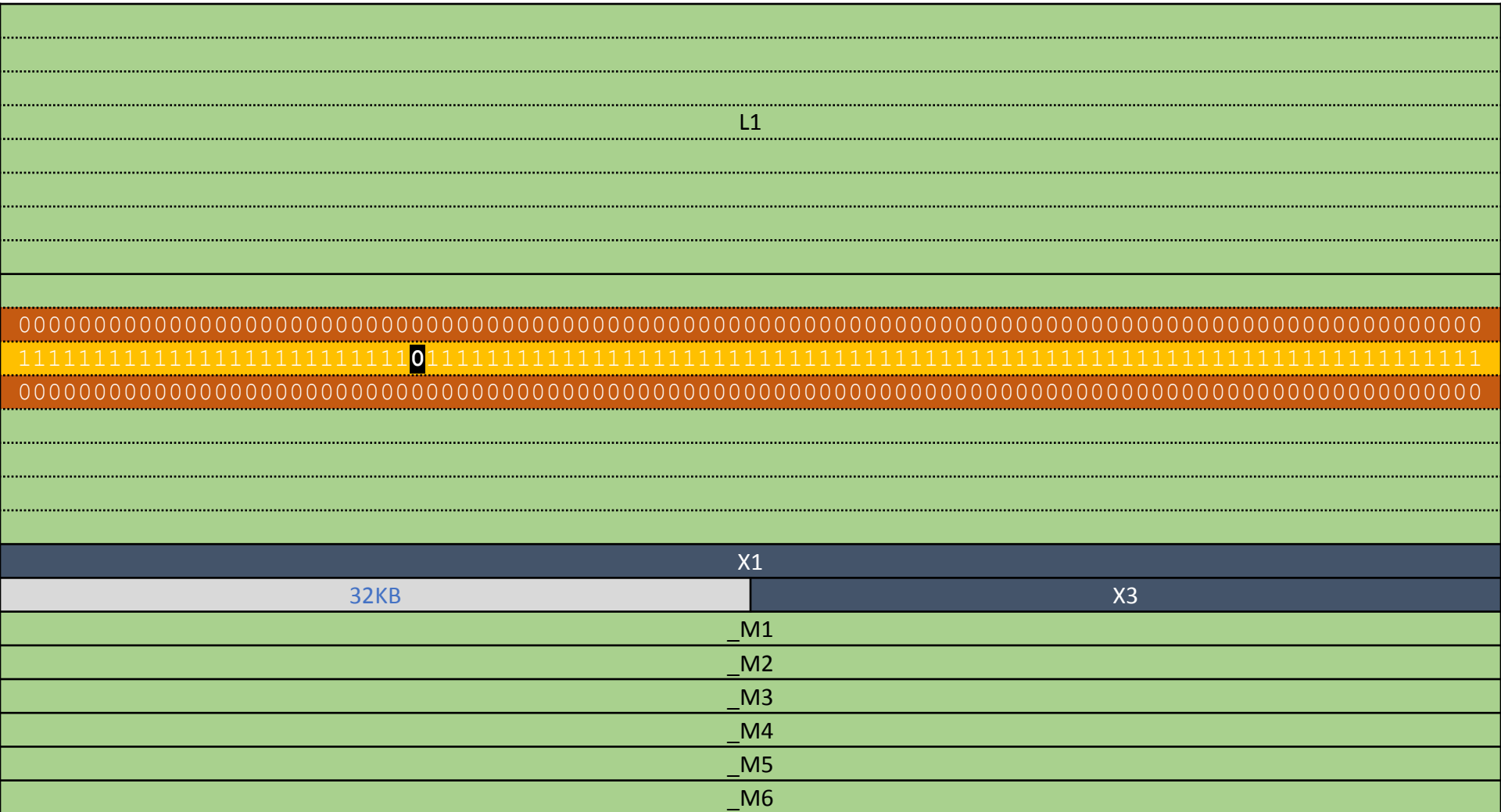
```
_M1, _M2, ..., _Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 3/8

Release *Large* chunk with vulnerable page

**Release (L2) ; // L chunk with vulnerable page**

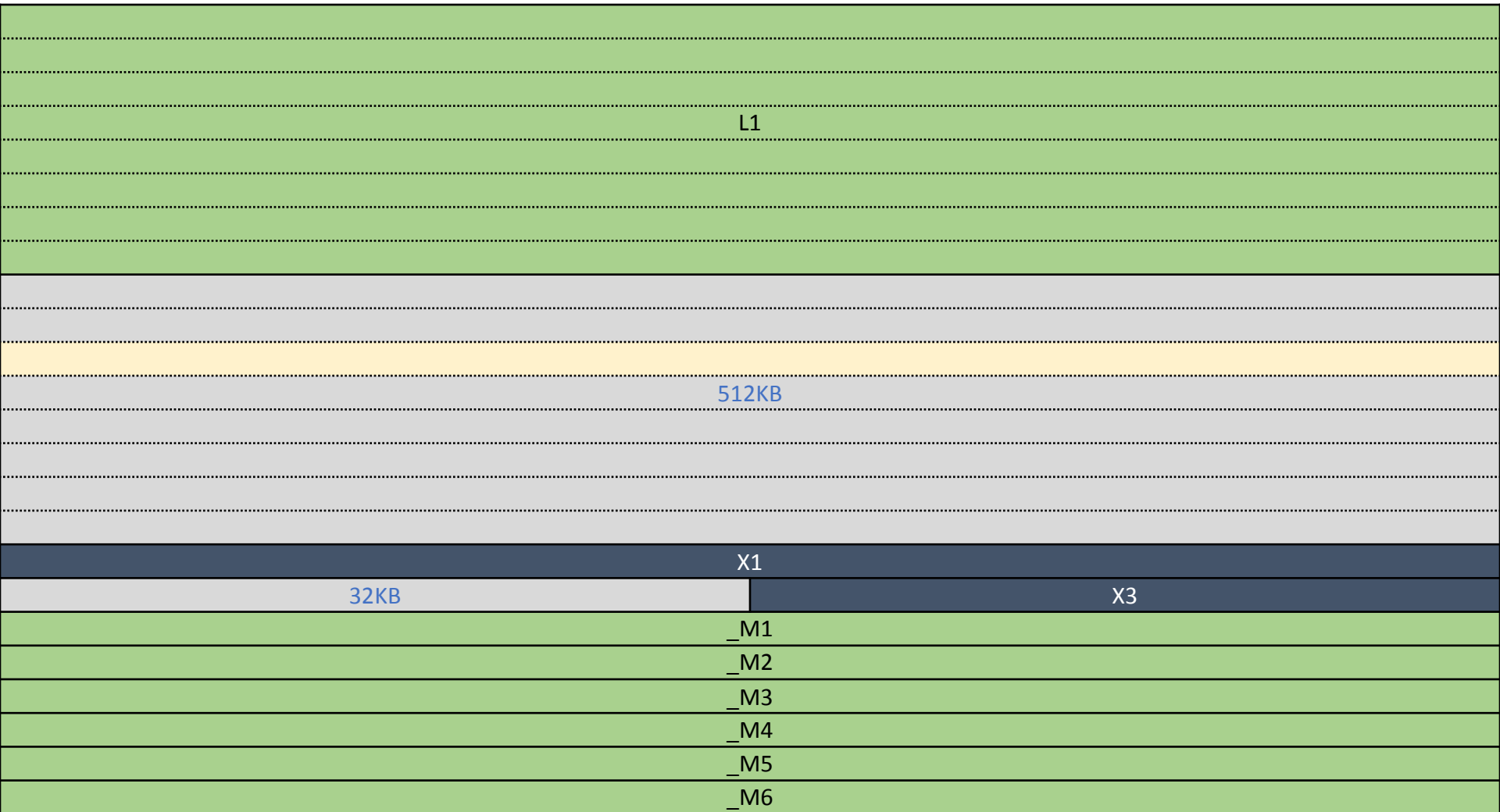




# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

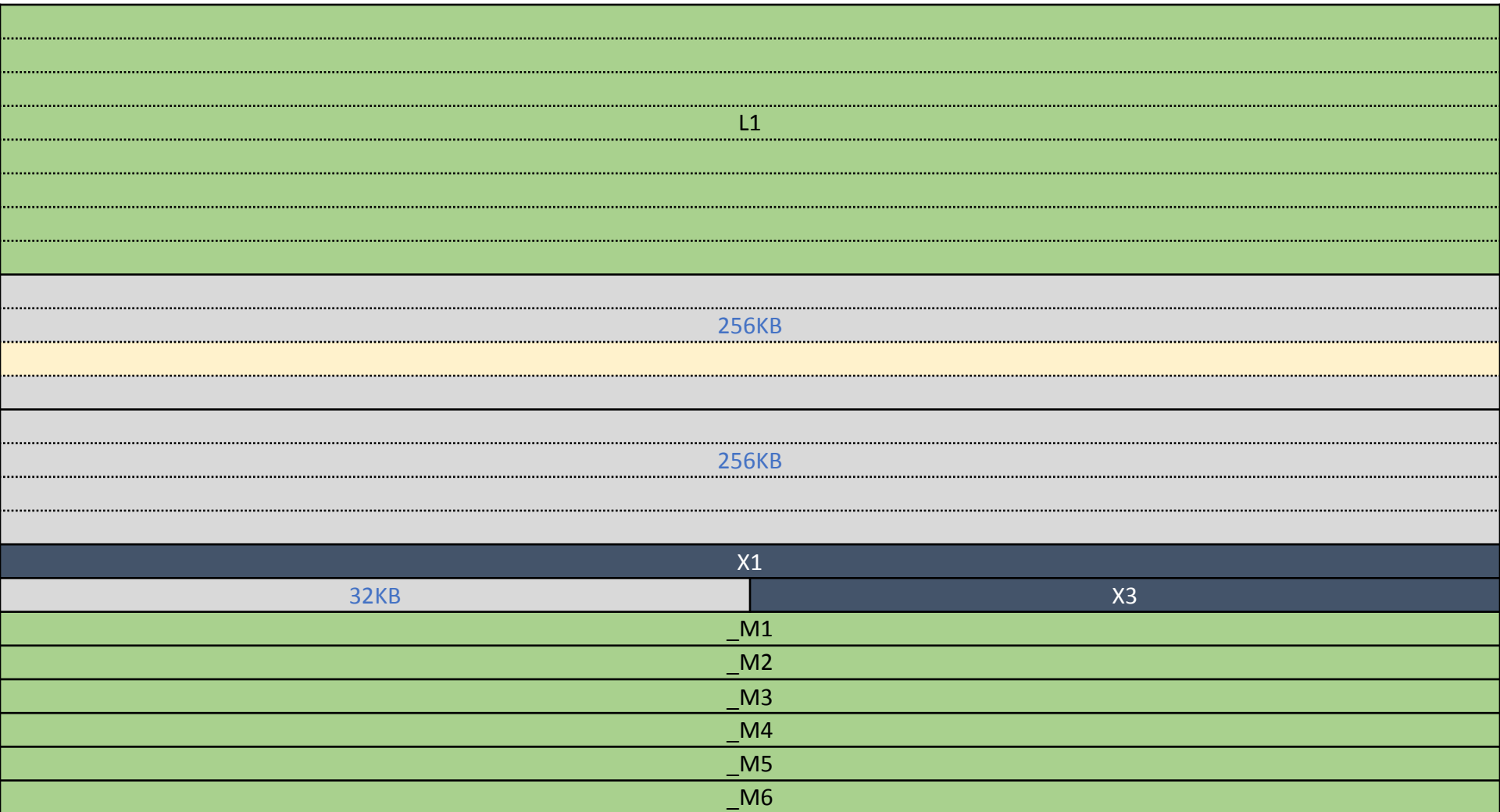
```
M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks
```



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

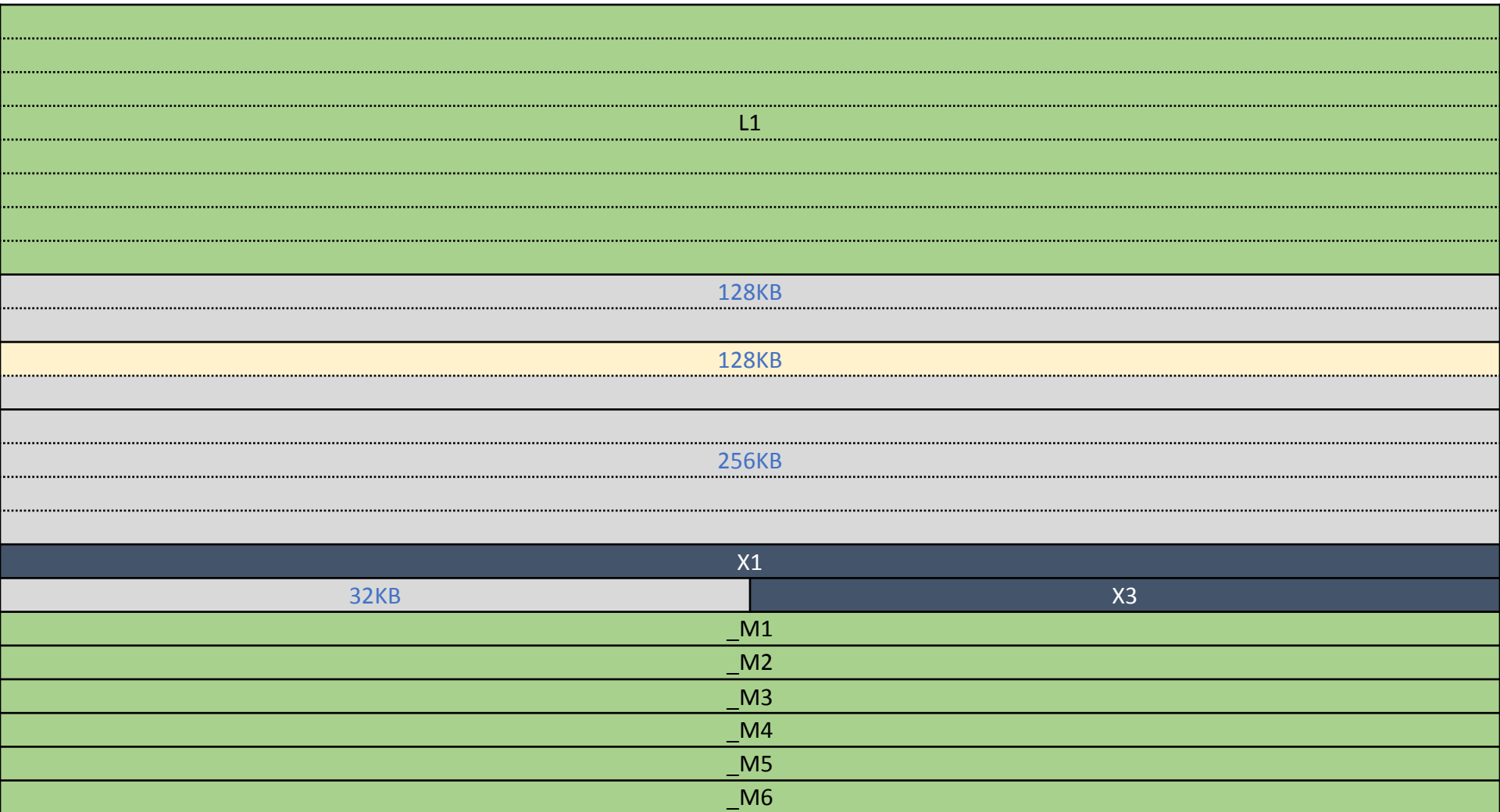
```
M1, M2, ..., Mn = exhaust(6); // get all 2^6 = 64KB chunks
```



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

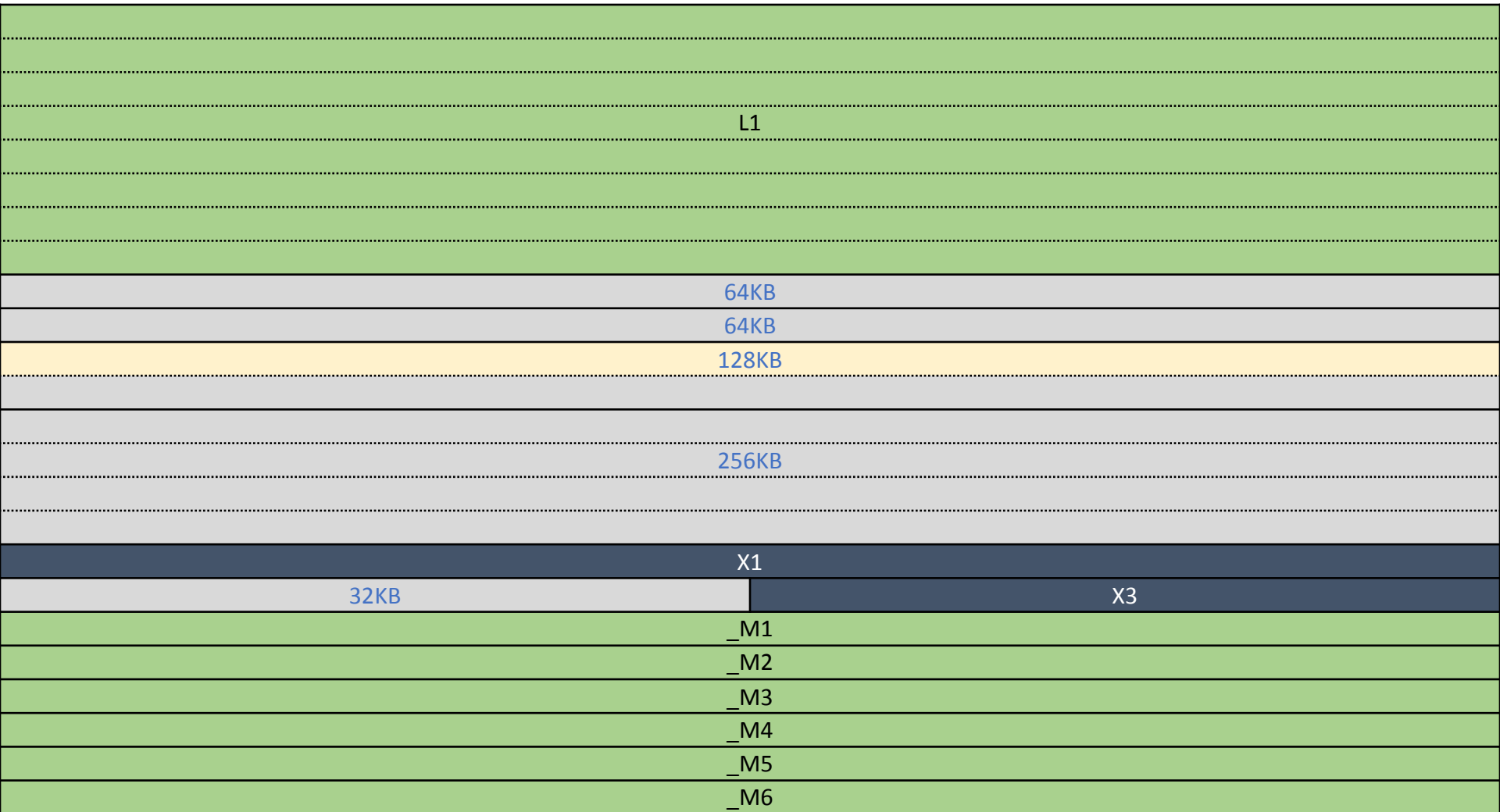
**M1, M2, ..., Mn = exhaust(6);** // get all  $2^6 = 64\text{KB}$  chunks



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

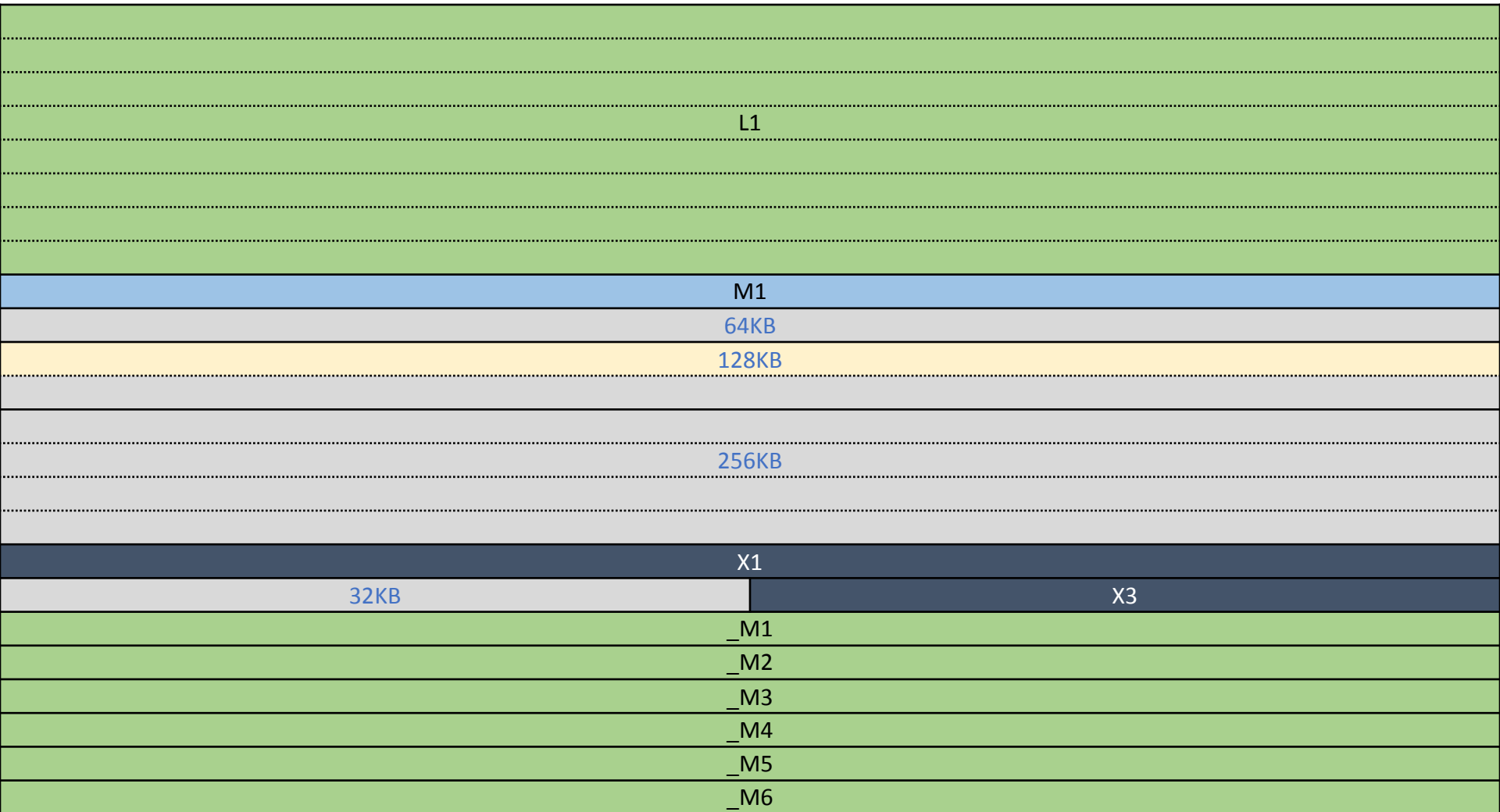
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

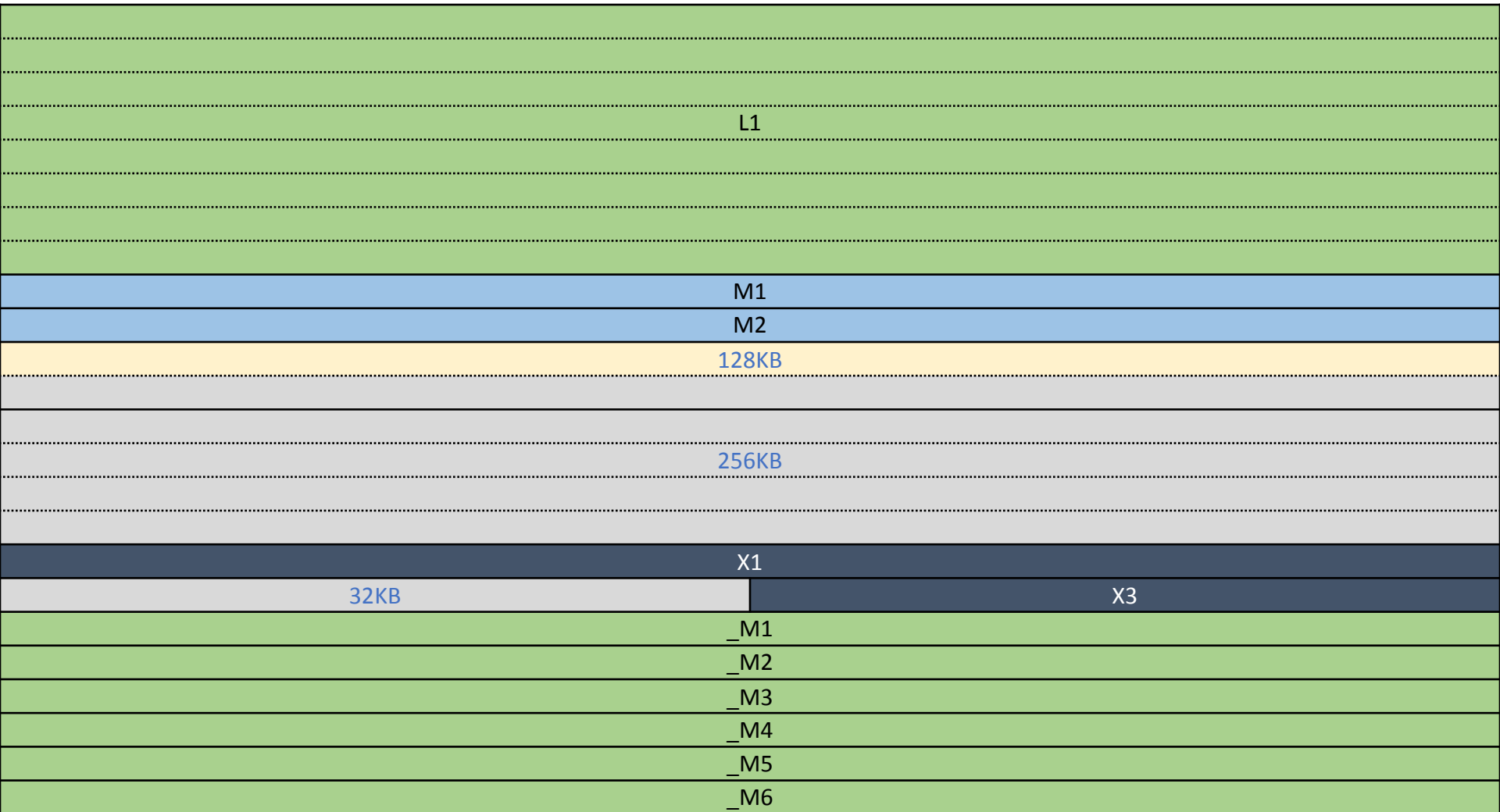
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

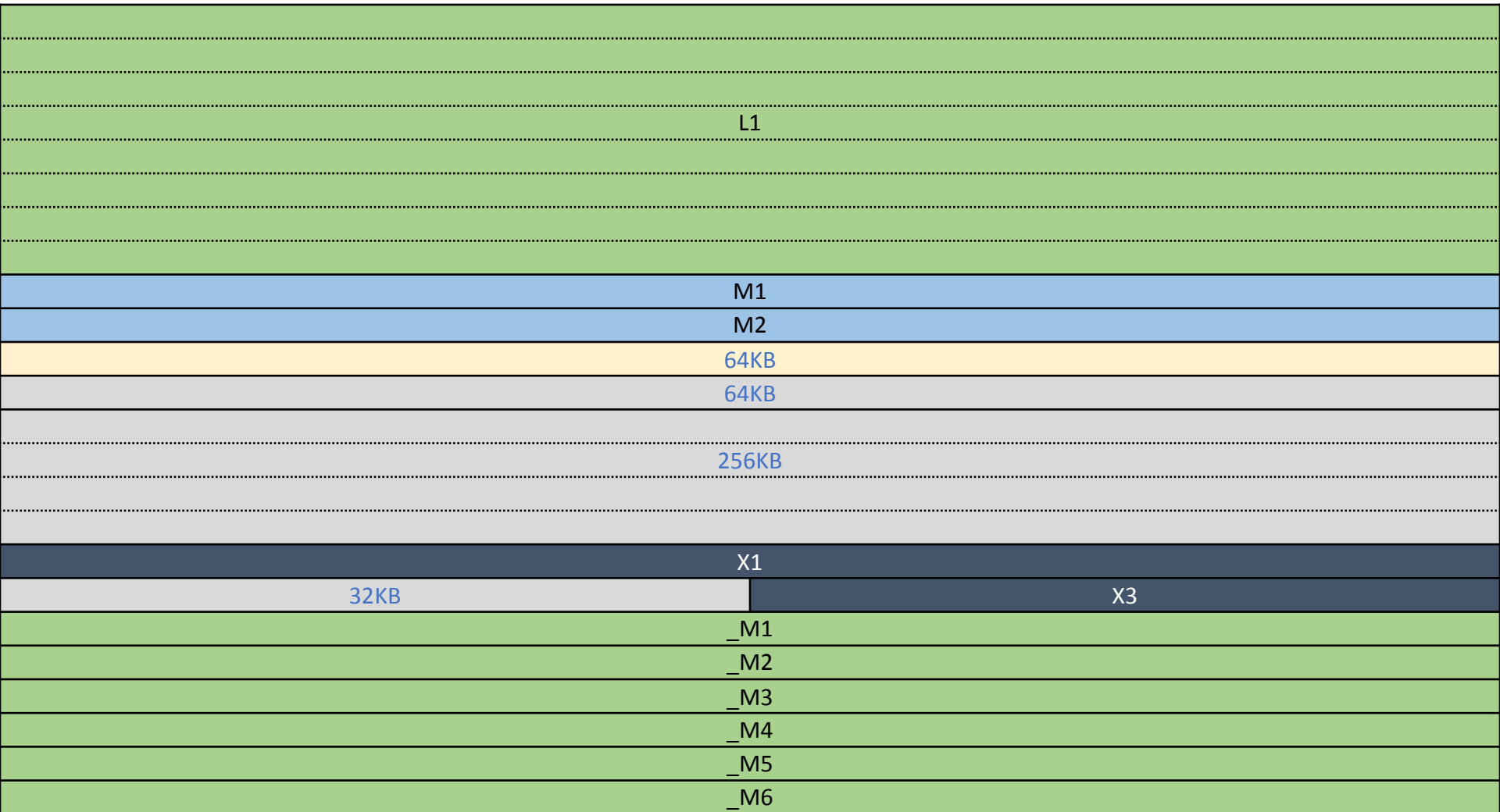
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

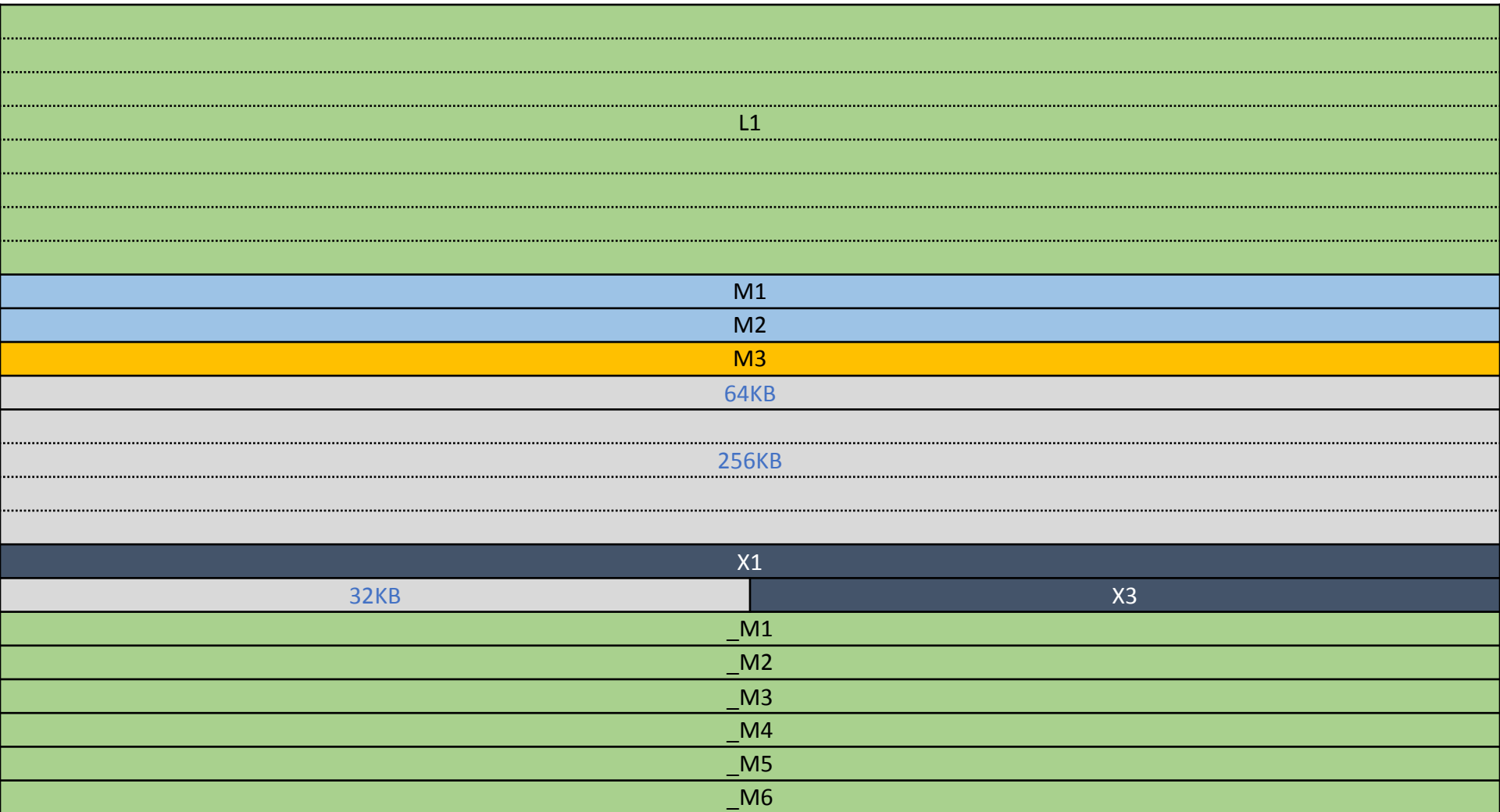
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**

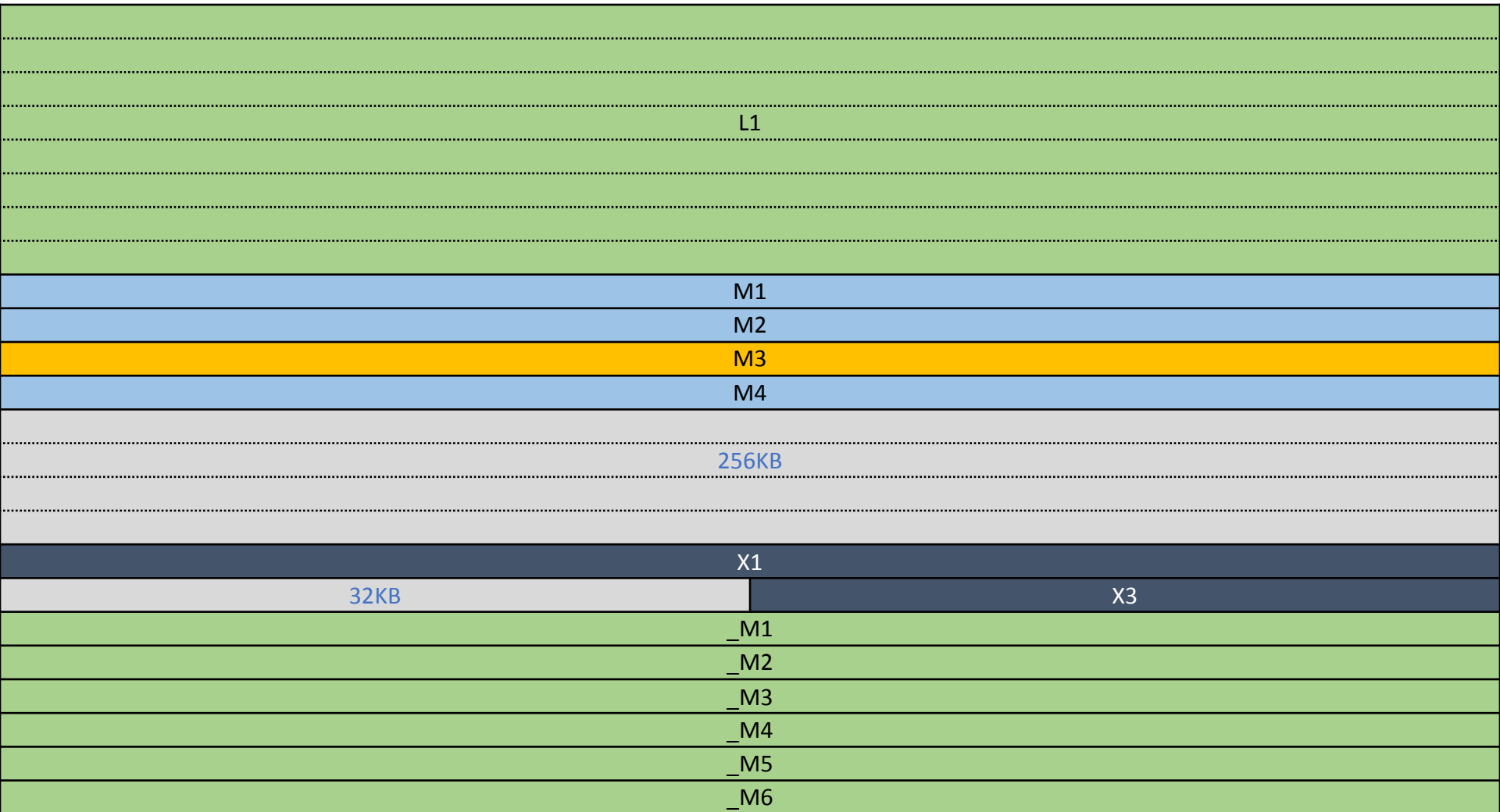




# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

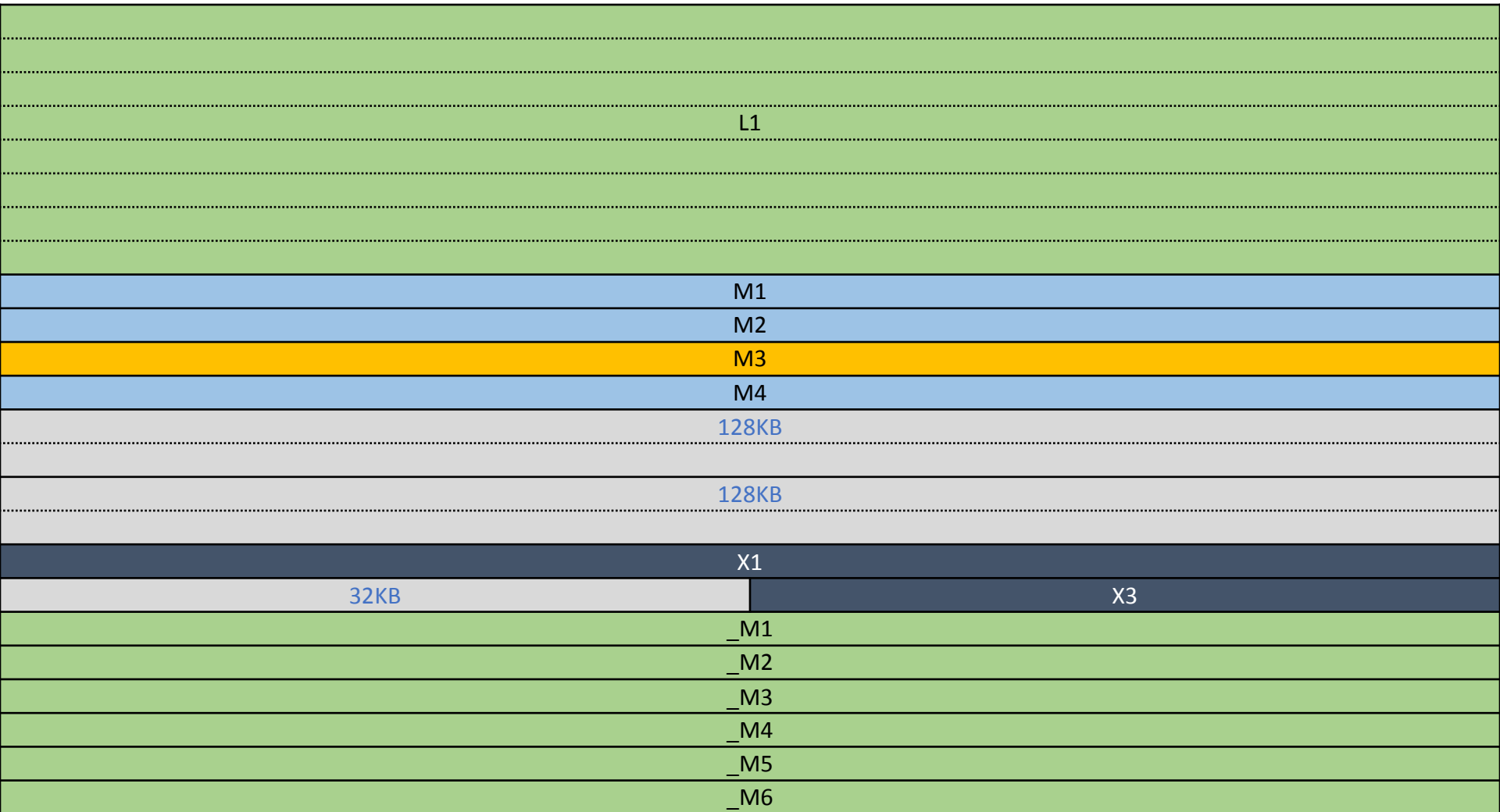
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

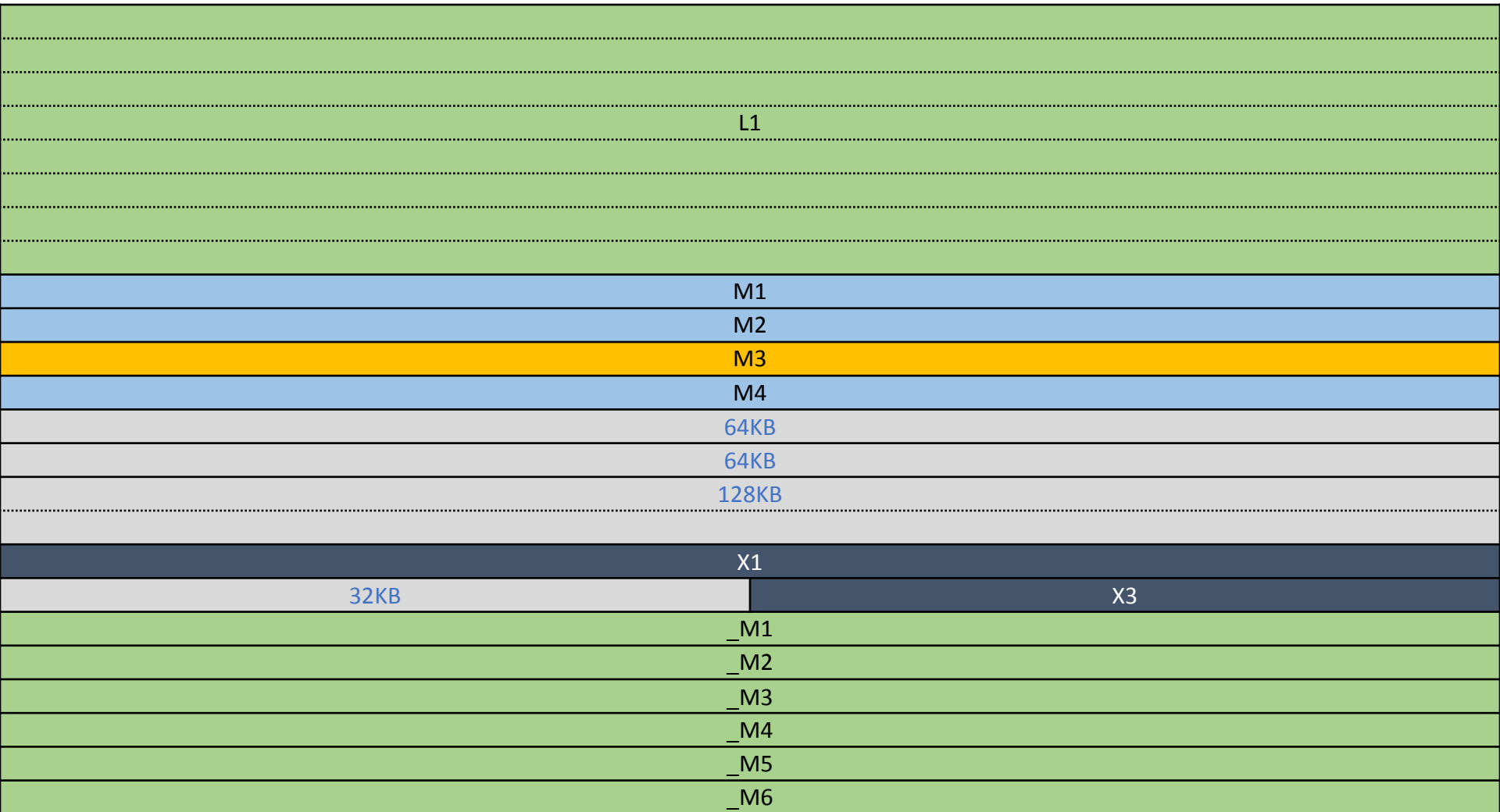
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

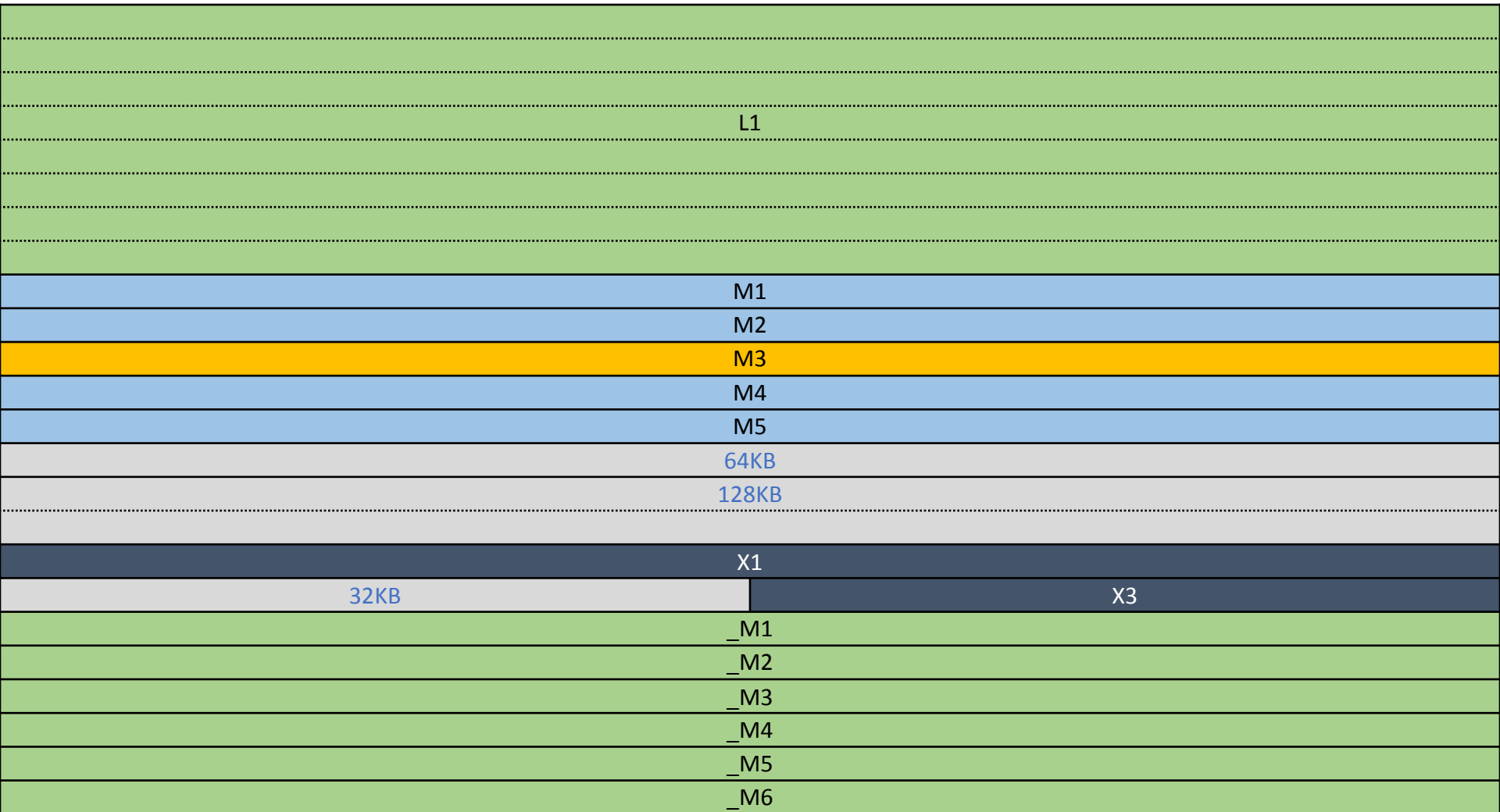
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

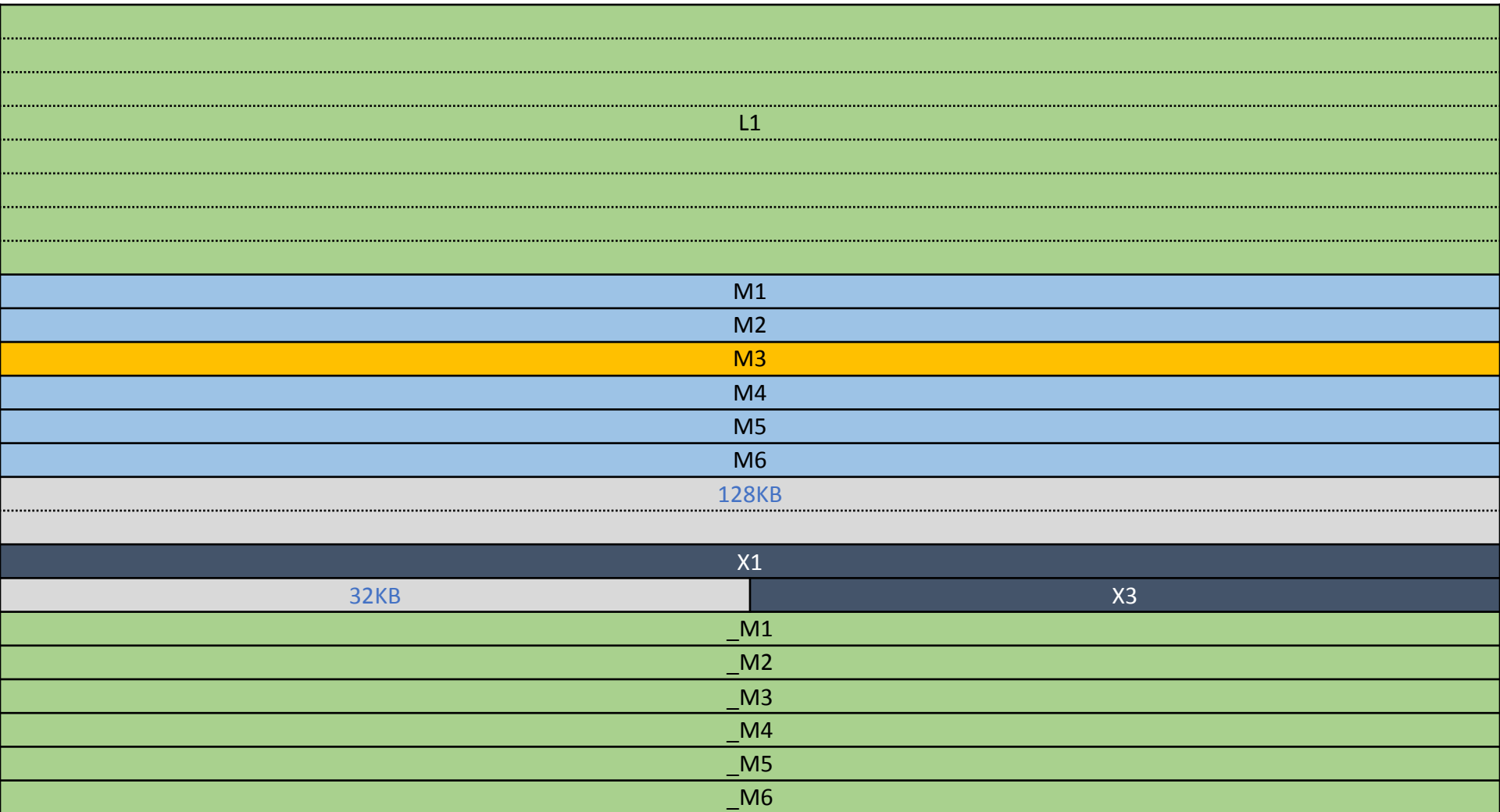
```
M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks
```



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

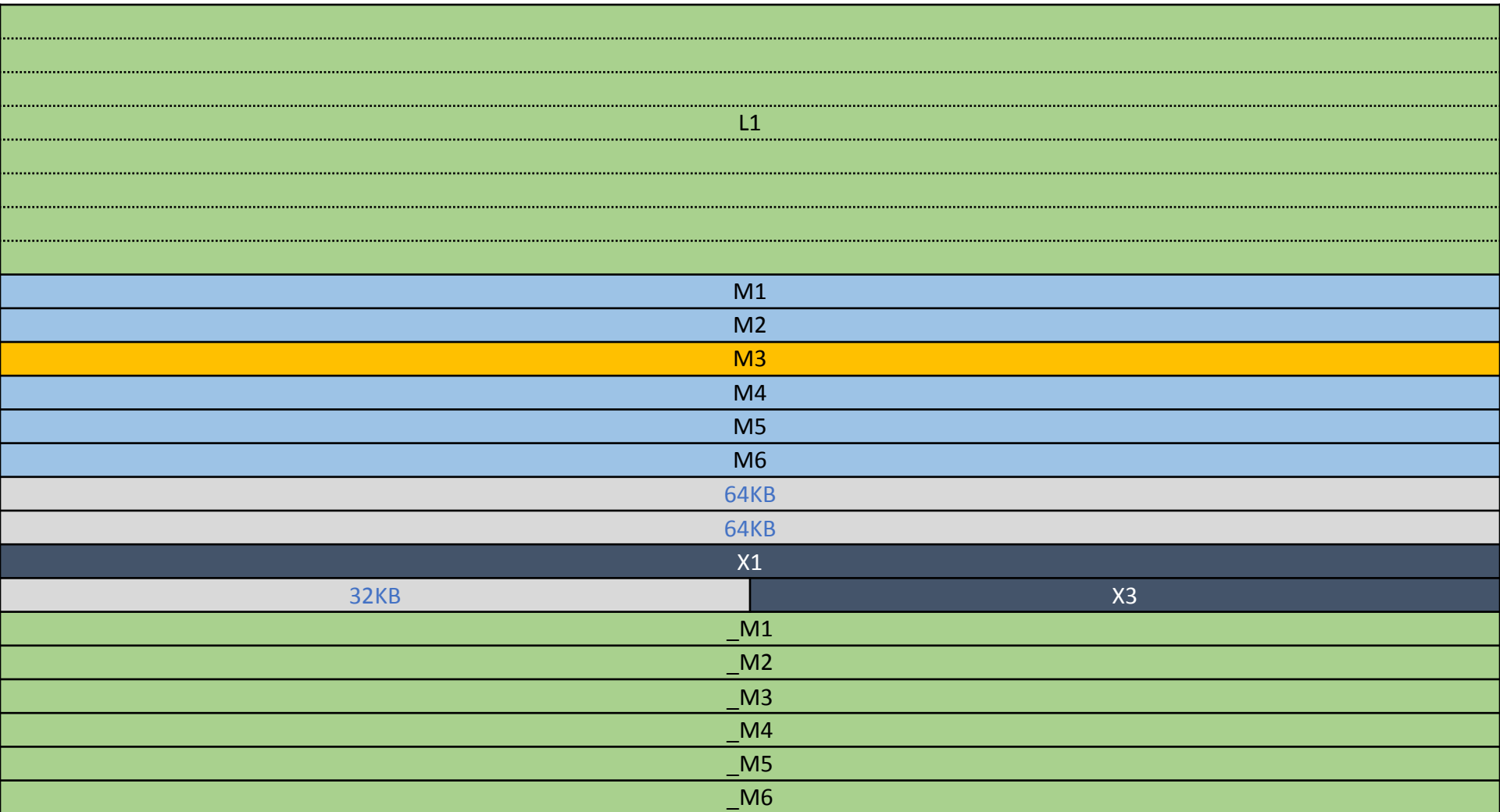
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

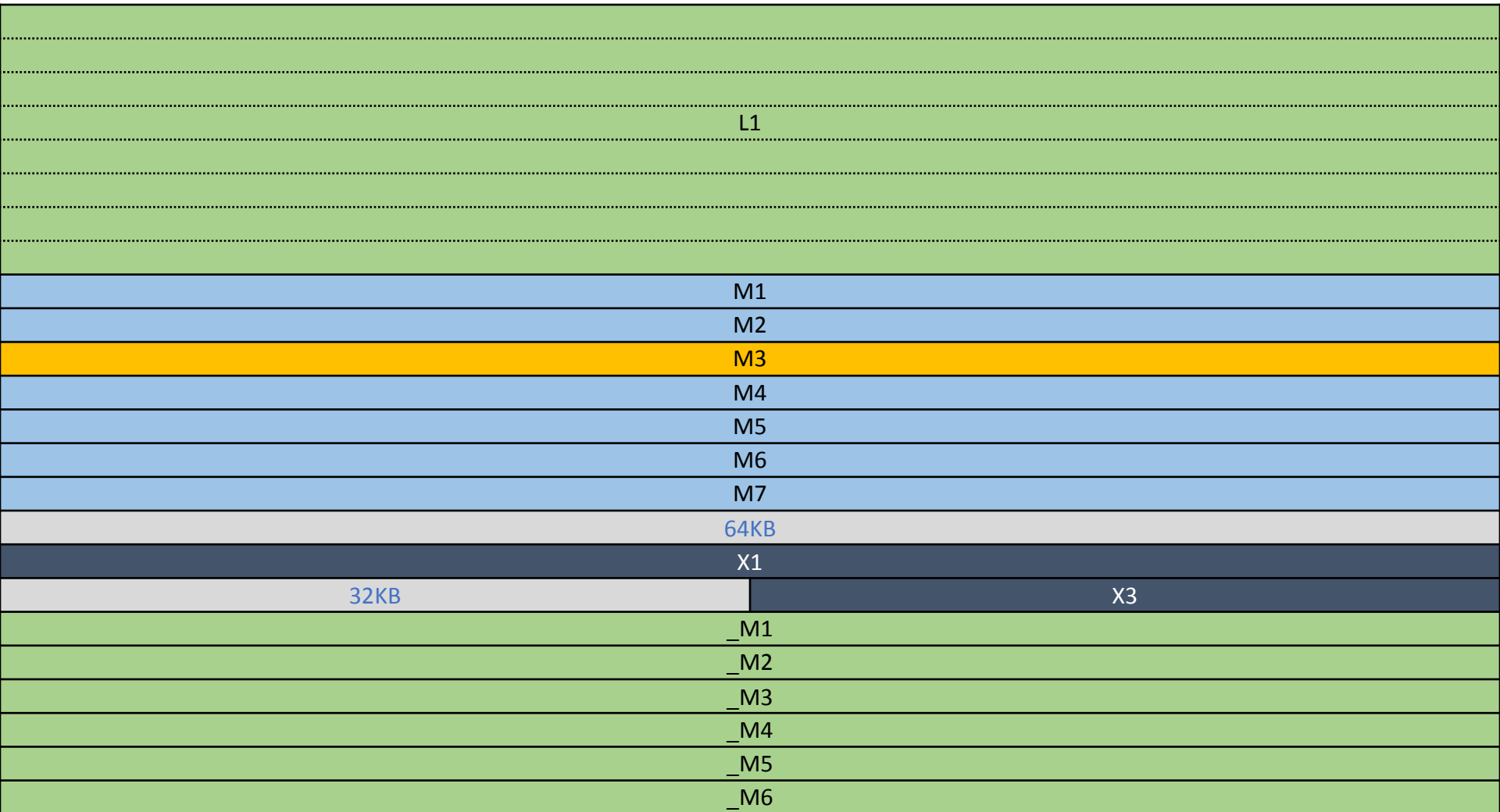
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

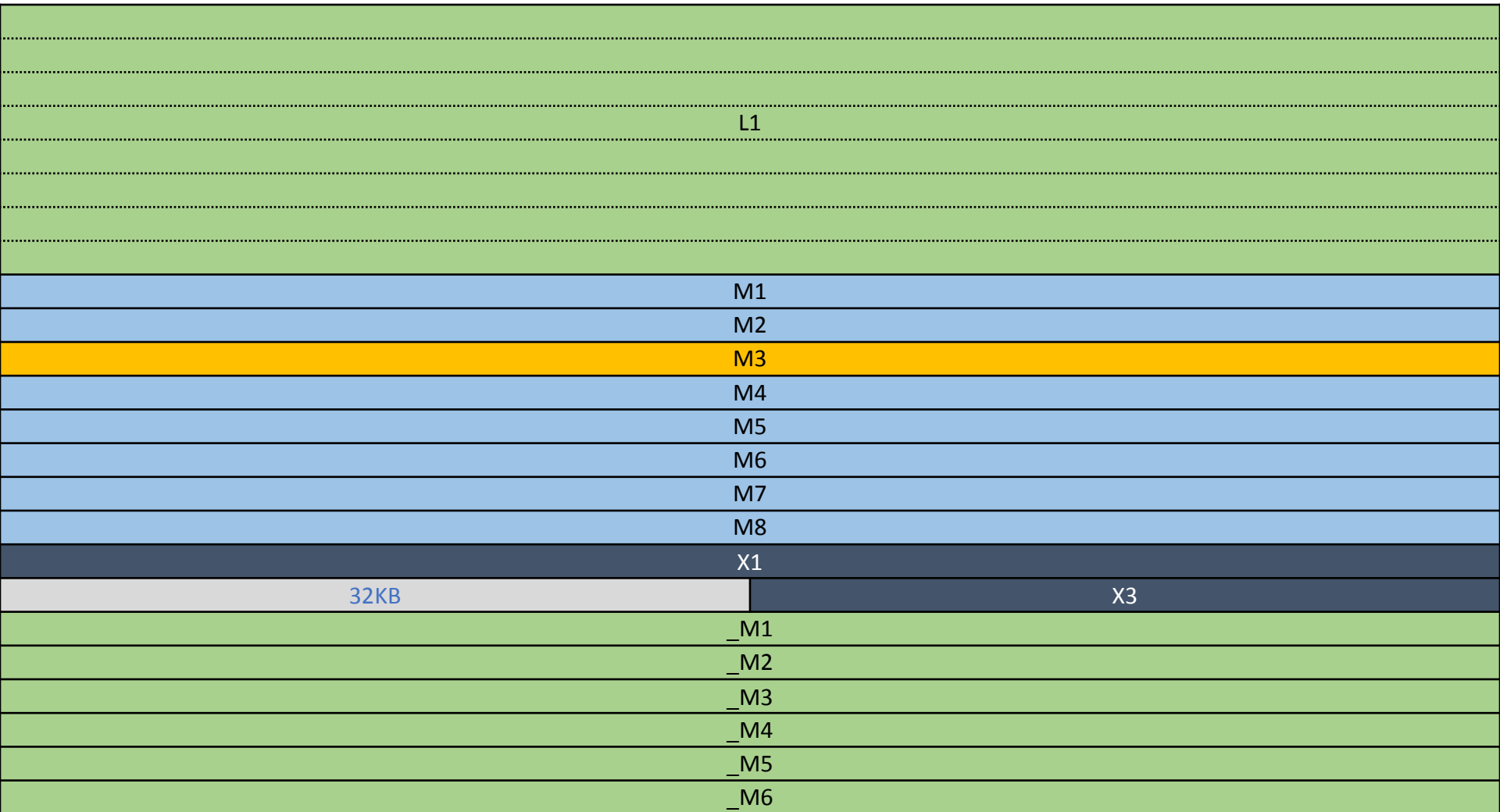
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**



# Phys Feng Shui step 4/8

Exhaust *Medium-sized* chunks (again)

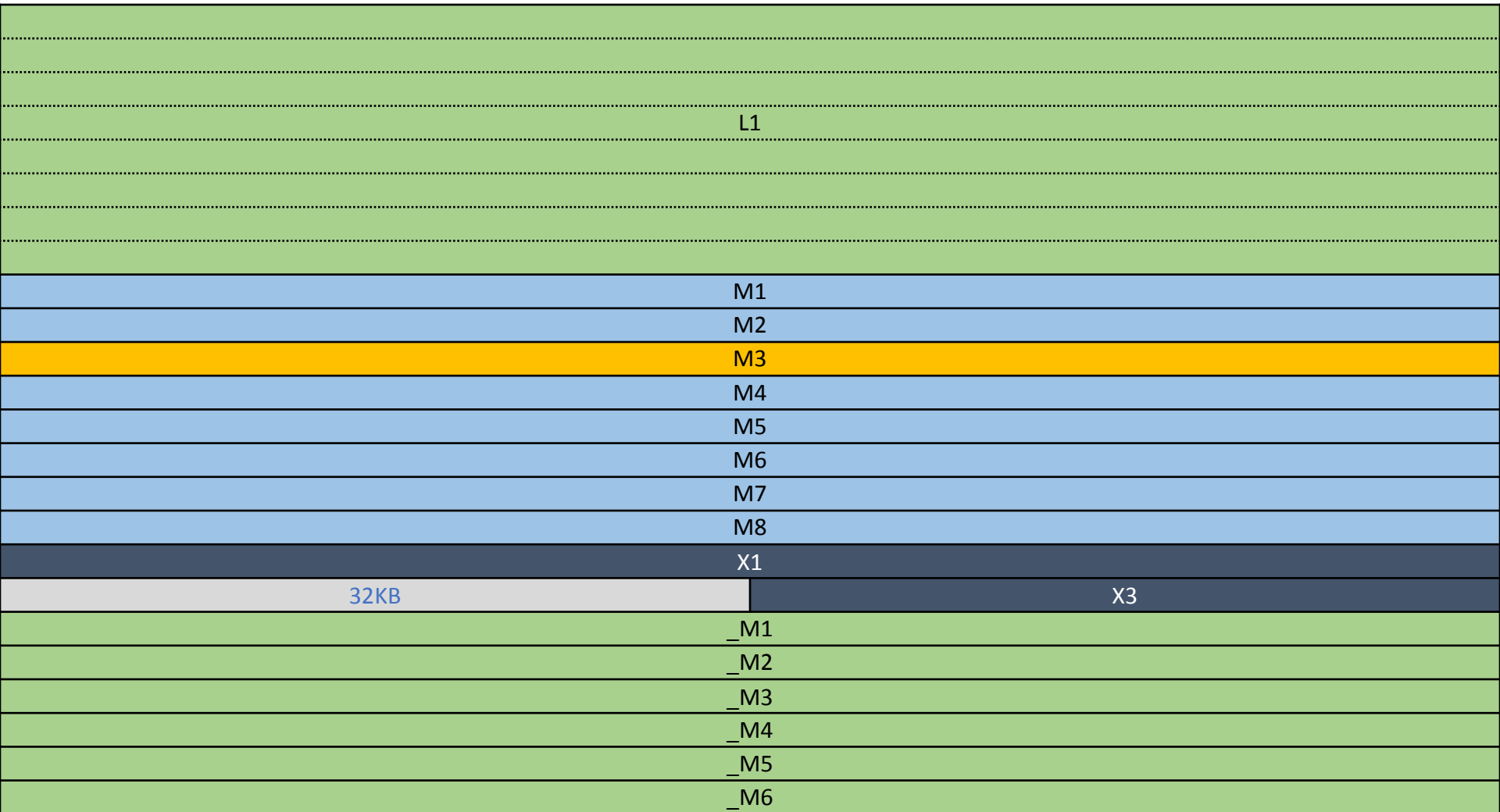
**M1, M2, ..., Mn = exhaust(6); // get all  $2^6 = 64\text{KB}$  chunks**





# Phys Feng Shui step 5/8

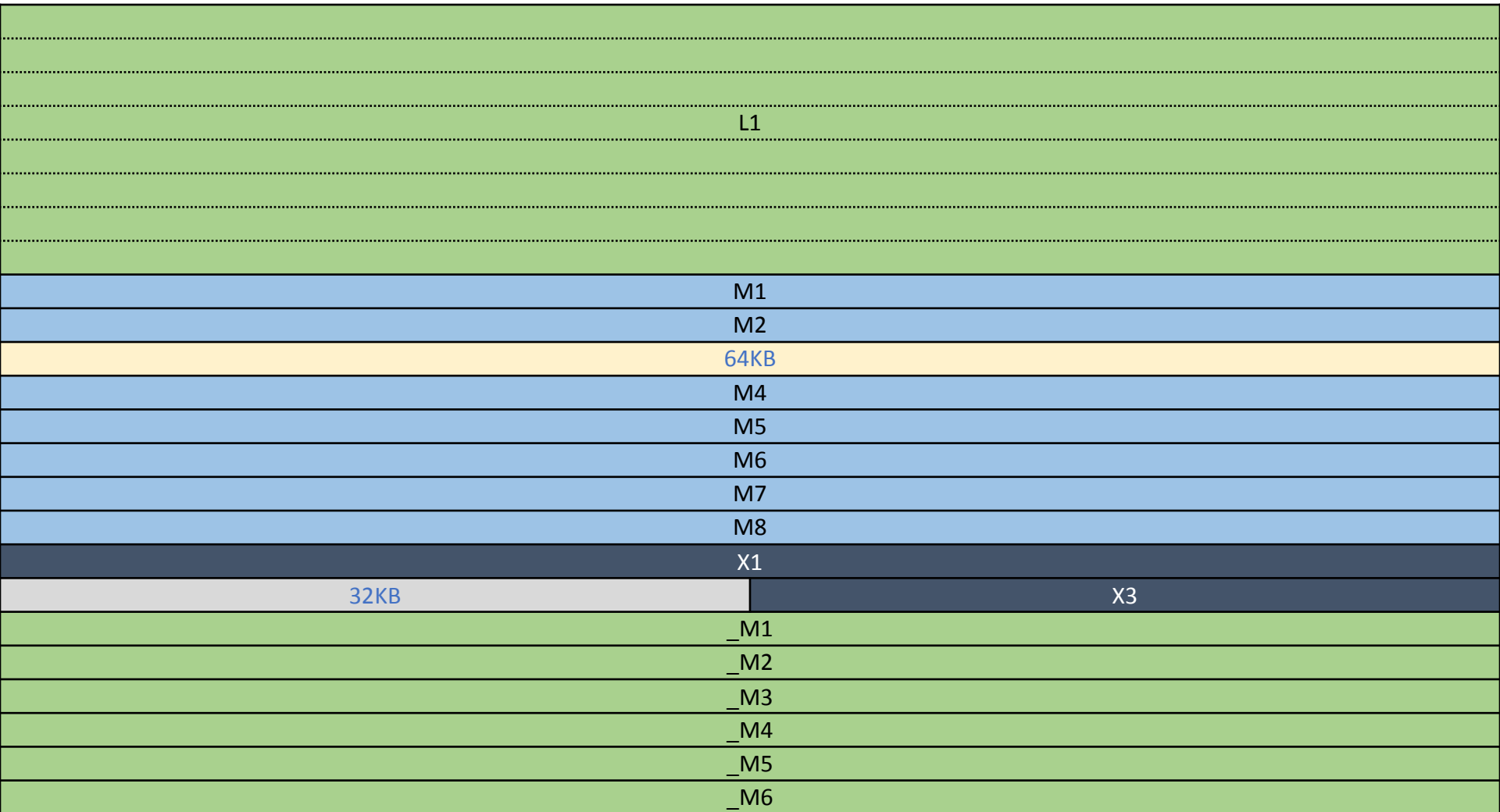
Release vulnerable *Medium-sized* chunk + Release all Large chunks



# Phys Feng Shui step 5/8

Release vulnerable *Medium-sized* chunk + Release all Large chunks

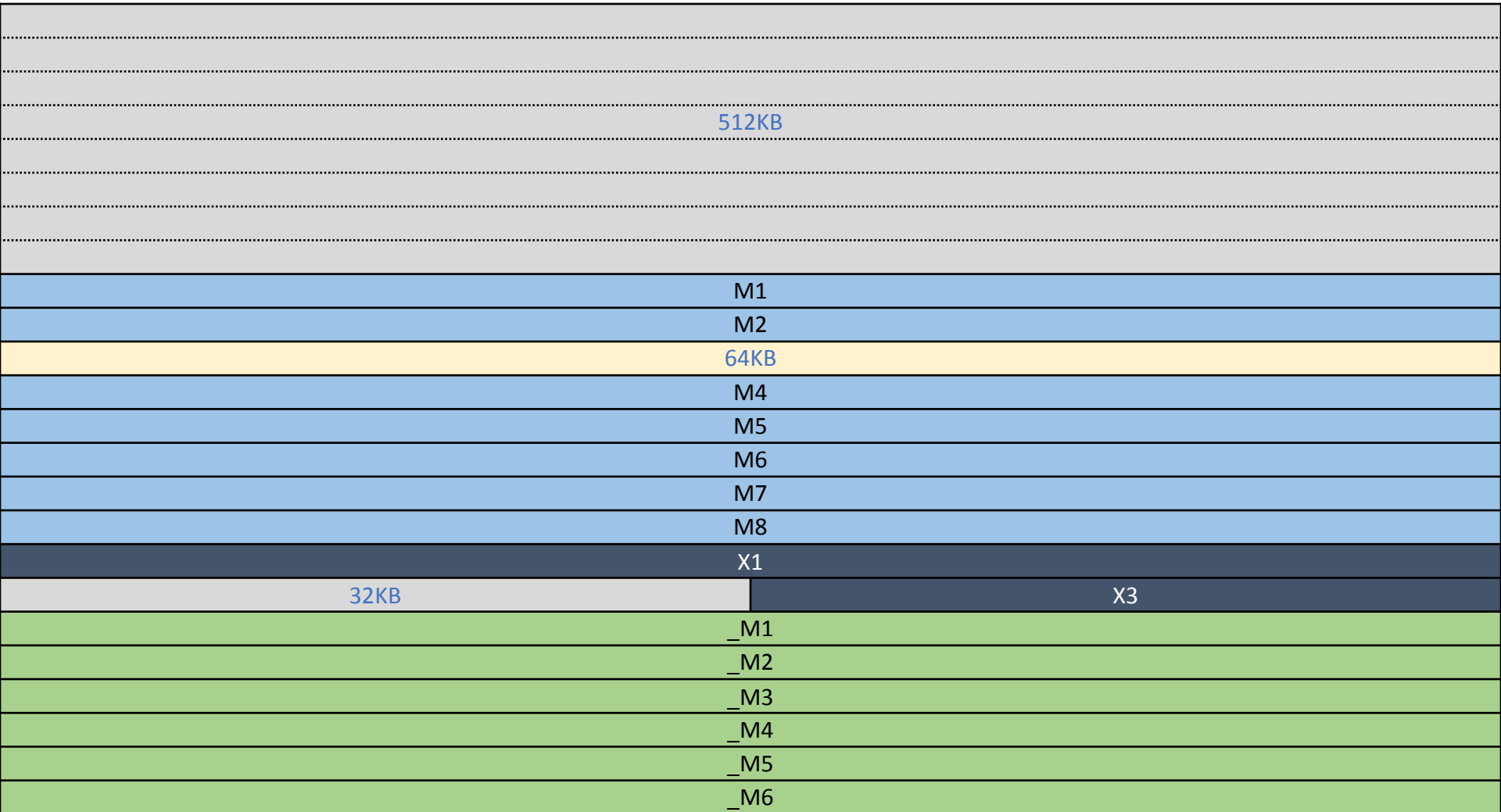
**Release (M3) ; // releases the vulnerable row**



# Phys Feng Shui step 5/8

Release vulnerable *Medium-sized* chunk + Release all Large chunks

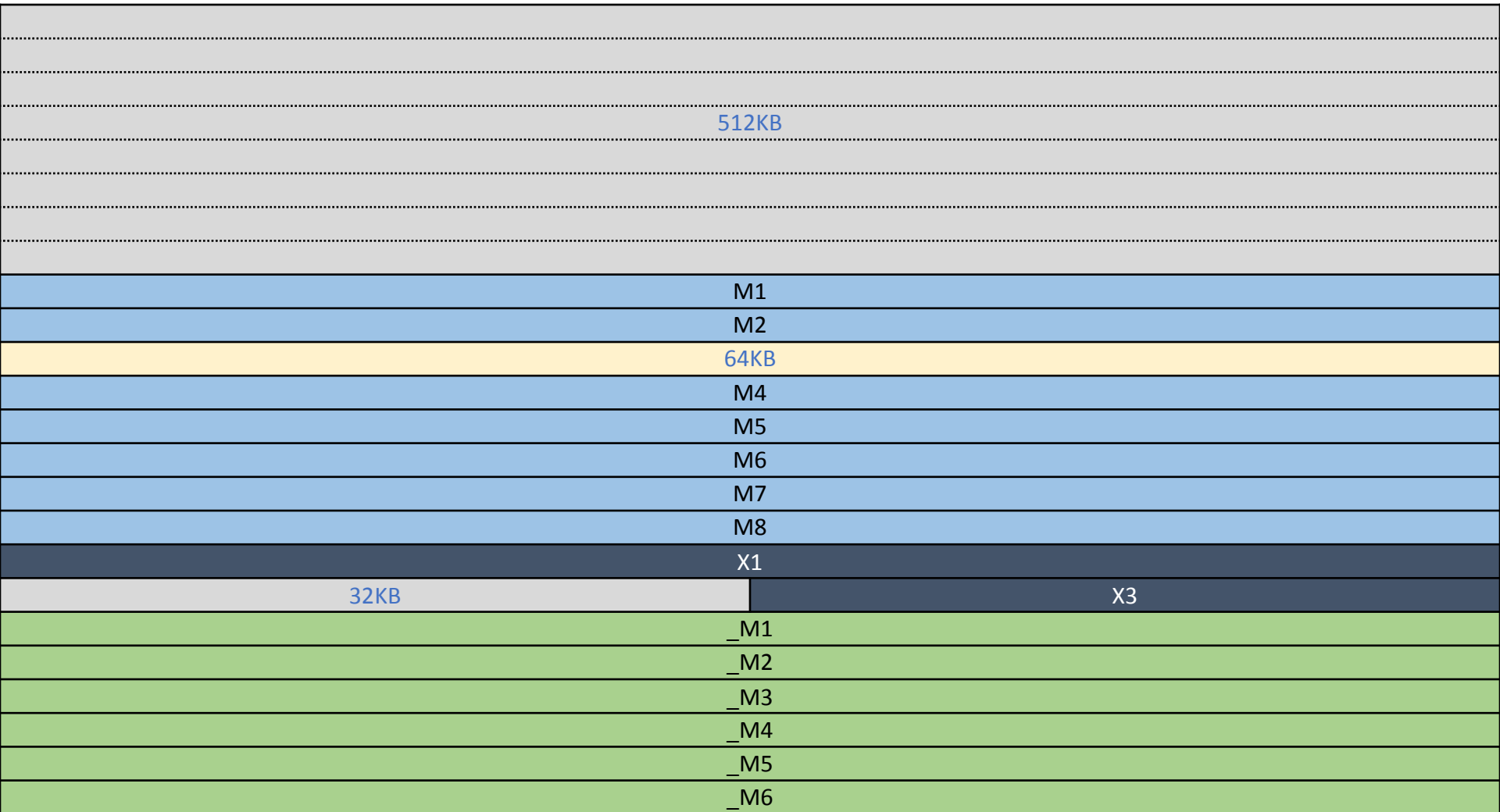
`ReleaseAll(L); //` to avoid going out-of-memory later



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

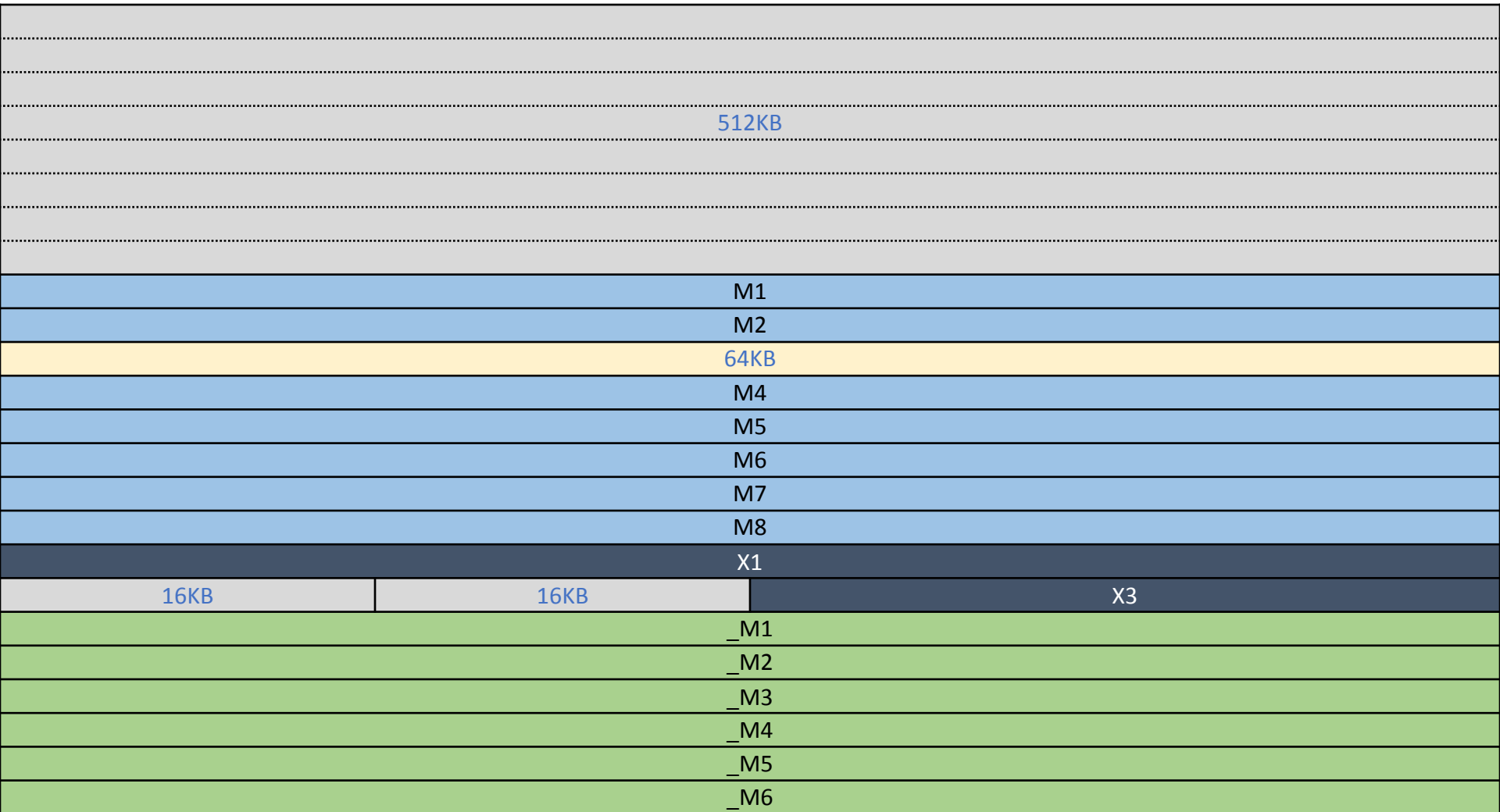
```
Land(S) ; // allocate 4KB pages until the 64KB is used
```



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

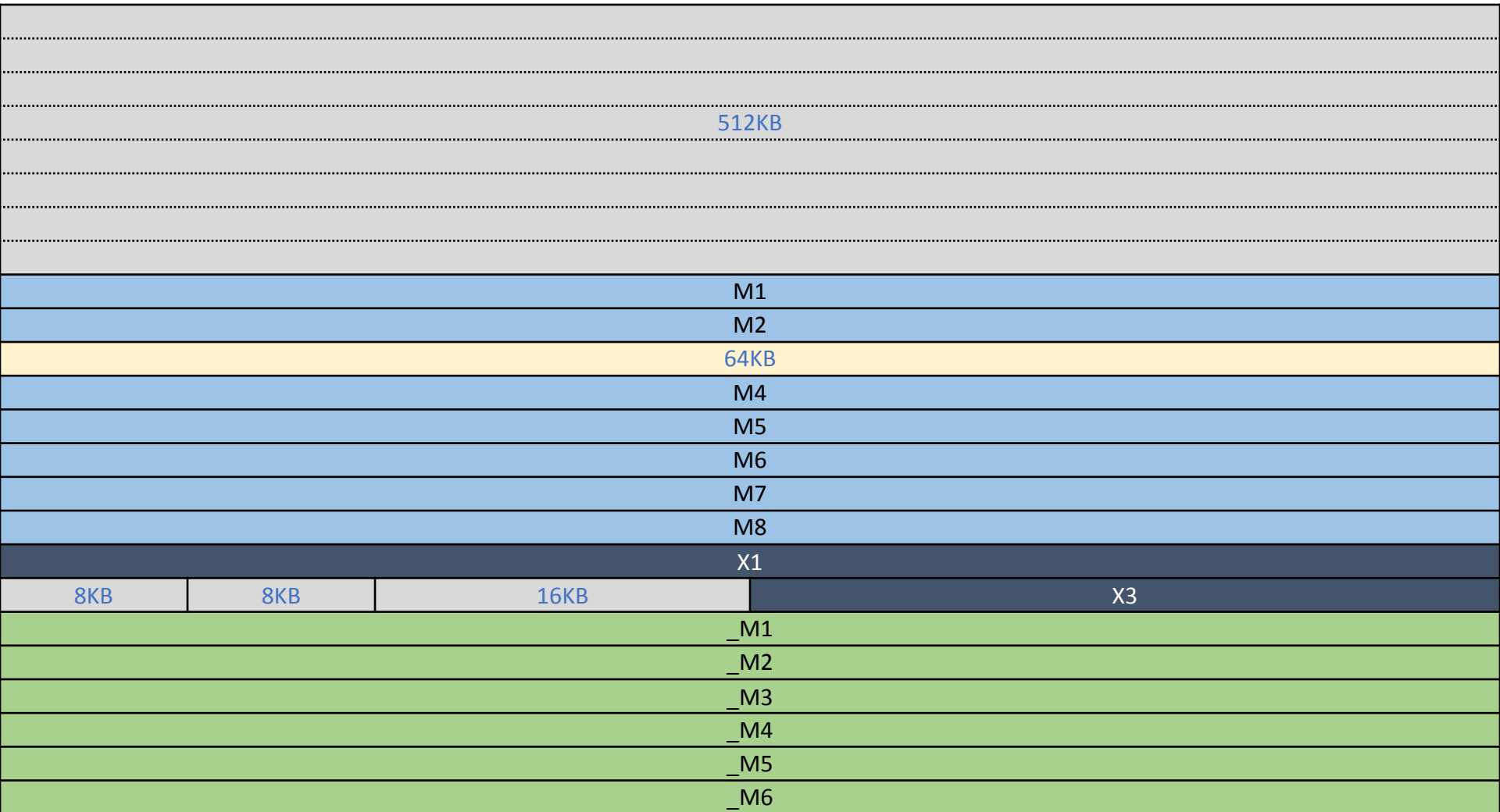
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

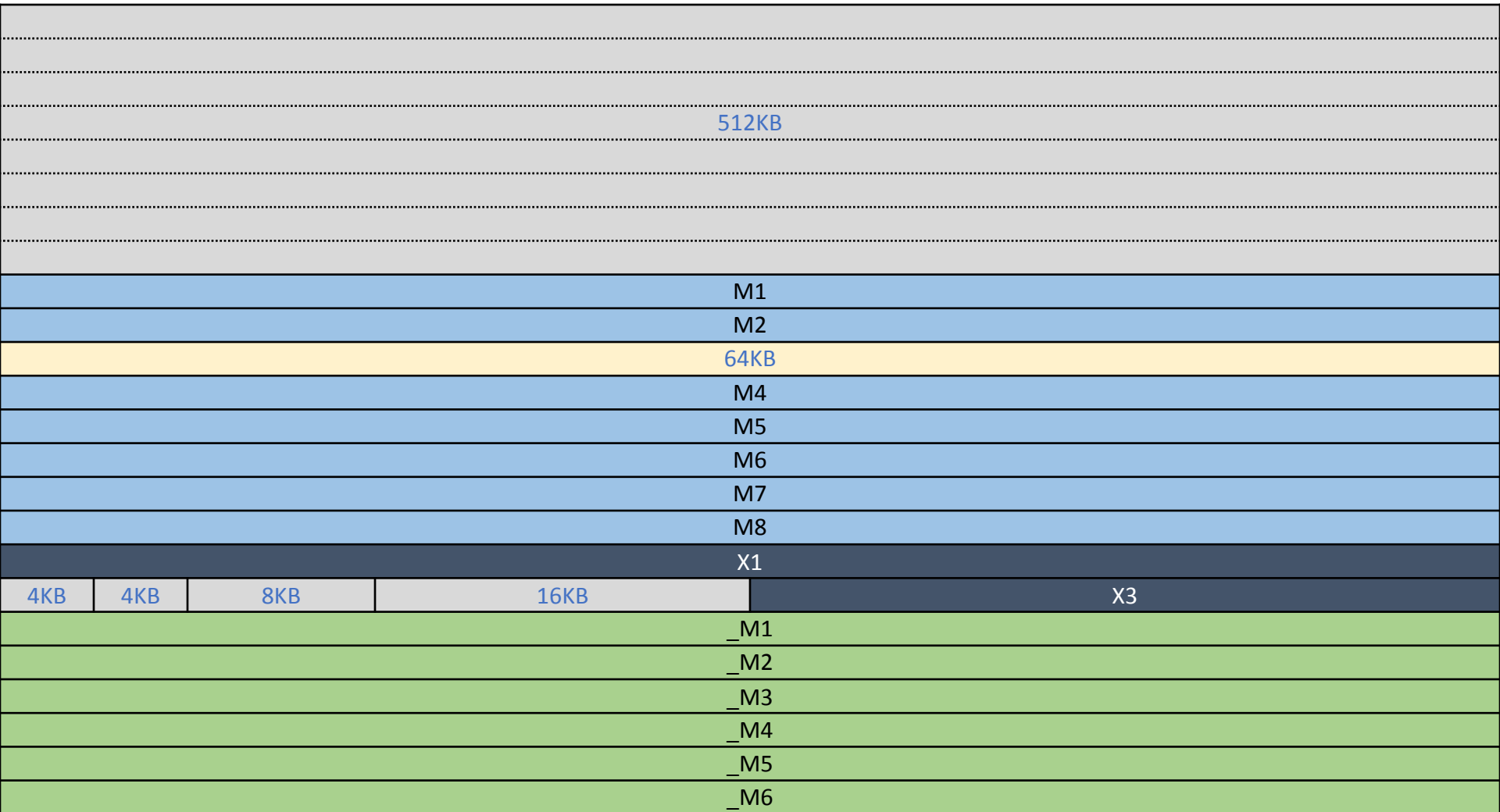
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

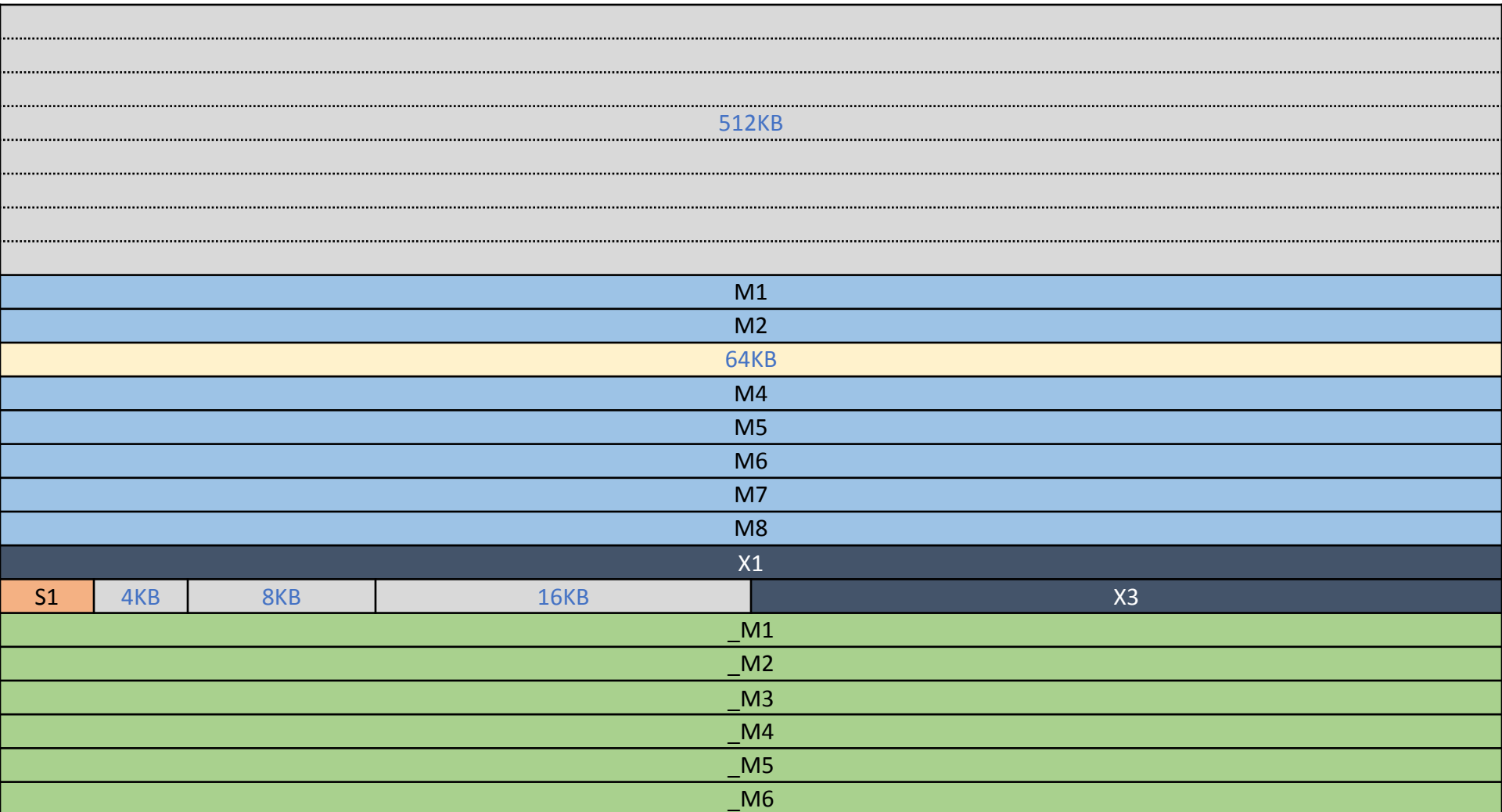
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

**Land (S) ; // allocate 4KB pages until the 64KB is used**

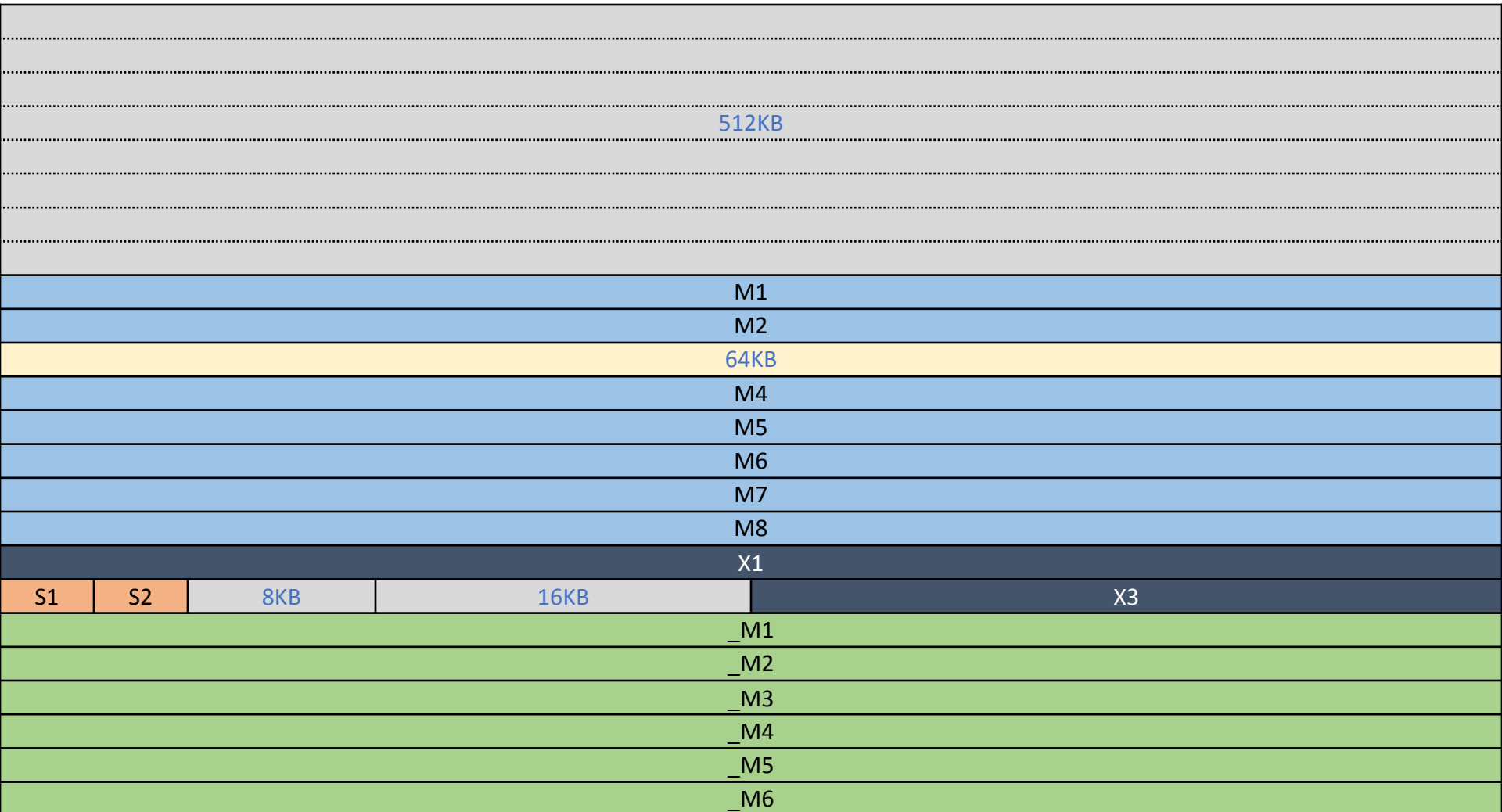




# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

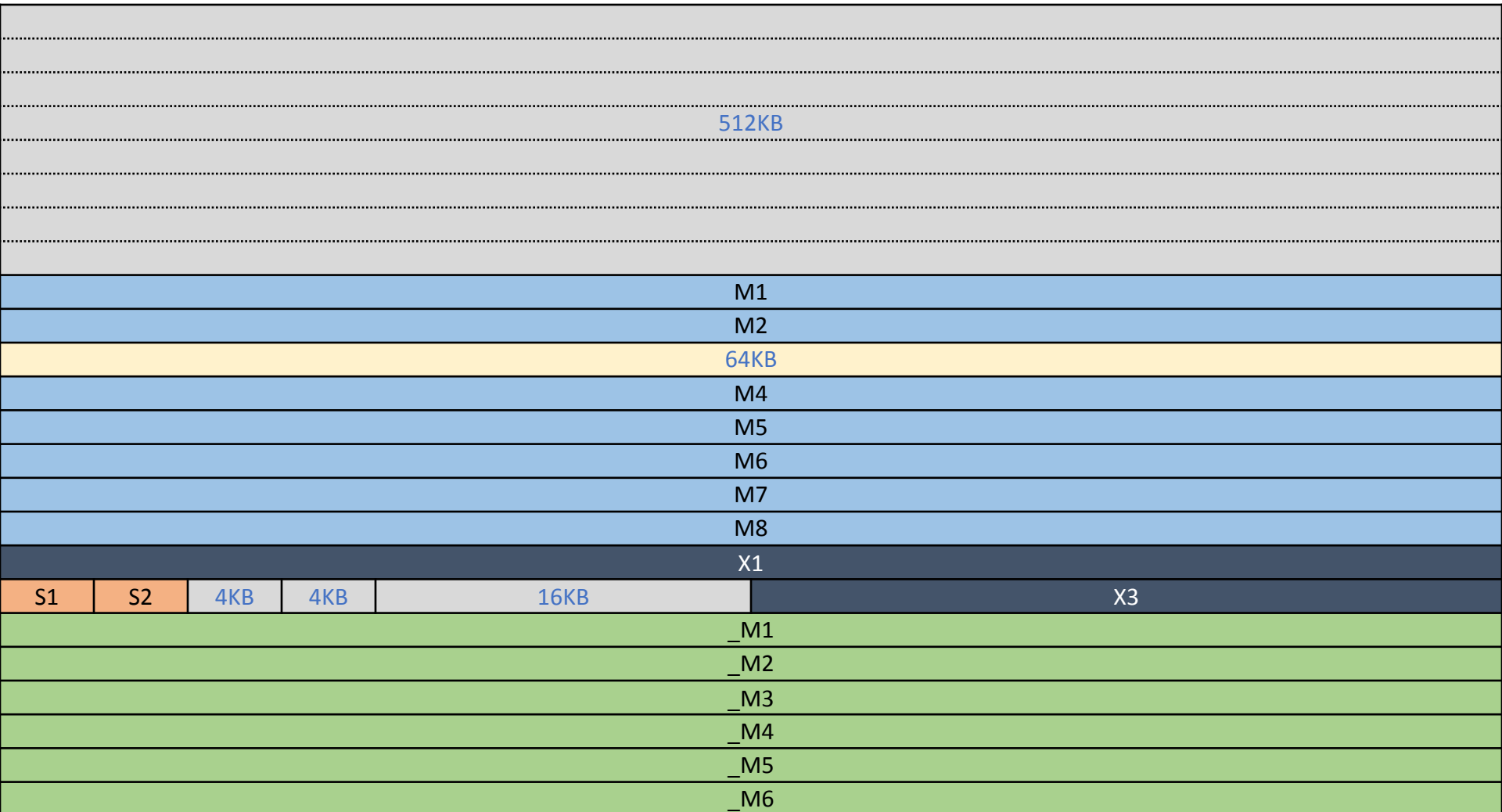
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

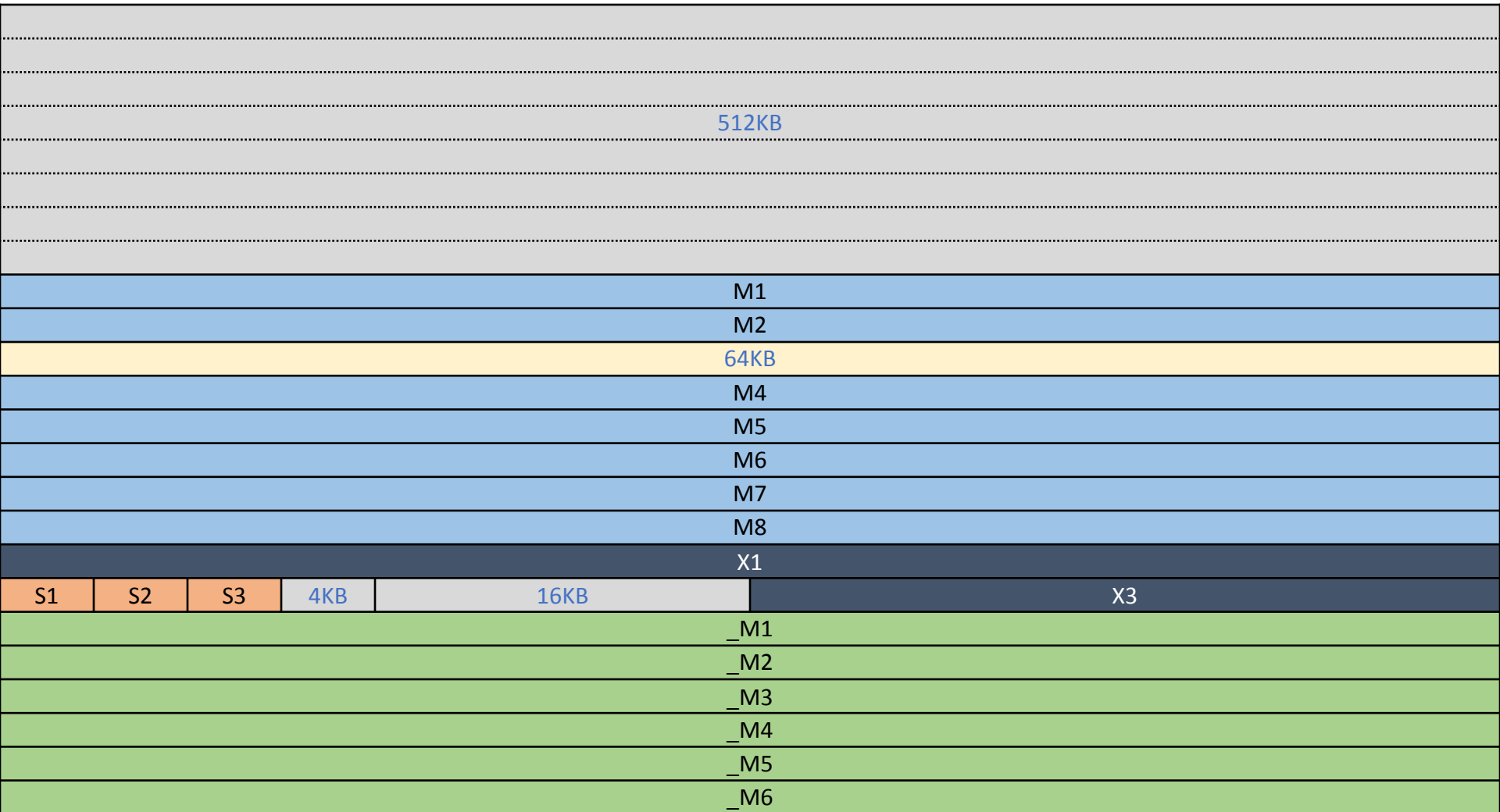
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# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

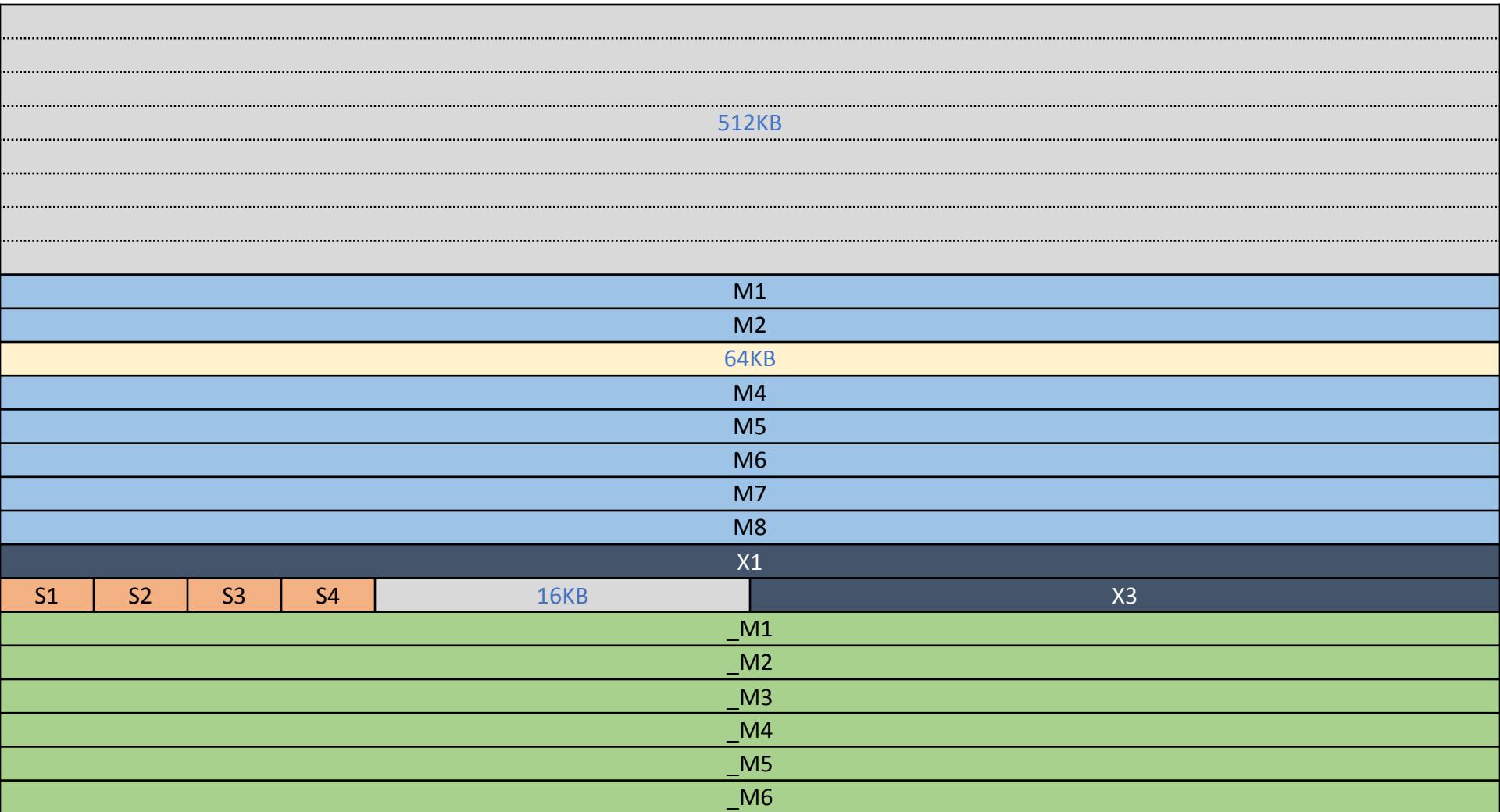
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

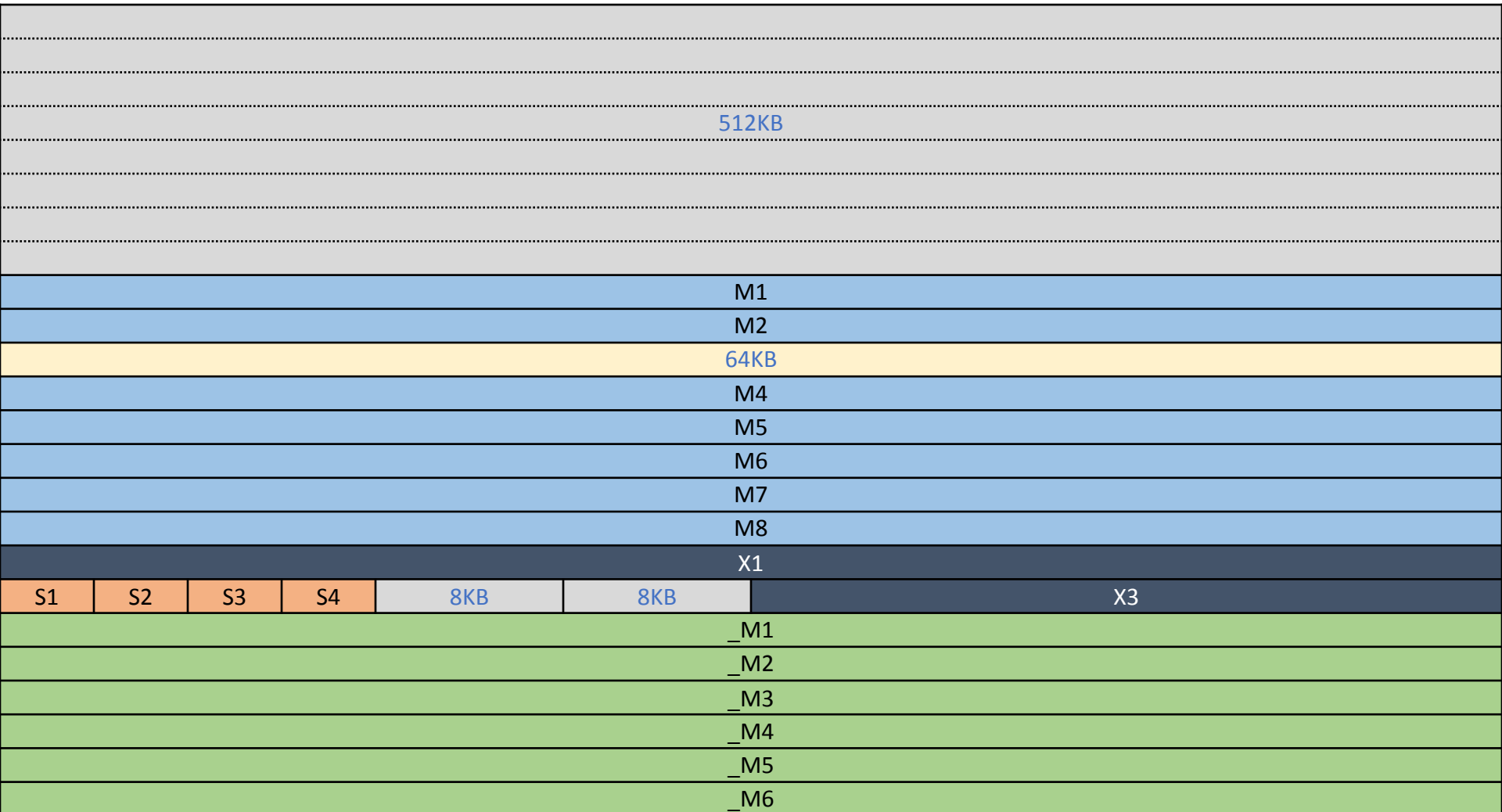
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

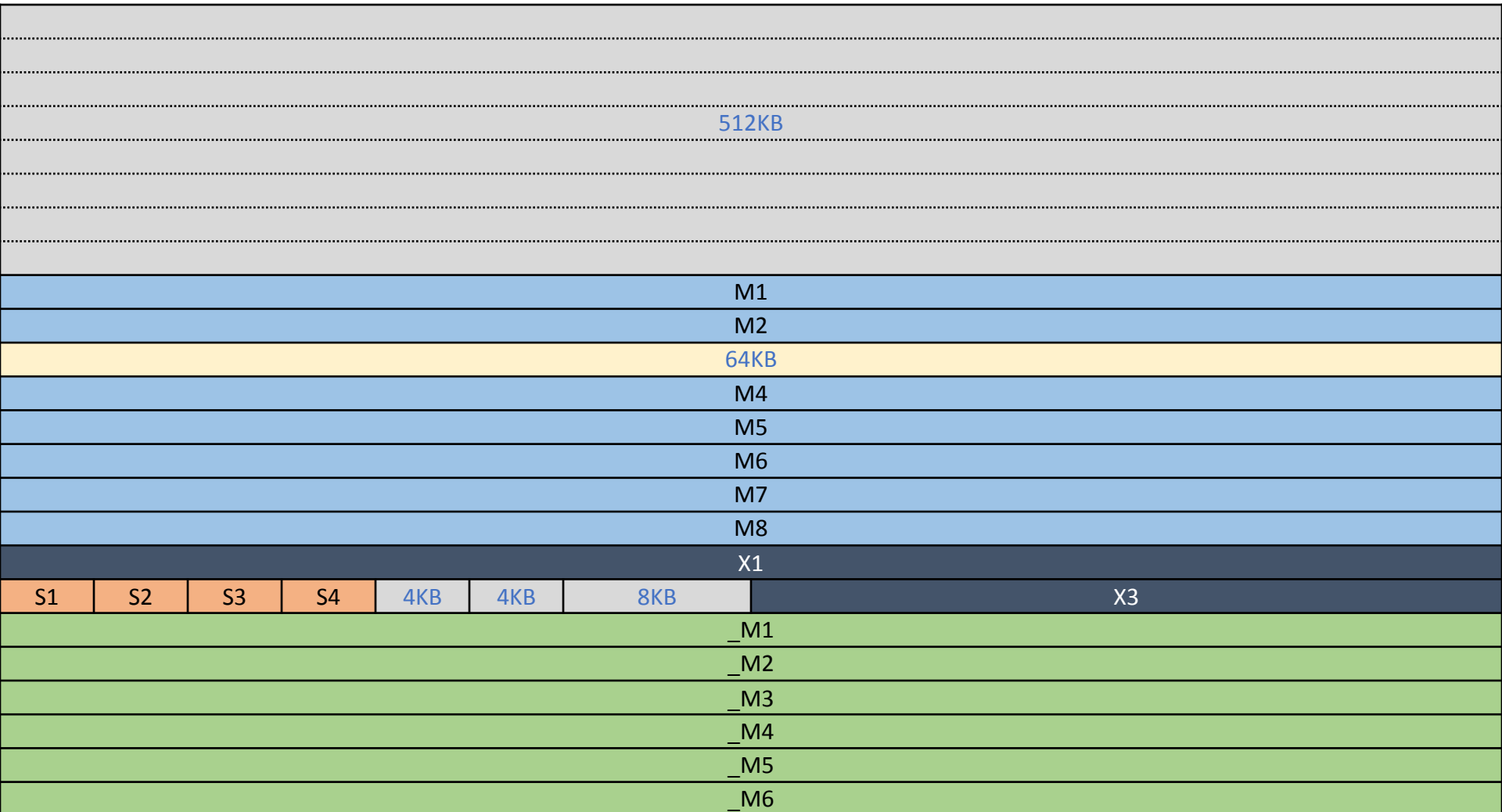
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

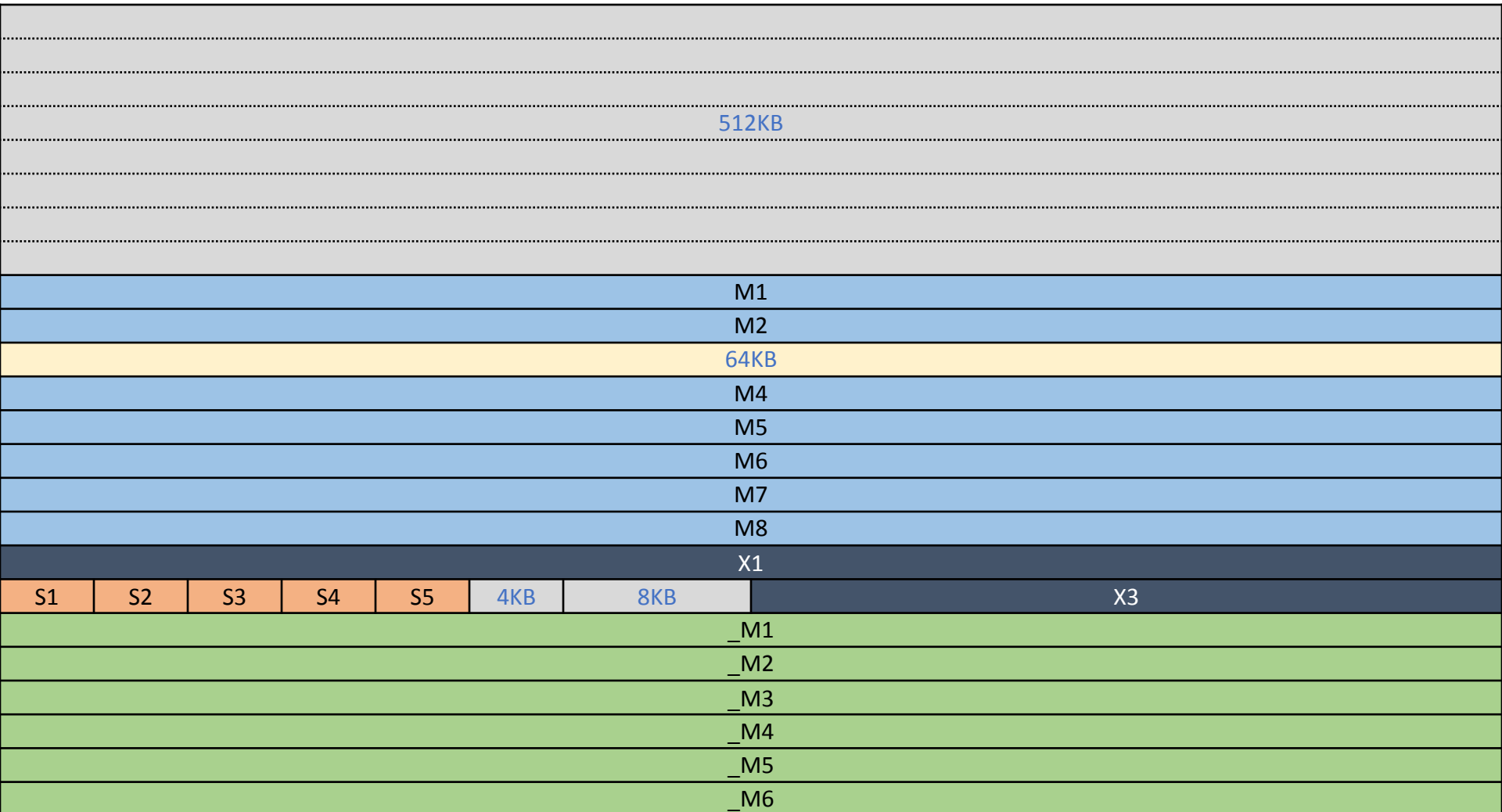
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

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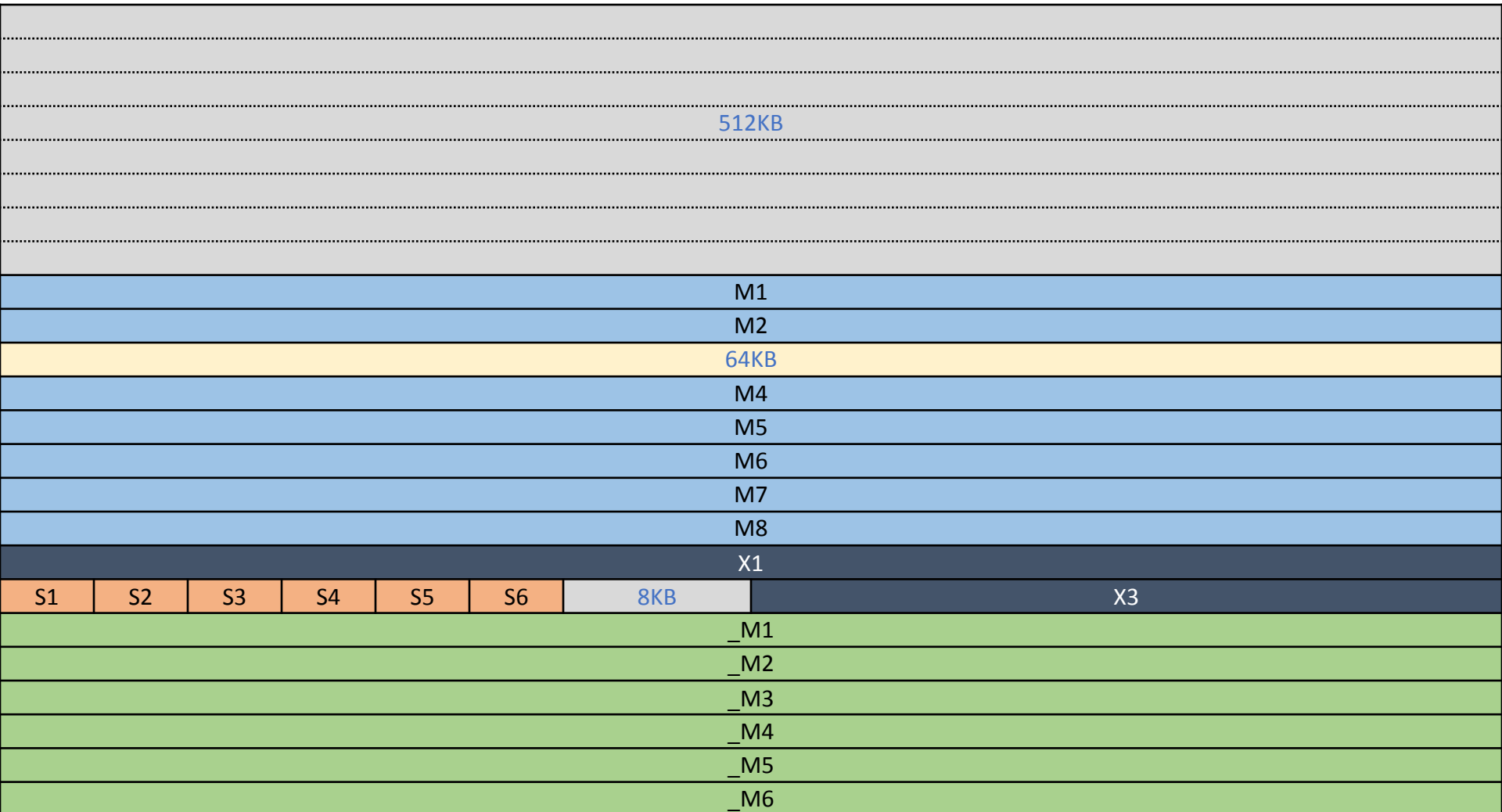
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

**Land (S) ; // allocate 4KB pages until the 64KB is used**

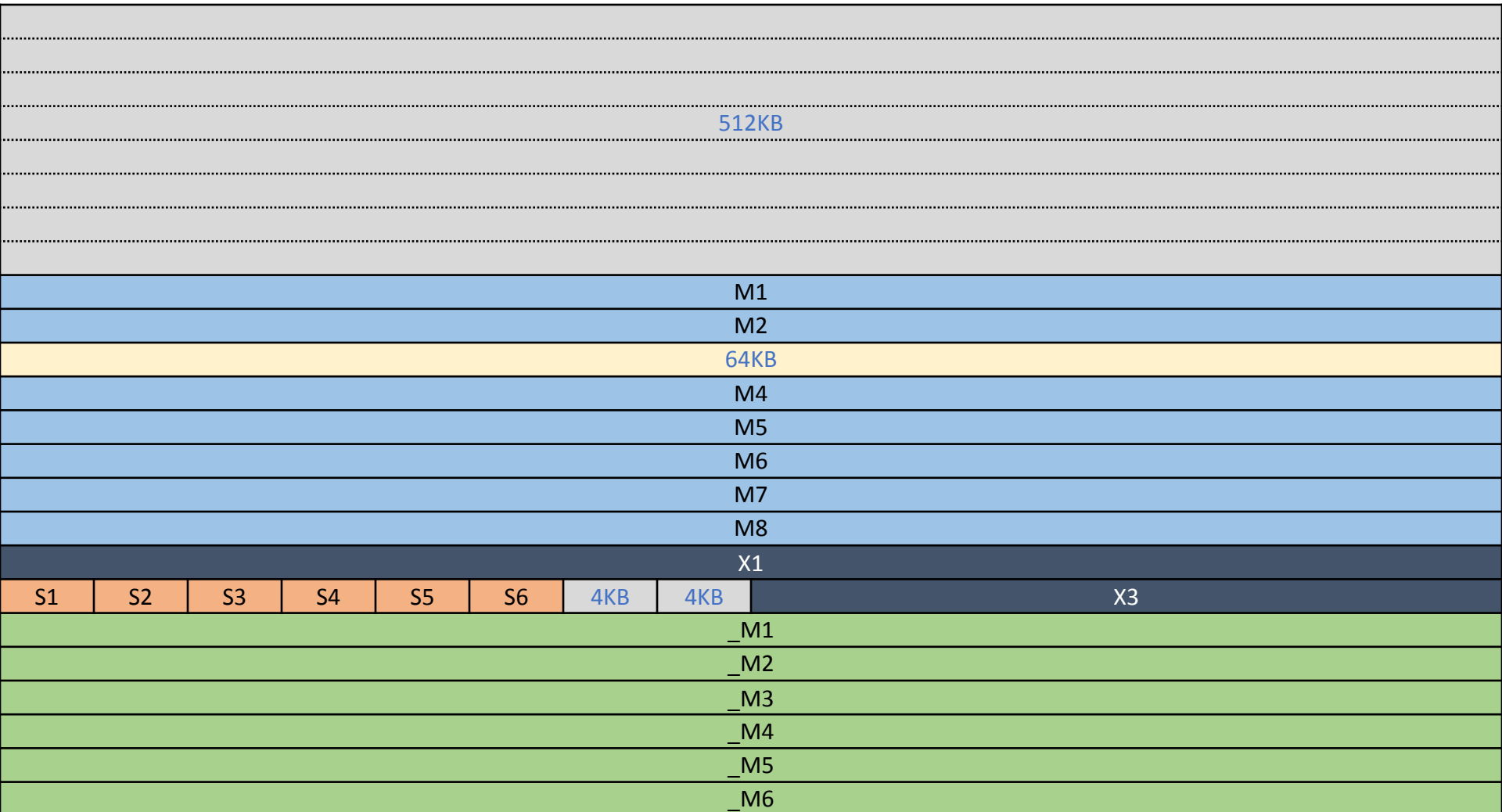




# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

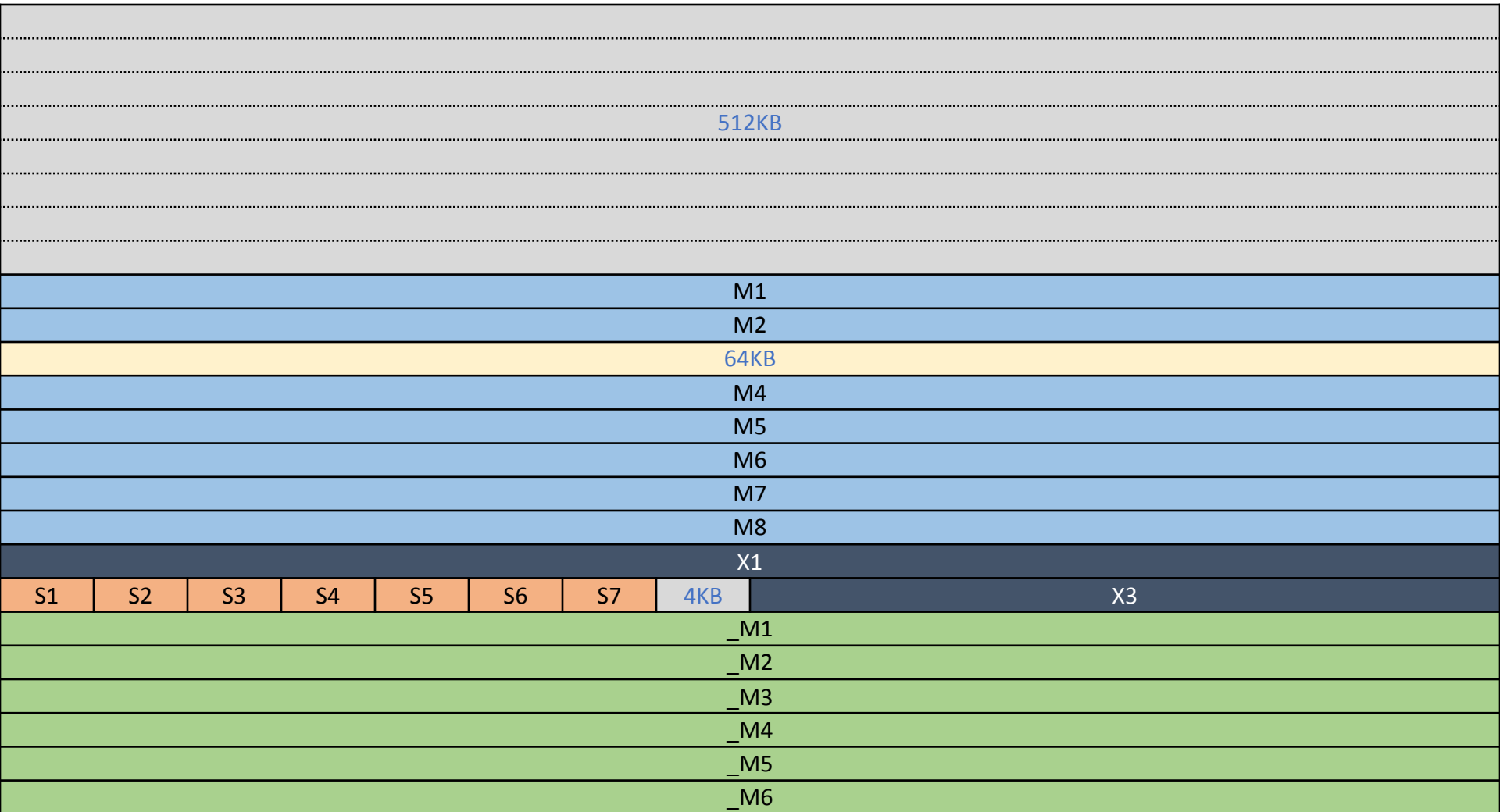
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

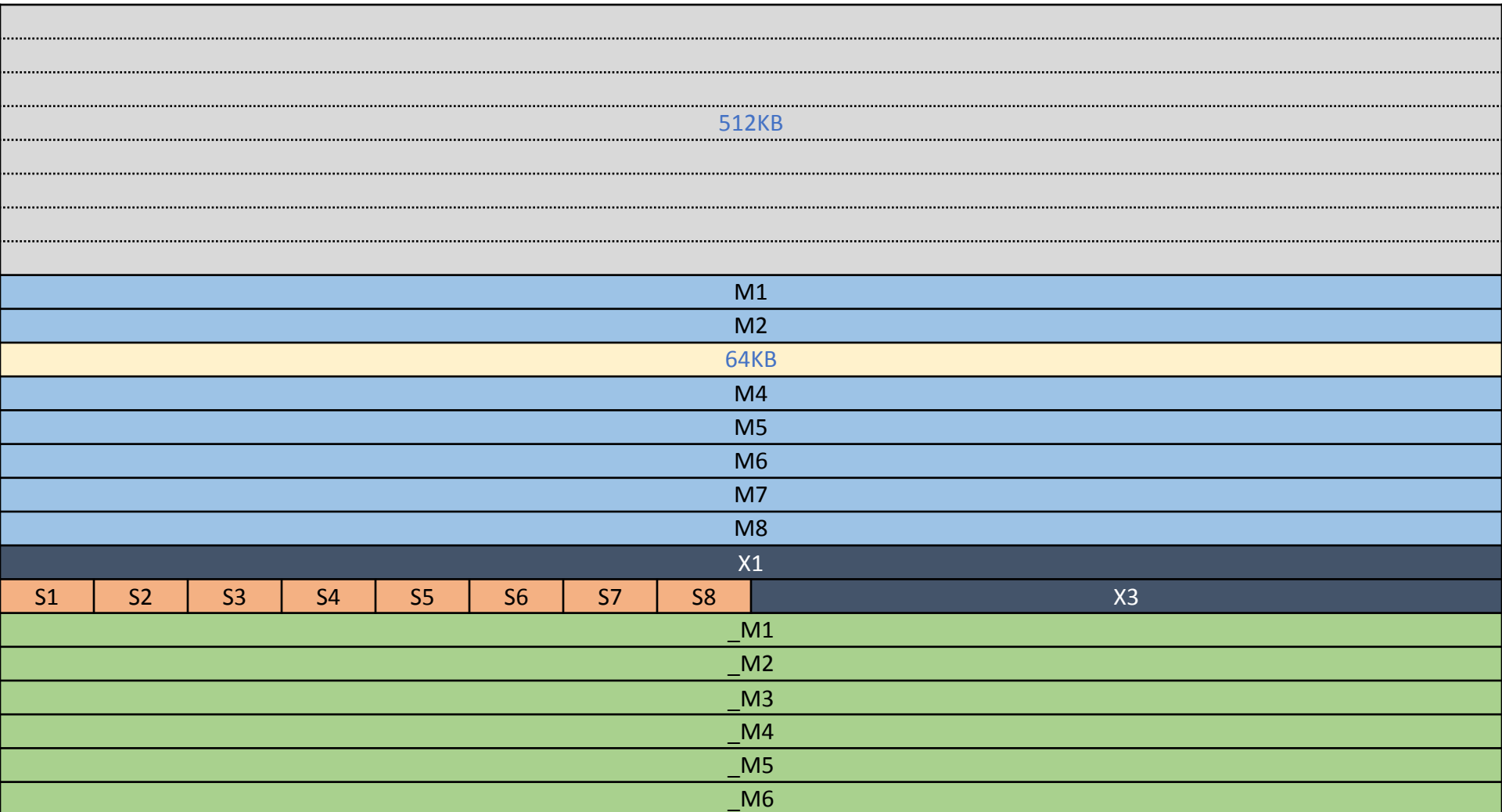
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

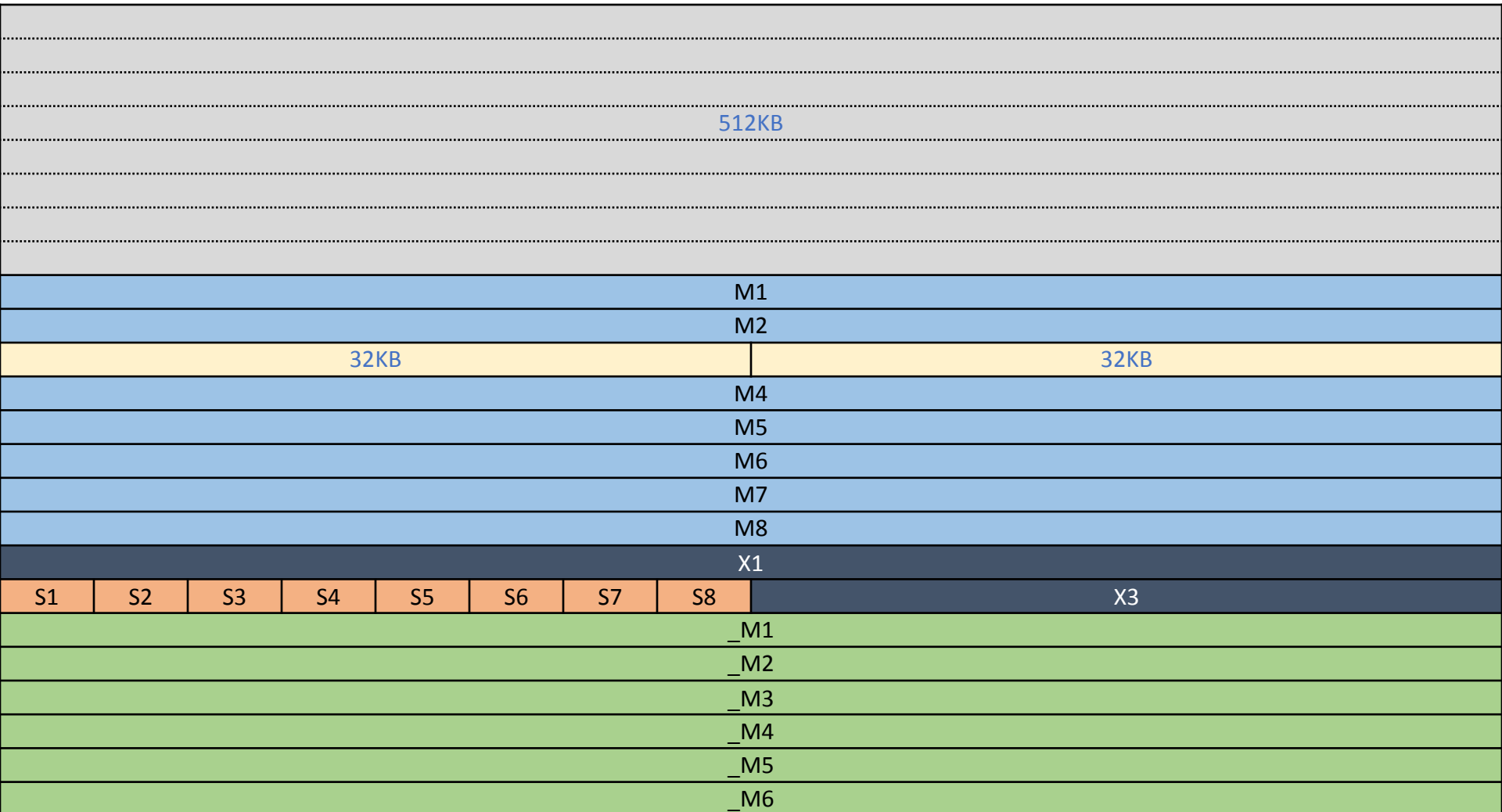
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

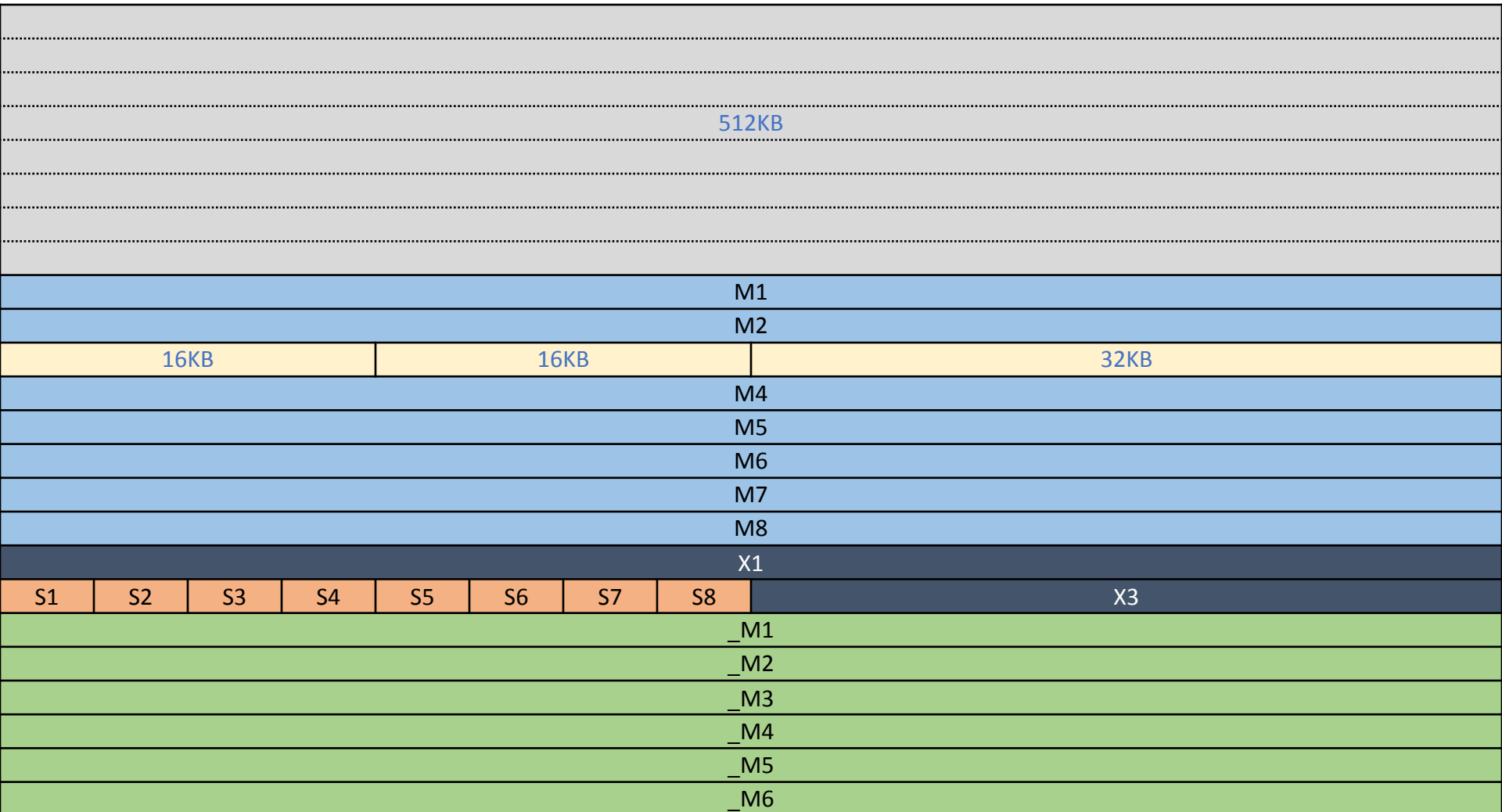
**Land (S) ; // allocate 4KB pages until the 64KB is used**



# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

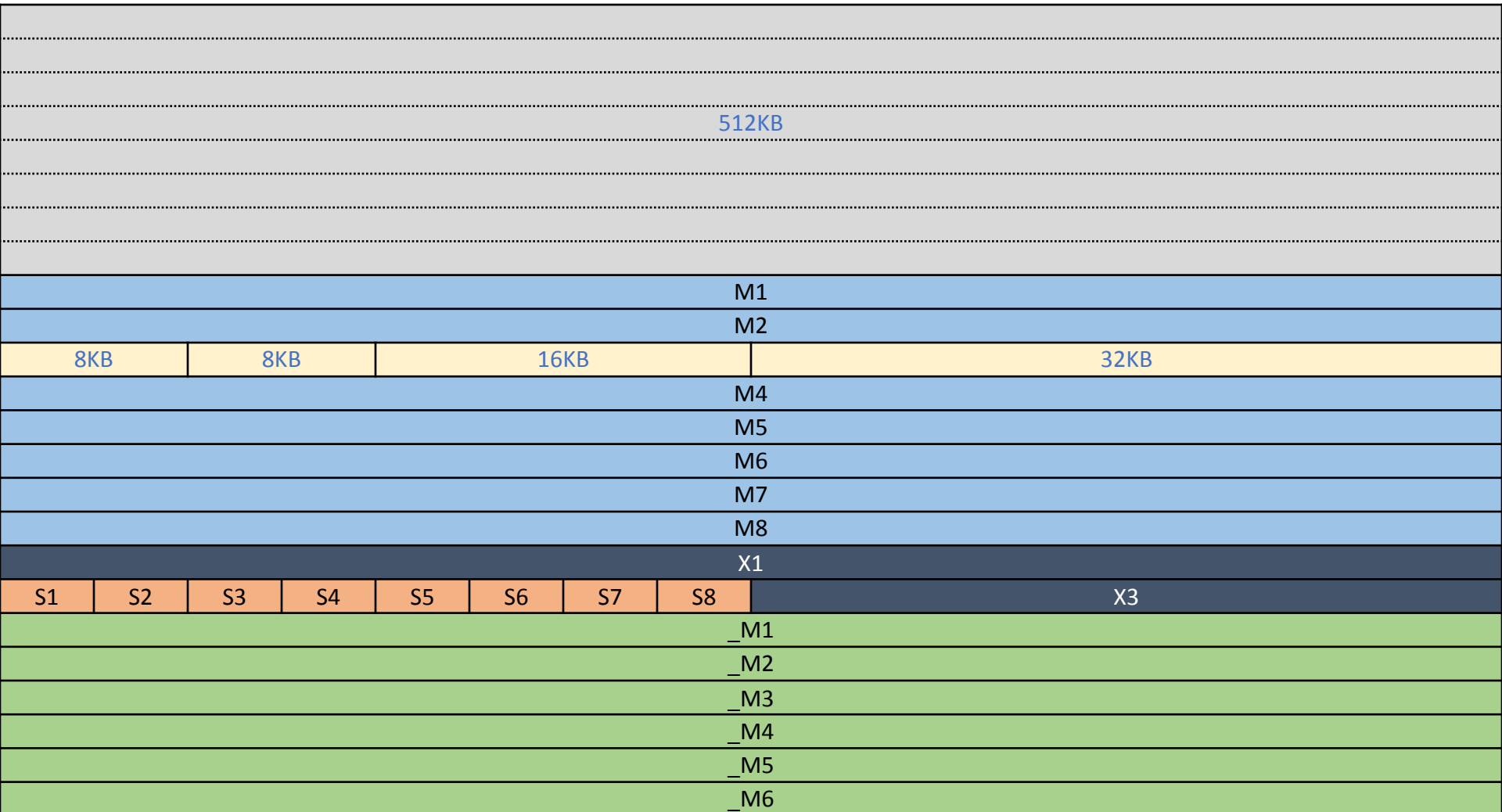
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# Phys Feng Shui step 6/8

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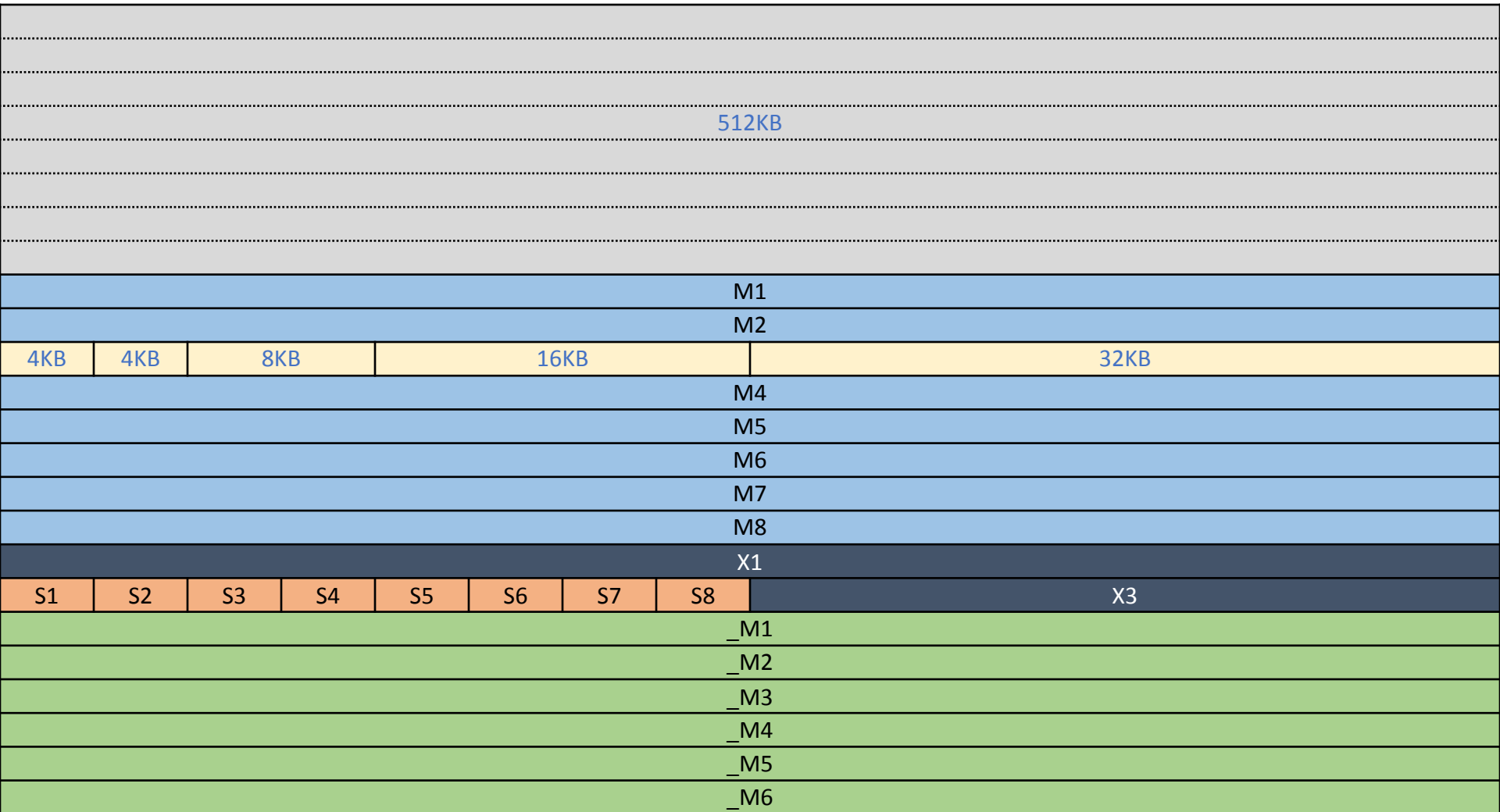
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# Phys Feng Shui step 6/8

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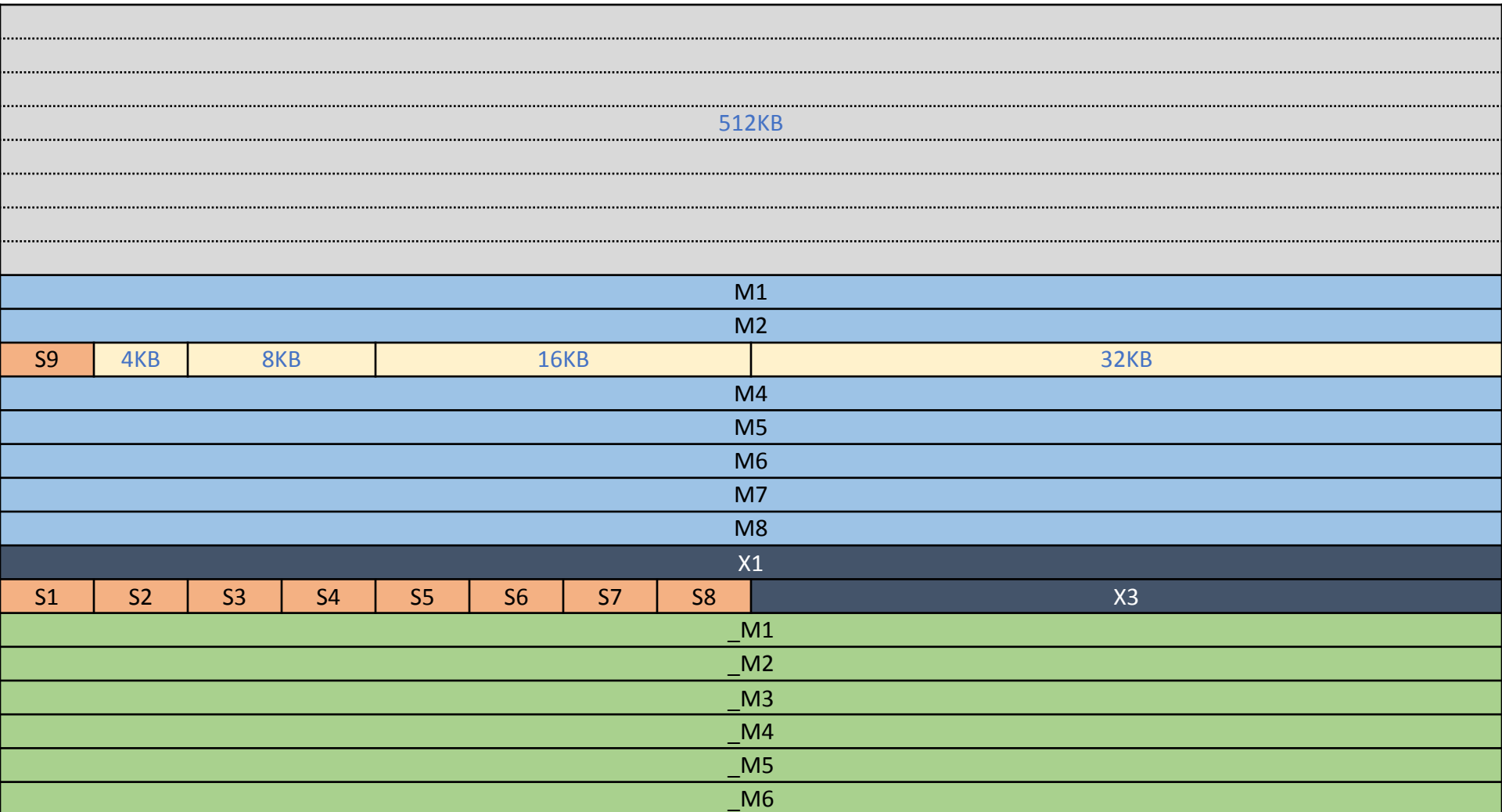
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# Phys Feng Shui step 6/8

Land a *small* chunk in the vulnerable 64 KB row

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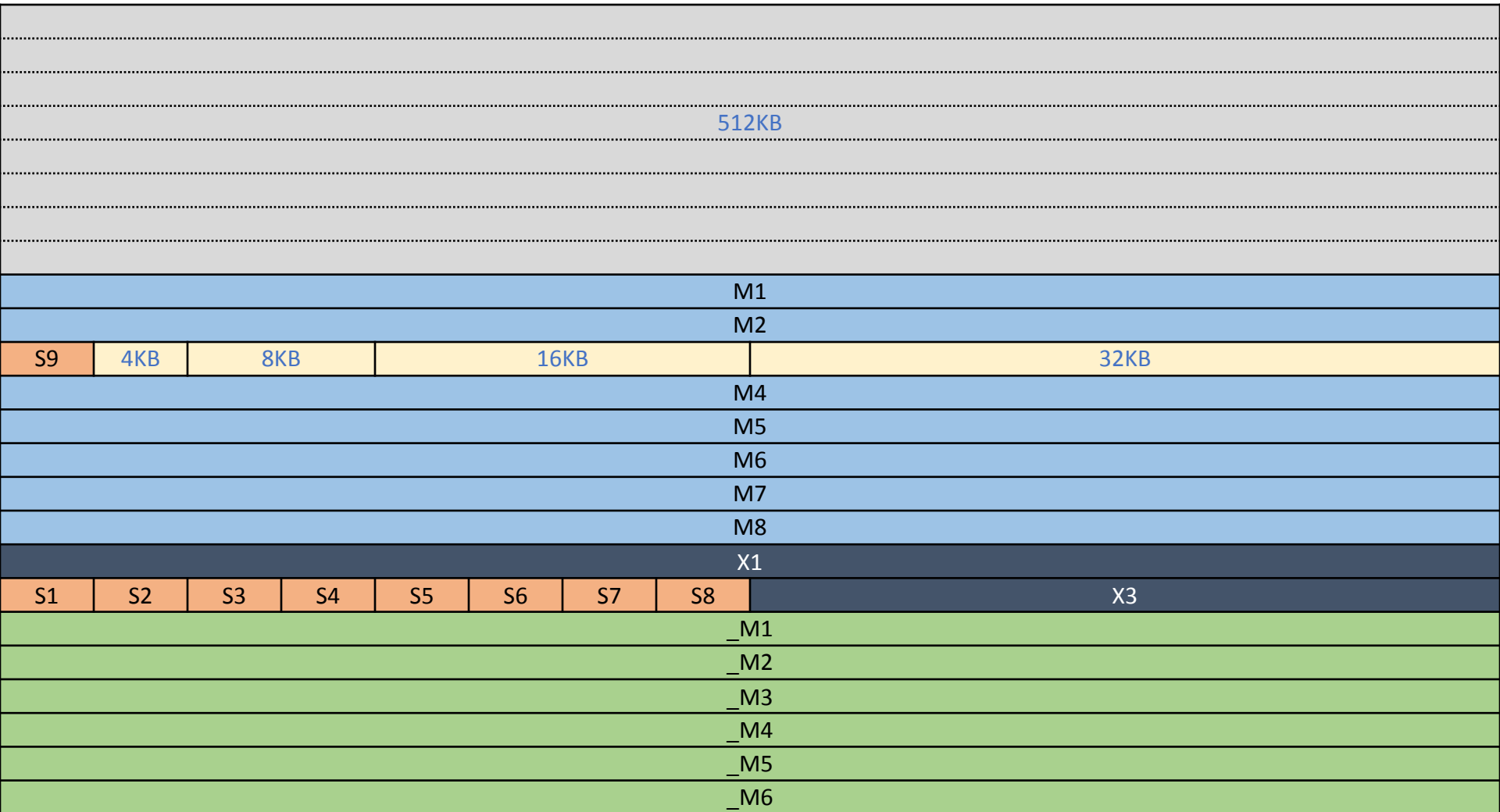




# Phys Feng Shui step 7/8

Pad *small* chunks until the vulnerable page

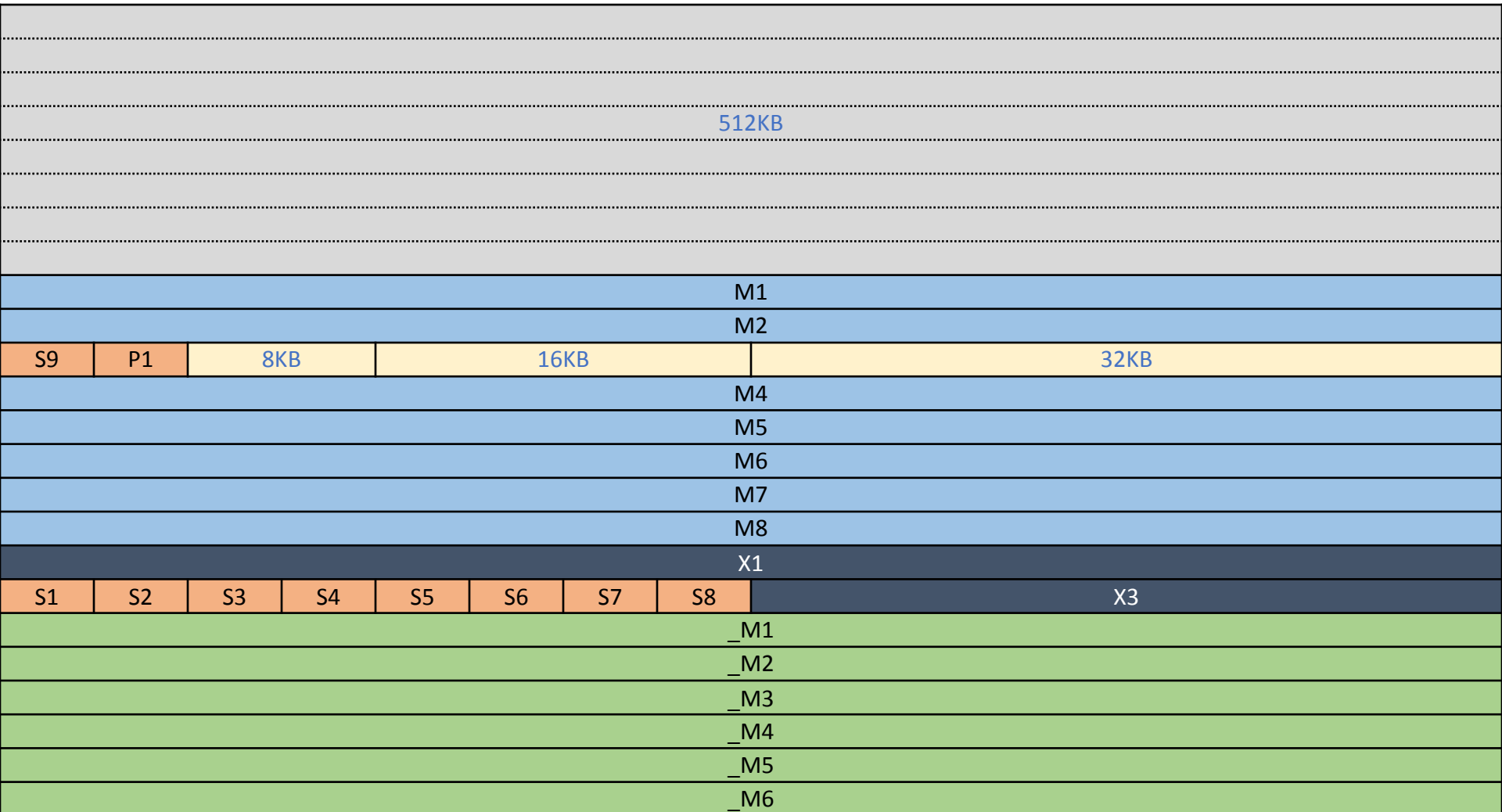
**Pad (P) ; //** insert padding until vulnerable page



# Phys Feng Shui step 7/8

Pad *small* chunks until the vulnerable page

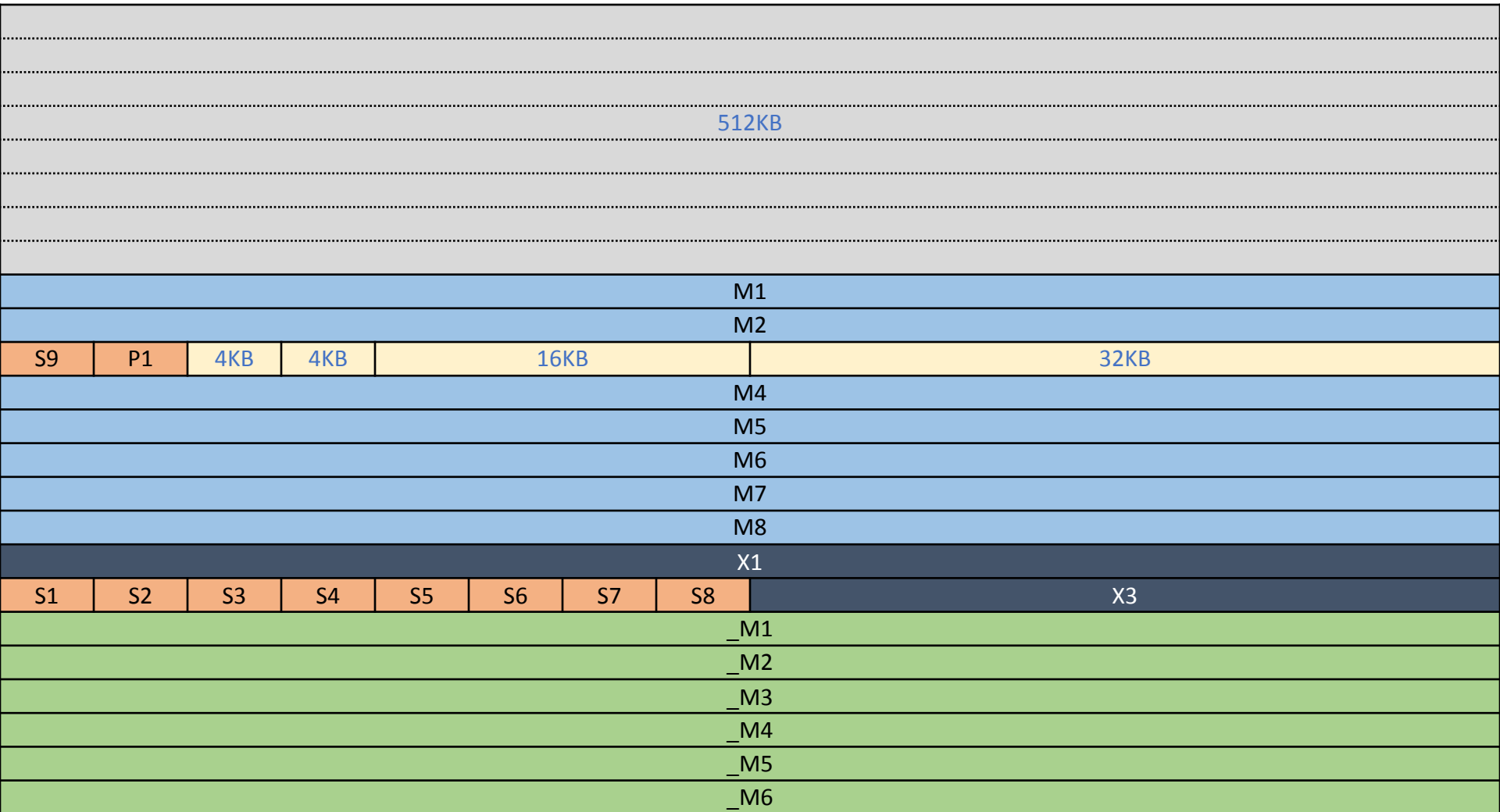
**Pad (P) ; //** insert padding until vulnerable page



# Phys Feng Shui step 7/8

Pad *small* chunks until the vulnerable page

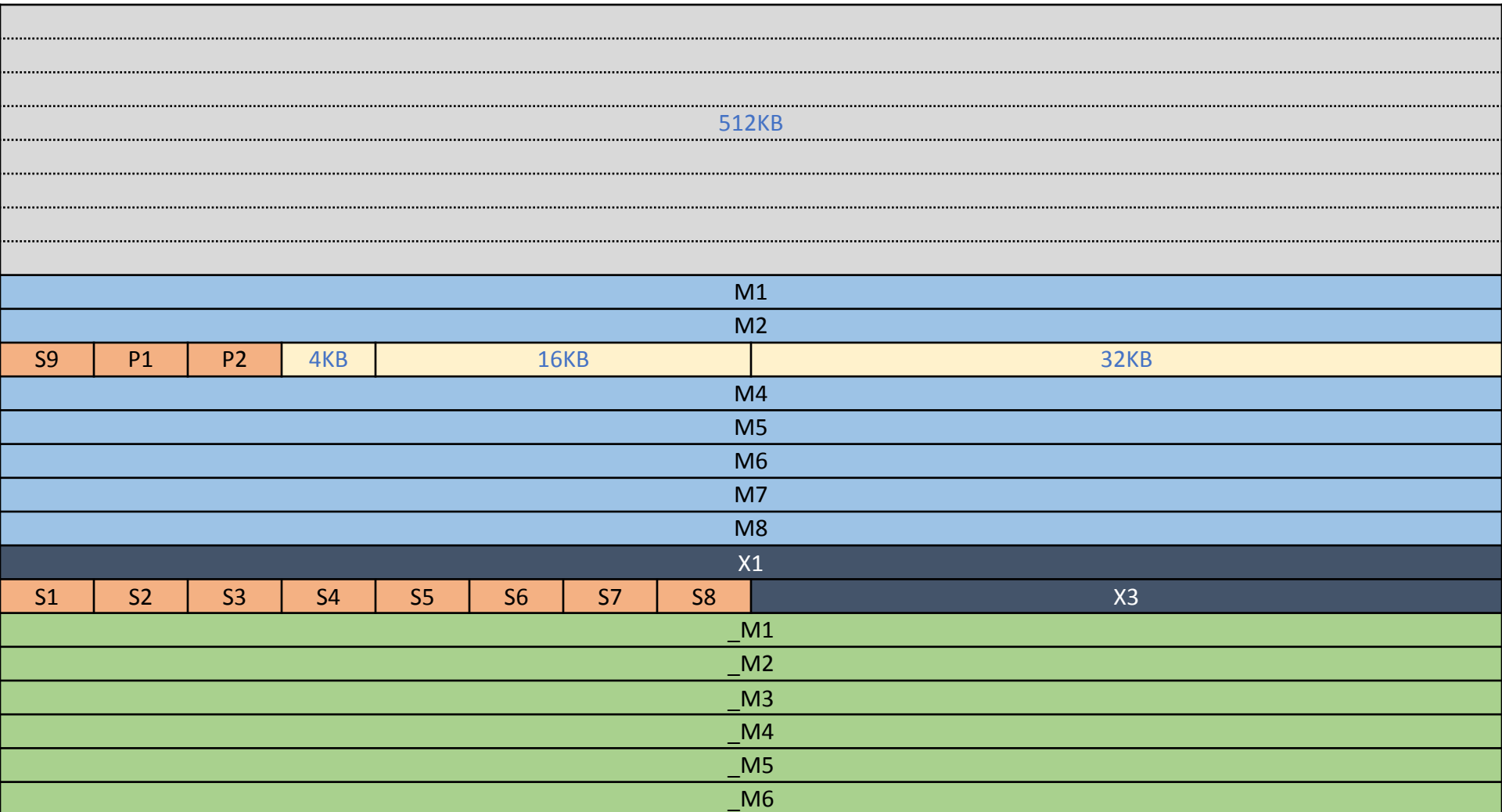
**Pad (P) ; //** insert padding until vulnerable page



# Phys Feng Shui step 7/8

Pad *small* chunks until the vulnerable page

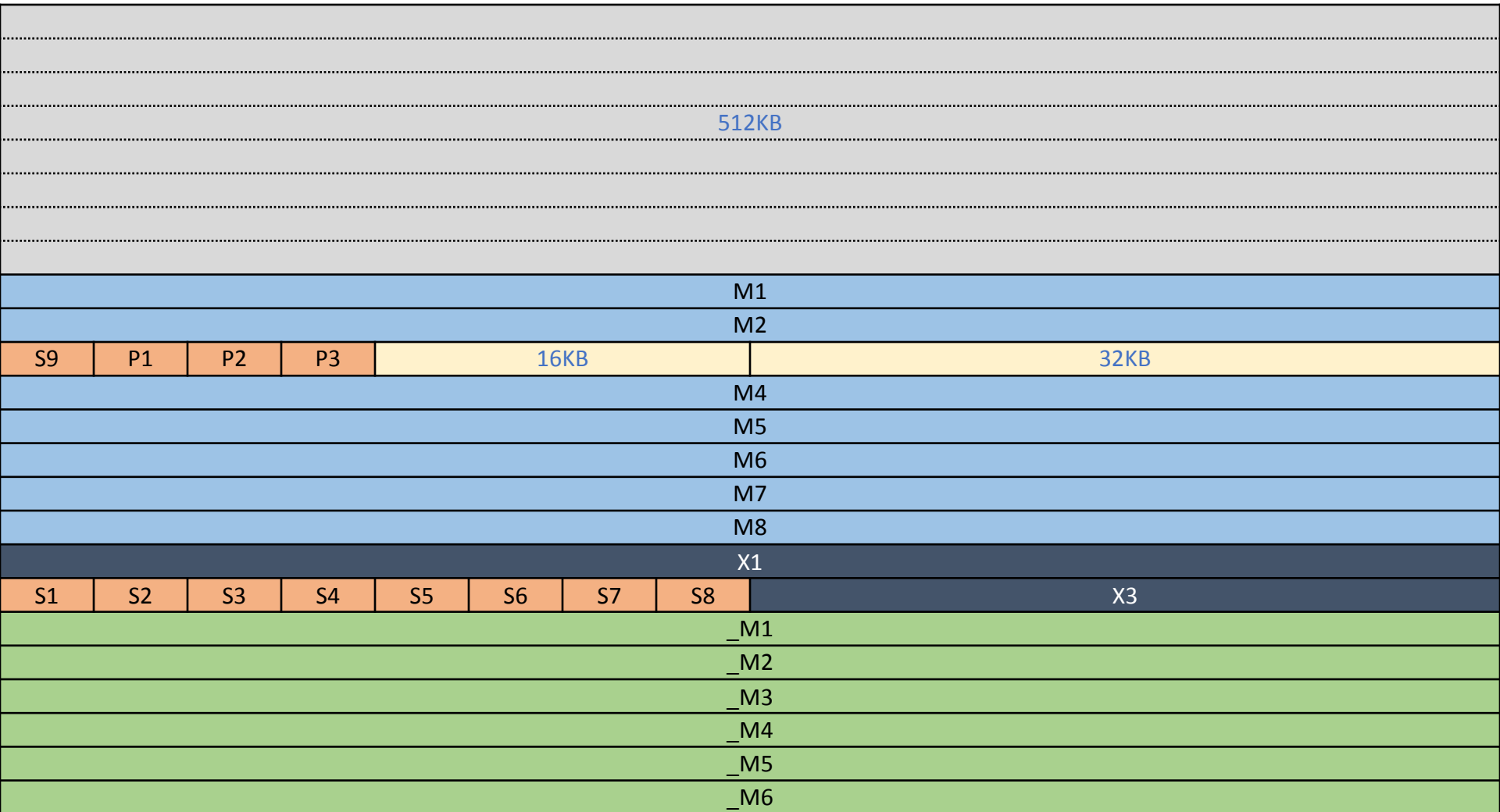
**Pad(P) ; //** insert padding until vulnerable page



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Pad *small* chunks until the vulnerable page

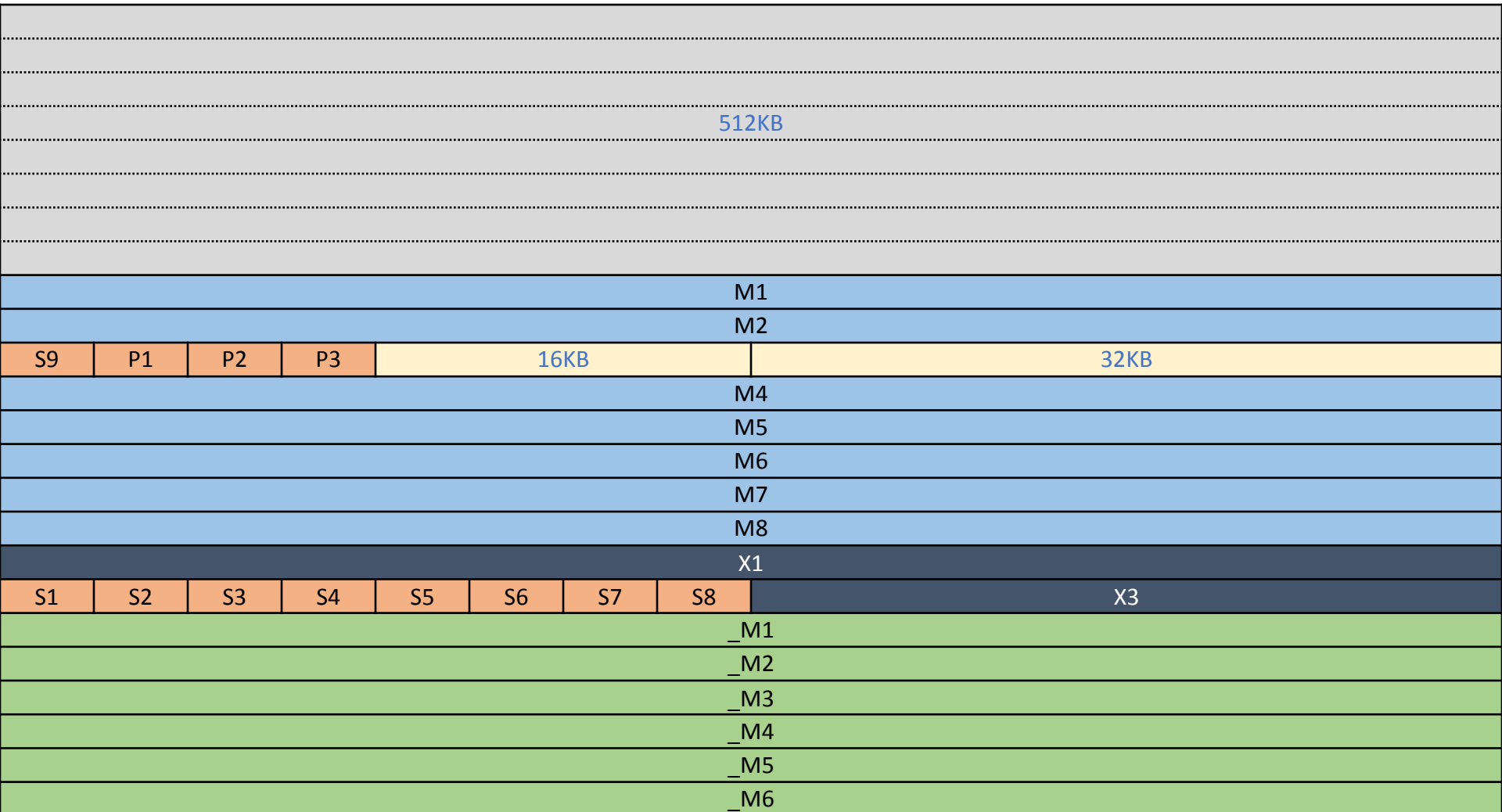
**Pad(P) ; //** insert padding until vulnerable page



# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE

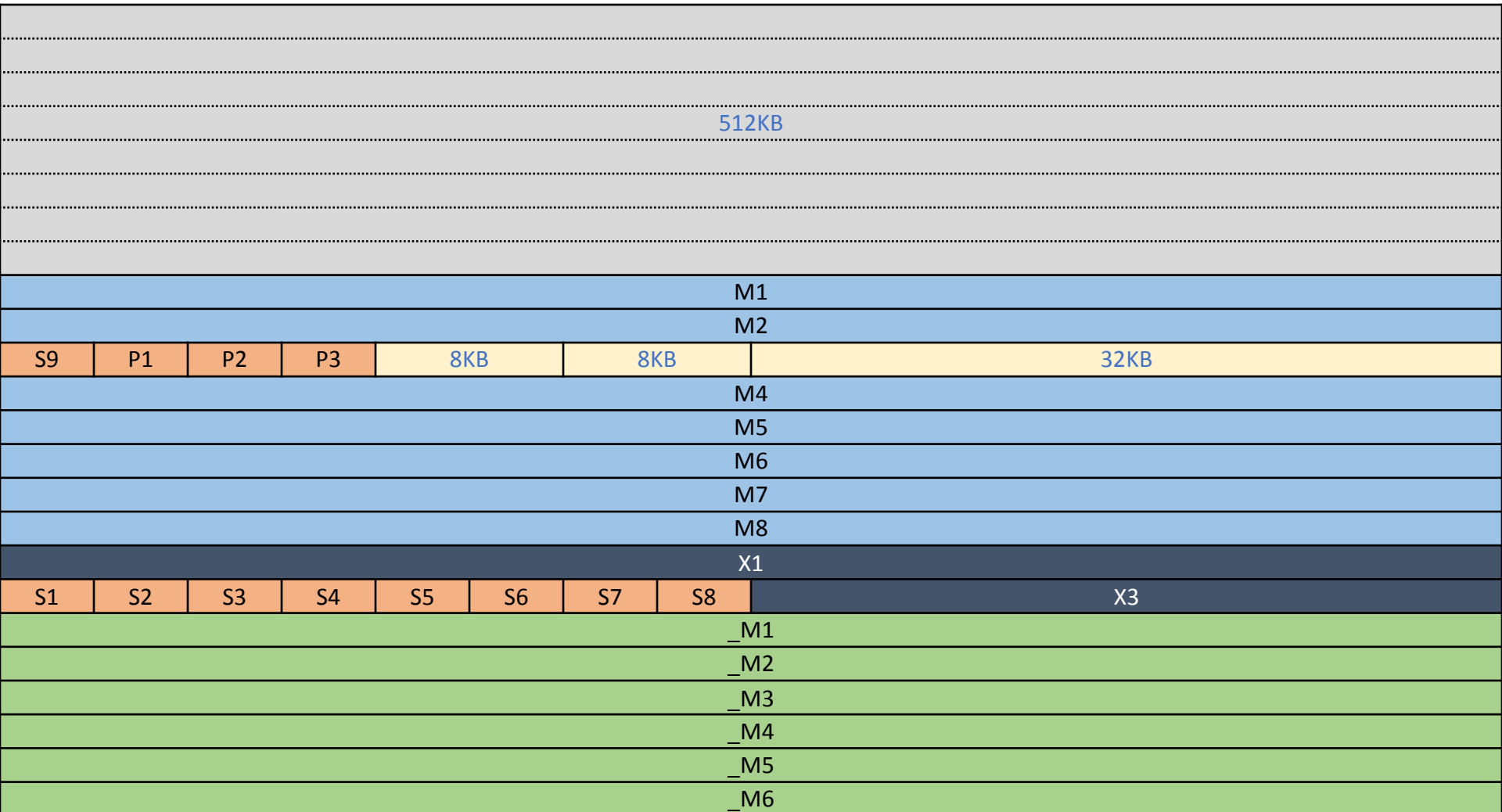
```
PT = mmap(MAP_FIXED); // Force a Page Table allocation
```



# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE

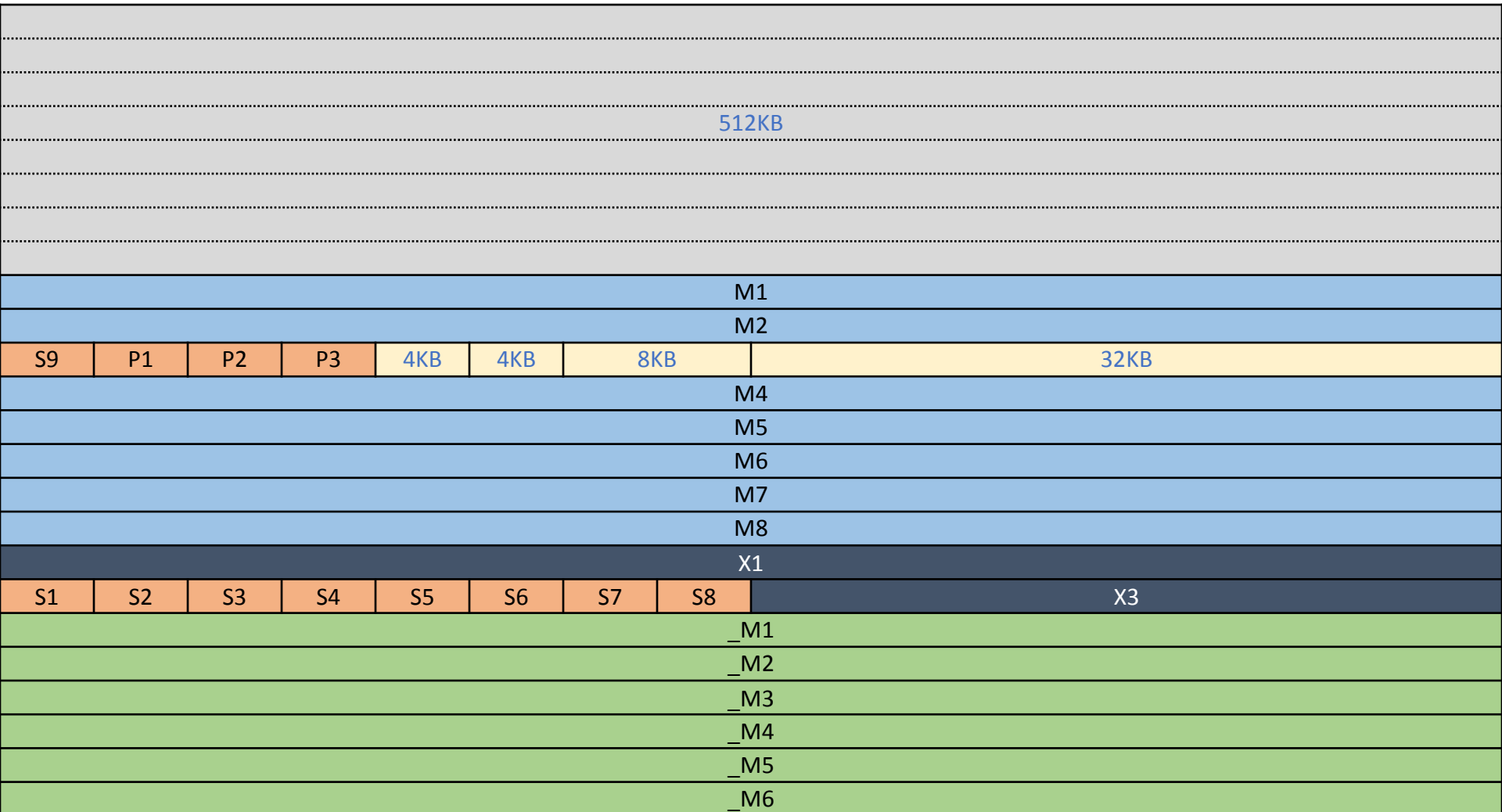
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# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE

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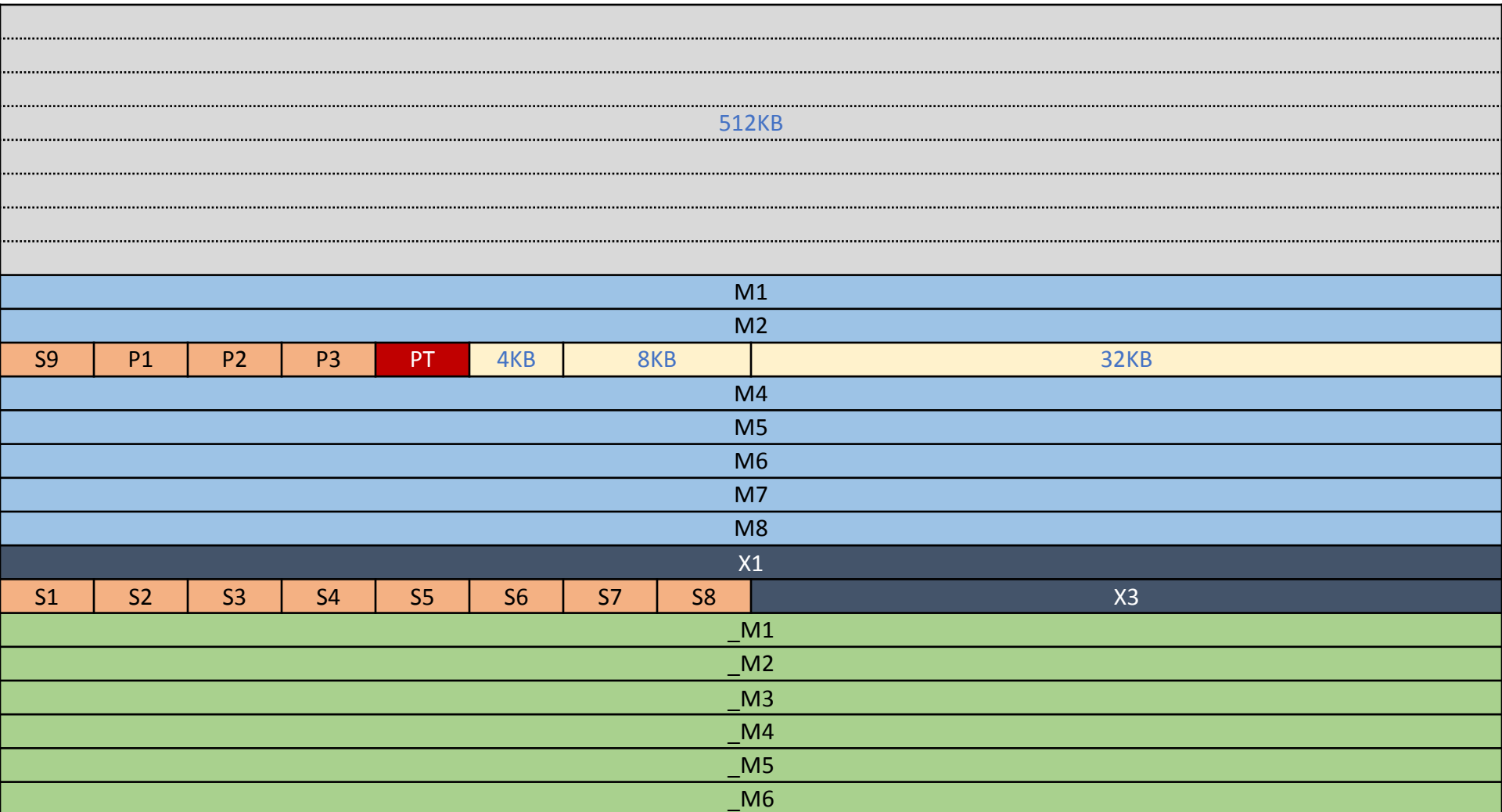




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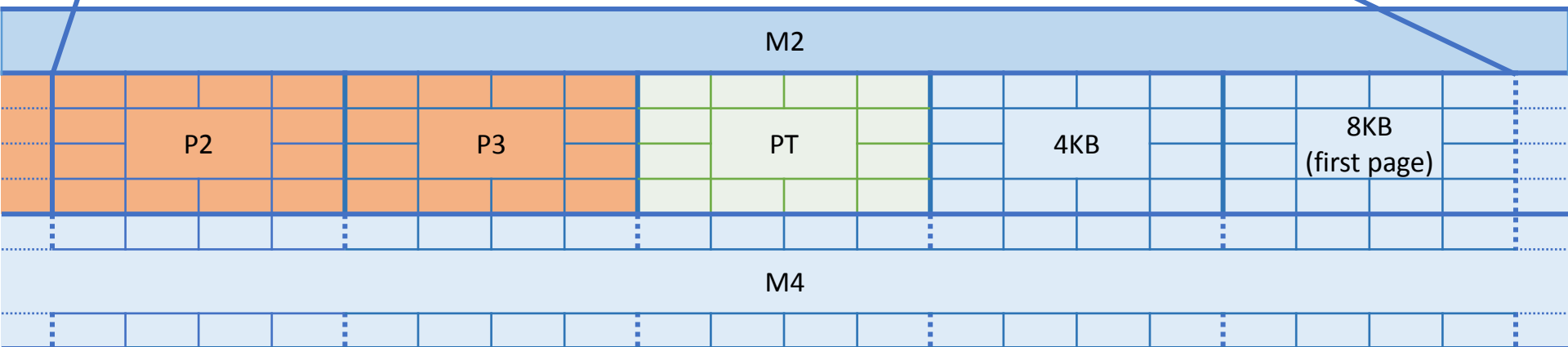
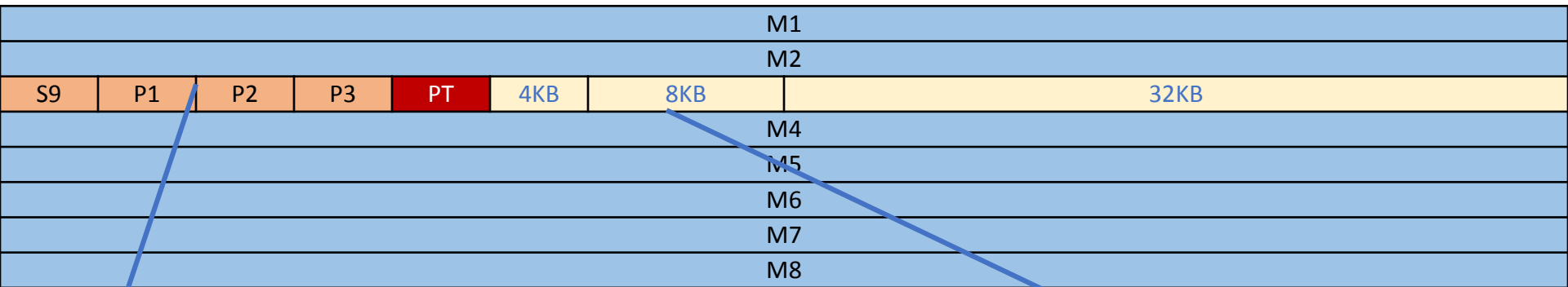
# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE

							M1
							M2
S9	P1	P2	P3	PT	4KB	8KB	32KB
							M4
							M5
							M6
							M7
							M8

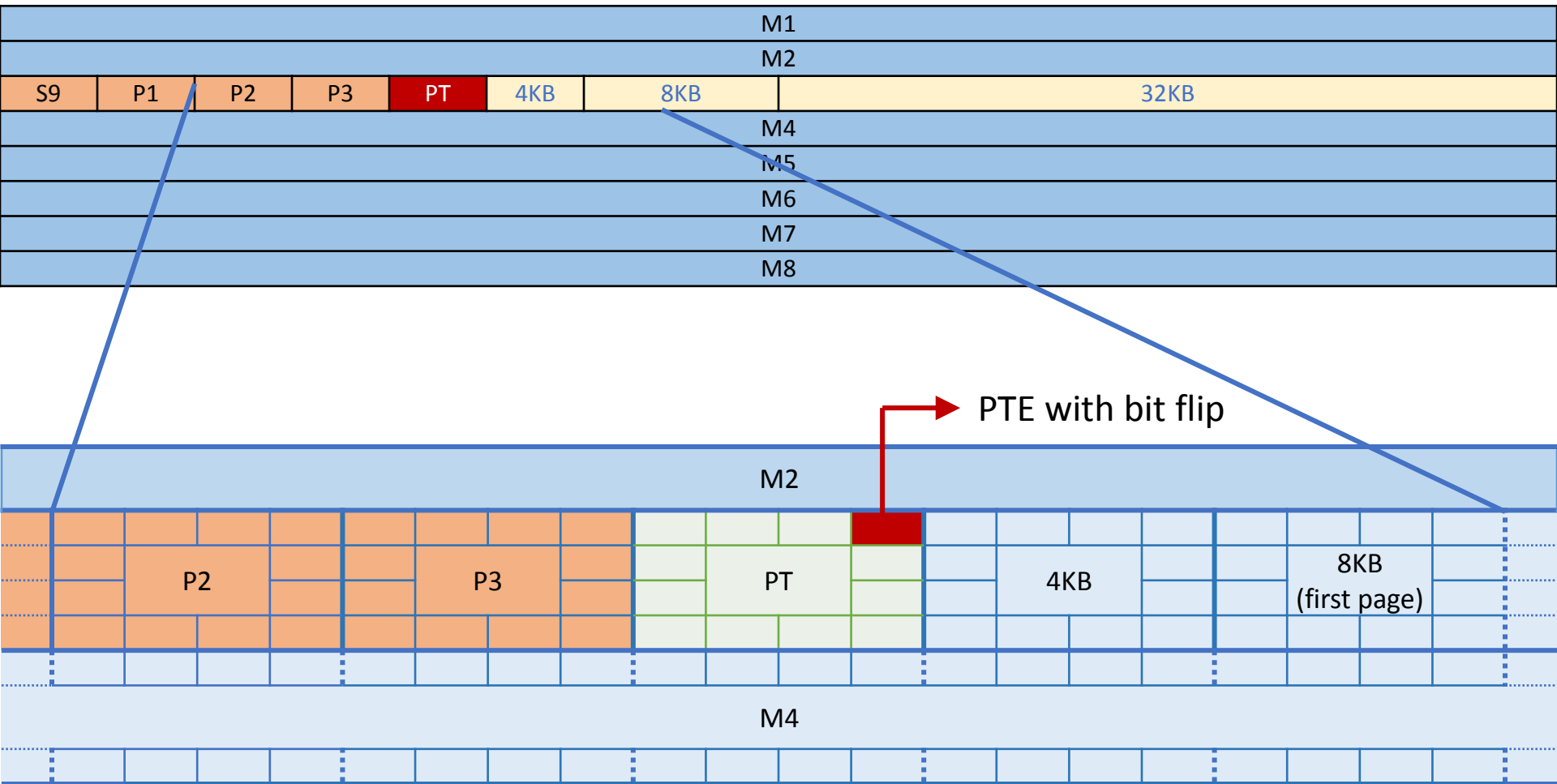
# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE



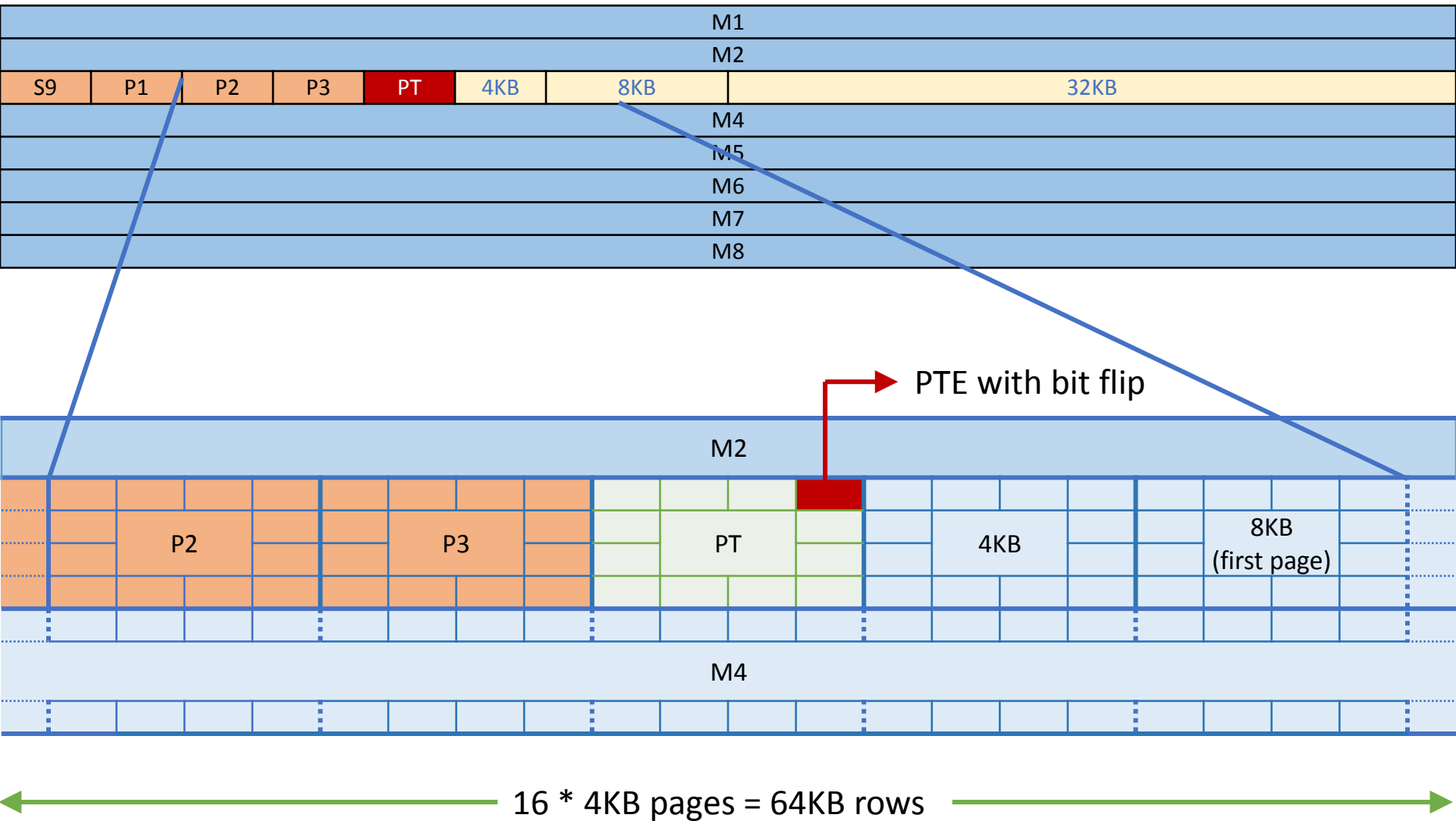
# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE



# Phys Feng Shui step 8/8

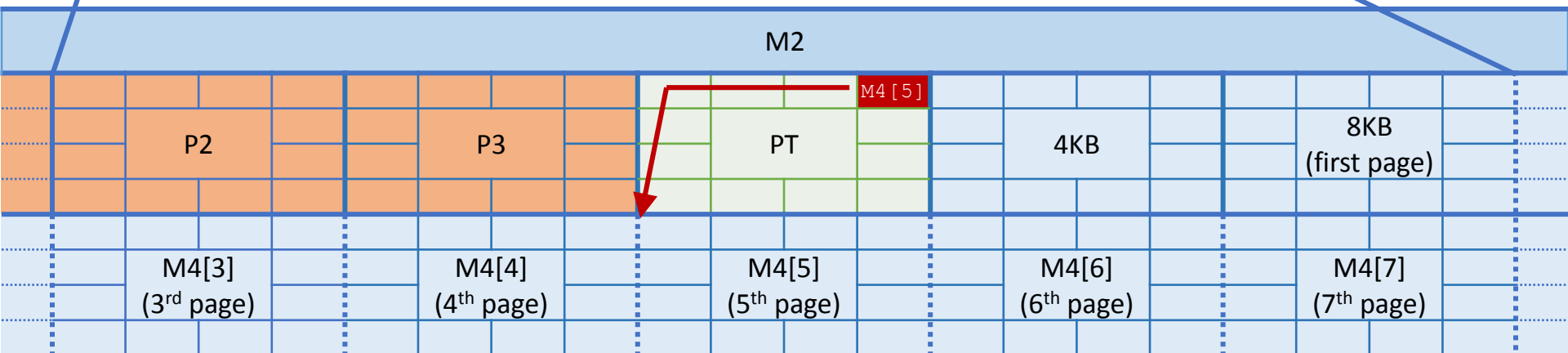
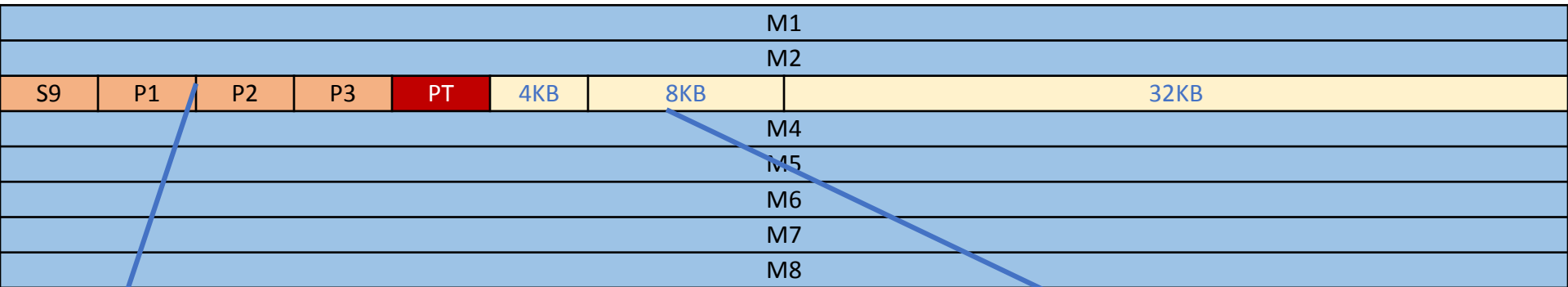
Force a Page Table allocation + map the vulnerable PTE



# Phys Feng Shui step 8/8

Force a Page Table allocation + map the vulnerable PTE

```
mmap (M4 [5], MAP_FIXED); // map vulnerable PTE 64KB 'away'
```



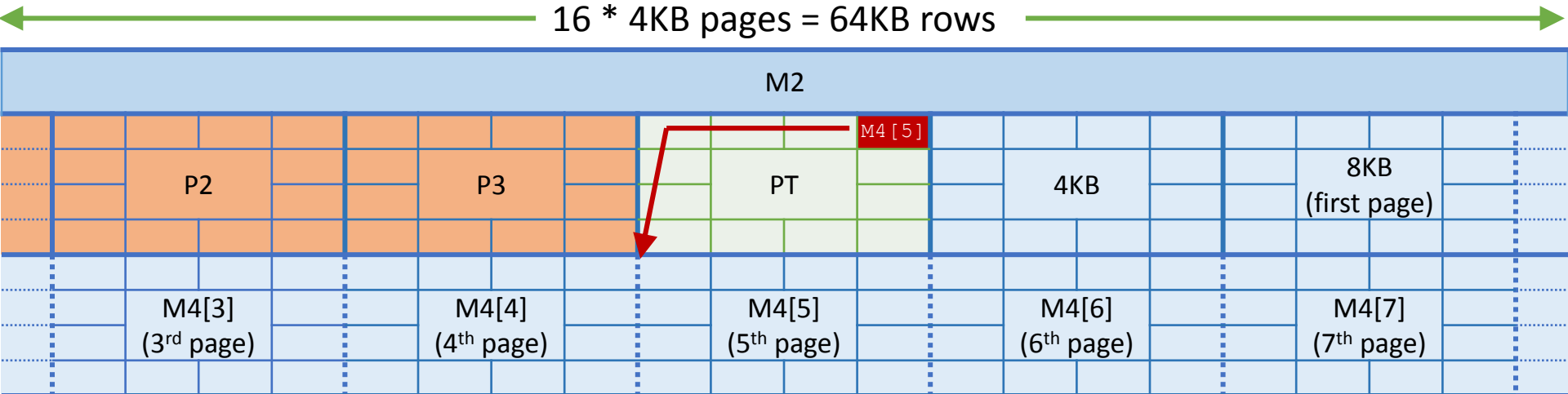
16 \* 4KB pages = 64KB rows

# Overview

1. Memory Templating  
Scan memory for useful bit flips
2. Land a Page Table  
Store a page table on a vulnerable page
3. **Reproduce the bit flip**  
Modify the data structure and get root acces

# Drammer

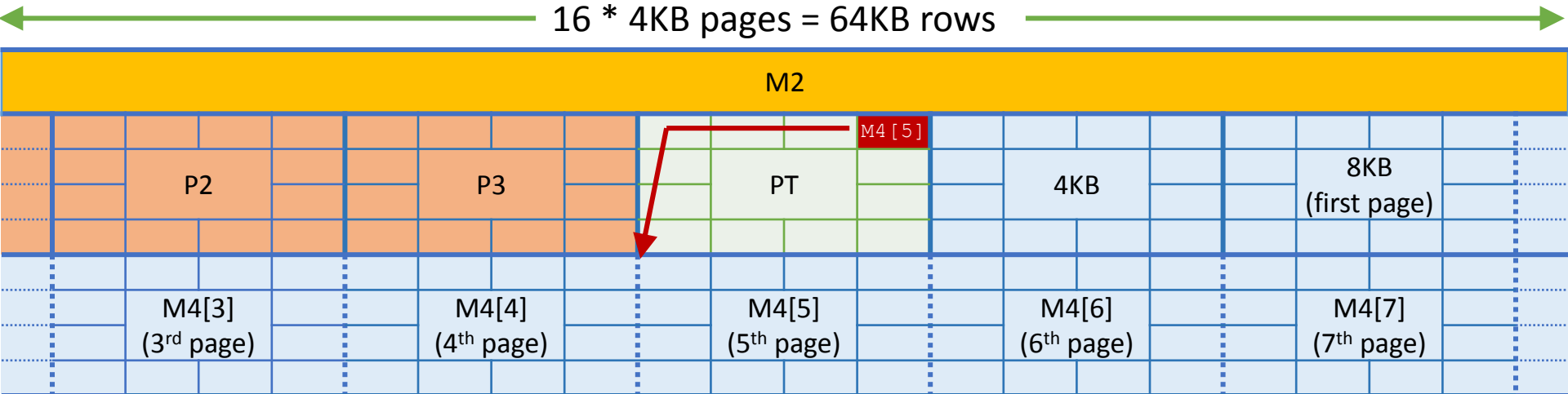
Perform double-sided rowhammer to flip a bit in the PTE





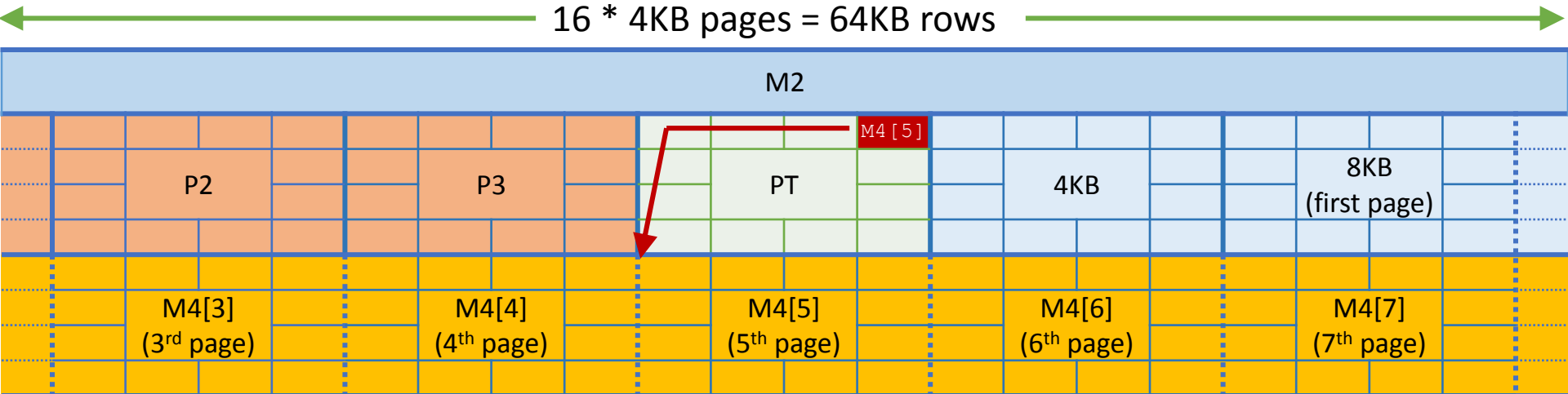
# Drammer

Perform double-sided rowhammer to flip a bit in the PTE



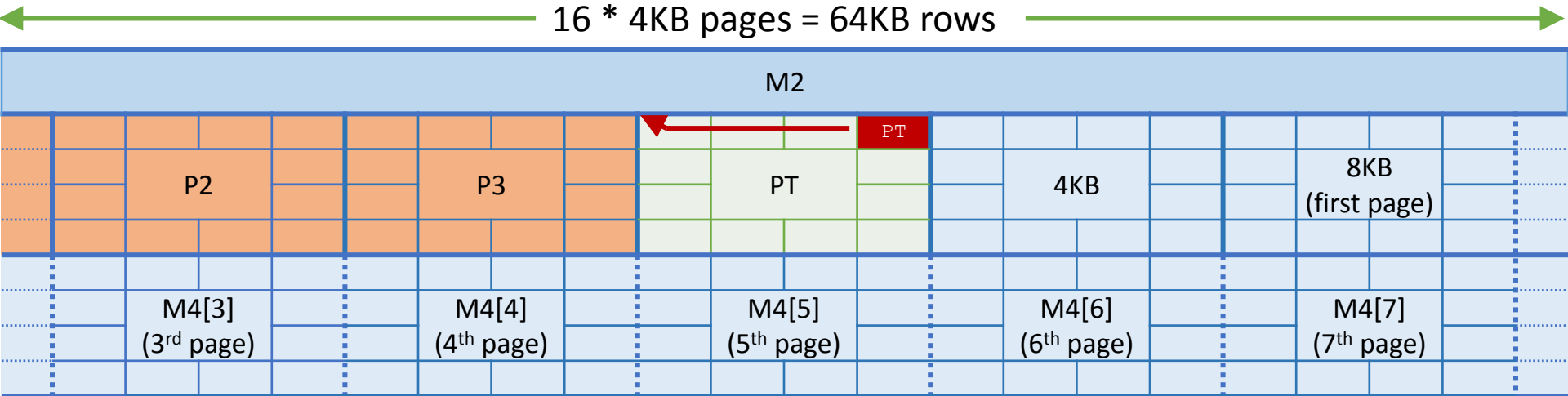
# Drammer

Perform double-sided rowhammer to flip a bit in the PTE



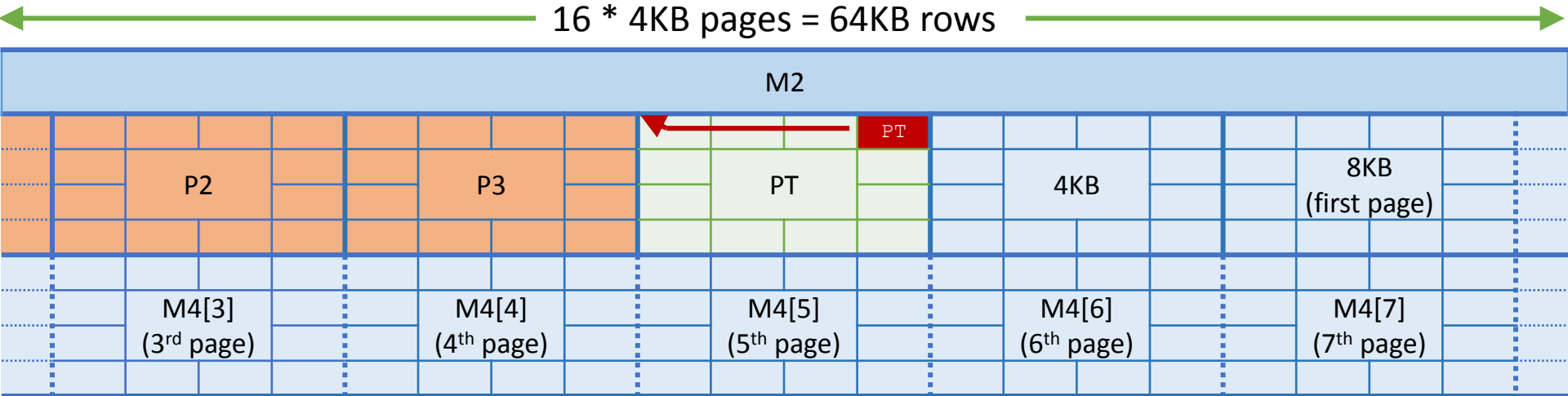
# Drammer

## Write access to a Page Table



# Drammer

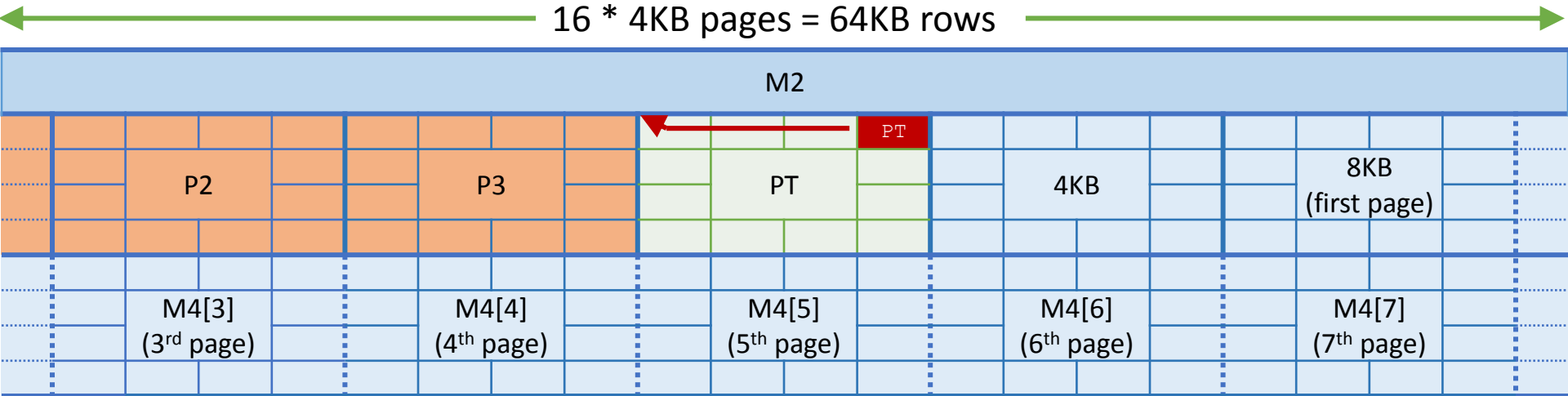
## Write access to a Page Table



1. Fill PT with Page Table Entries to kernel memory

# Drammer

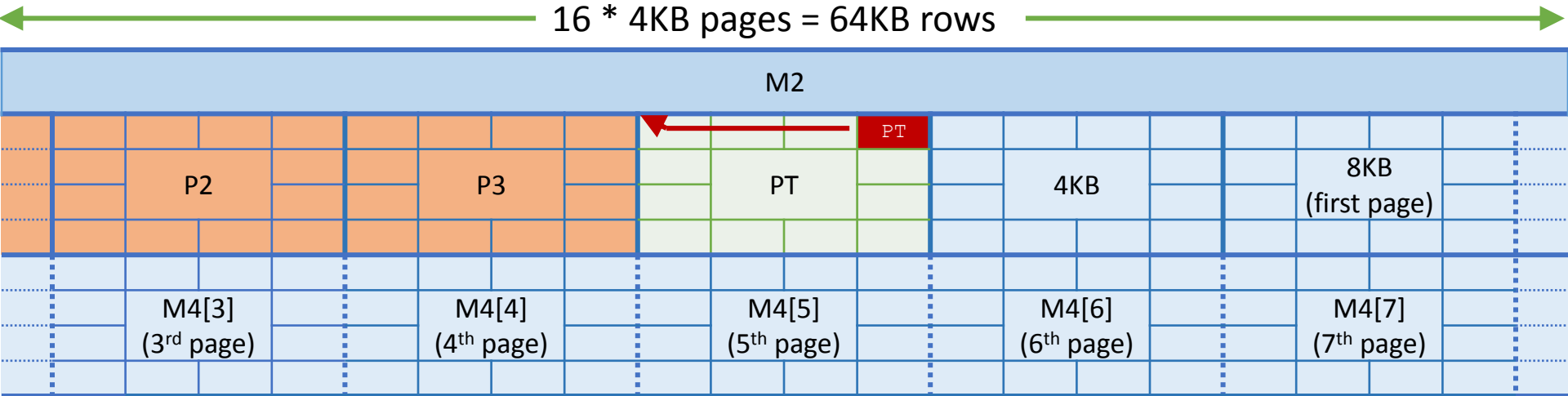
## Write access to a Page Table



1. Fill PT with Page Table Entries to kernel memory
2. Search kernel memory for our `struct cred`

# Drammer

## Write access to a Page Table



1. Fill PT with Page Table Entries to kernel memory
2. Search kernel memory for our `struct cred`
3. Overwrite our `uid` and `gid` to get root privileges

# Evaluation

Device	#flips	1 <sup>st</sup> exploitable flip after
<b>LG Nexus 5<sup>1</sup></b>	1058	116s
<b>LG Nexus 5<sup>4</sup></b>	0	-
<b>LG Nexus 5<sup>5</sup></b>	747,013	1s
<b>LG Nexus 4</b>	1,328	7s
<b>OnePlus One</b>	3,981	942s
<b>Motorola Moto G (2013)</b>	429	441s
<b>LG G4 (ARMv8 – 64-bit)</b>	117,496	5s

Bit flips on 18 out of 27 tested devices

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After the 1<sup>st</sup> exploitable flip, exploitation takes **at most 22 seconds**

Drammer test app reported bit flips on:

**Google Pixel, OnePlus 3, Galaxy Note 7, HTC One M8, ...**

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Contacted Google with a list of suggested mitigations on July 25

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*“What if we support you financially?”*



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But now it does

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Rewarded \$4000 for a *critical* issue

Partial hardening in November’s updates

*“We will continue to work on a longer term solution”*

# Conclusion

- Deterministic Rowhammer exploitation
- **No special memory management features required (e.g., deduplication)**
- ARM memory controllers are fast enough to do Rowhammer
- LPDDR\* found vulnerable
- No easy software fix

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# Conclusion

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- No easy software fix
- Using DMA bypasses state-of-the-art defenses (e.g., ANVIL)
- More details
  - Demos, statistics and test app:  
<https://vusec.net/projects/drammer>
  - Open source:  
<https://github.com/vusec/drammer>