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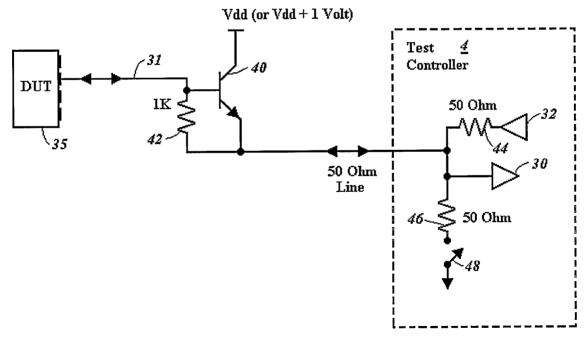
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(54) Title: BI-DIRECTIONAL BUFFER FOR INTERFACING TEST SYSTEM CHANNEL



(57) Abstract: An emitter follower or source follower transistor is provided in the channel of a wafer test system between a DUT and a test system controller to enable a low power DUT to drive a test system channel. A bypass resistor is included between the base and emitter of the emitter follower transistor to enable bi-directional signals to be provided between the DUT channel and test system controller, as well as to enable parametric tests to be performed. The emitter follower transistor and bypass resistor can be provided on the probe card, with a pull down termination circuit included in the test system controller. The test system controller can provide compensation for the base to emitter voltage drop of the emitter follower transistor.



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EAST; US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB Search Term: wafer and burn-in with (overcurrent near protection)		