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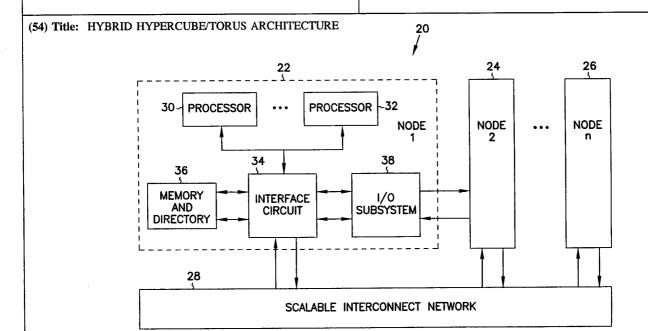
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(57) Abstract

A system and method for communicating between a plurality of processing element nodes within a scalable multiprocessor system. Each processing element node includes at least one processor and memory. A scalable interconnect network includes physical communication links interconnecting the processing element nodes in an n-dimensional topology. Routers in the scalable interconnect network route messages between the plurality of processing element nodes on the physical communication links. The routers are capable of routing messages in hypercube topologies of up to six dimensions, and further capable of routing messages in at least one n-dimensional torus or mesh topology, wherein the topology has at least one of the n dimensions having a radix greater than four.

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INTERNATIONAL SEARCH REPORT

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