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4 1  
5a 5b 4 1 2  
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8 4 1 가  
9 4 2 가  
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(43) (43) (42) 가 (45) (44) (47) (44)  
 (43) (46) (45) (46) (47) (48) 가  
 (45) (46) (44) (45) (48)  
 (42) (45) 가 가 (48) 가 (42) (48)  
 (45) 가 가 (48) 가 (42) (48)  
 3 (D) , EL (D) (S) (16,18) ,  
 3 (D) (D), (S) (16,18) (2) (S) (16,18) ,  
 (4) (4) (S1 Sm) (6) (2)  
 가 (D) (16,18)  
 EL (B) (R) (G)  
 EL (B) (R) 가 (G) (G)  
 가 가 (R) 가 (R) (G) (B)  
 EL (R) (G) 가 (B) (B)  
 가 EL (R) 가 (G) 가 (G) (B)  
 가 가 (R)

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 2 1 1 , 3 1 5 2 2 4 3  
 1 2 , 5 2 2 2 3  
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(84) , 2 (60) 3 (62)

1 1 2 (86,88) . (84) , 2 (60) 3 (62)

1 (86) 1 (52) 2 (60) . (S1

8 1 (52) (R) (R) (G) (SP)

Sn) (DP) . 1 (58) (80) (R) (DP) 1 (58) 1 (58) (D)

(DP) (82) (G) 1 (58) 1 (58) (SP) (DP)

1 (58) (DP) 1 (58) (D) ,

(SP) (DP) 8 (F) (SP)

(DP) (F) , (R) (G) (F) (SP)

2 (88) 2 (54) 3 (62) ,

2 (88) 1 (86) 1 (S1) 가 , 2 (88) 1

(S1) 2 (88) 9 2 (54) (B)

(64) (SP) Sn) (DP) 4 (64) (B) (DP) 4 (64)

(D) (SP) , 2 (8) (8)

8) 2 (88) Sm) 가 (Refresh pulse : RP) 2 (8)

(S1) (R)

10 7 1

10 , 1 (86) (SP)가 (S) IC(In

egrated Circuit : 90) , IC(90) (Vs) 4 (S4) , 4 (S)

4) (GND) 5 (S5) , IC(90) (C1)

(C1) (GND) (S1 Sn) 3 (S3) (S1 Sn)

1 2 (S1,S2) (S1,S2,S3,S4,S5)

1 (86) 3 4 (S3,S4) - (Vs) 3 4

(S3,S4) (C1) (SP) IC(90) (S1 Sn) IC(90) (C1)

Sn) (SP) , (SP)가 (S1 Sn) (SP)가 (S1)

4 (S4) 3 (S3)가 - 5 (S5)가 - 5 (S5)가 -

(C1) (GND) , 5 (S5) (C1)

(C1) (GND) (C1)

IC(90) (S1 Sn) IC(90) (S1 Sn) (SP) (86)

(SP) 2 (60) .

11 7

11 (80,82) (D1 Dm)

(S4) IC(92) , IC(92) (Vd) 4 (S4) , 4

(C2) (GND) 5 (S5) , IC(92)

(C2) (GND) (D1 Dm) 3 (S3) (D1 Dm)

IC(92) (D1 Dm) / / (D1 Dm)

1 2 (S1,S2) IC(92) / / (D1

(D1 Dm) (C2) IC(92) / / (D1

Dm) (S1,S2,S3,S4,S5)

(80,82,84) 10 (86,88) , (80,

82,84) (S) 가 가 (D1 Dm) (DP) (DP)

(S) 2 (54) 가 2 (88) Dm) (DP)

2 (88) 12 (84)



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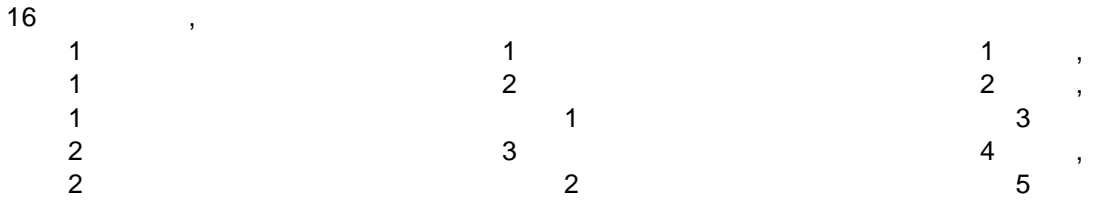
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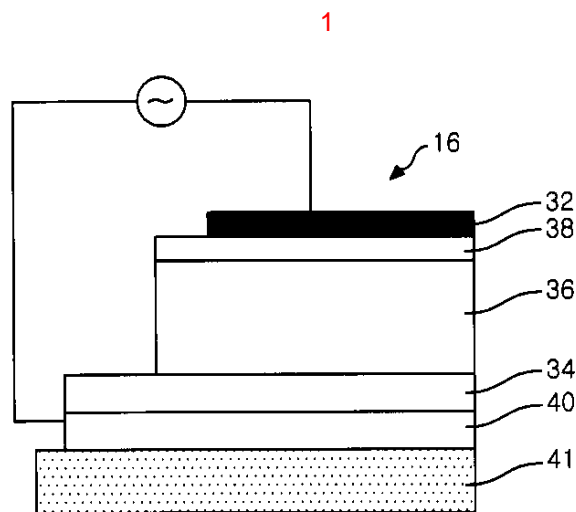
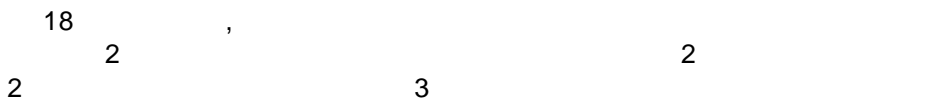
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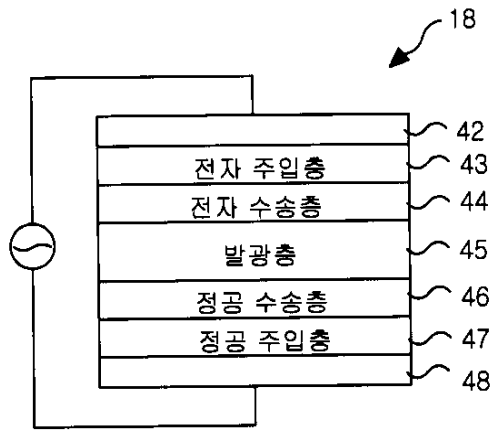


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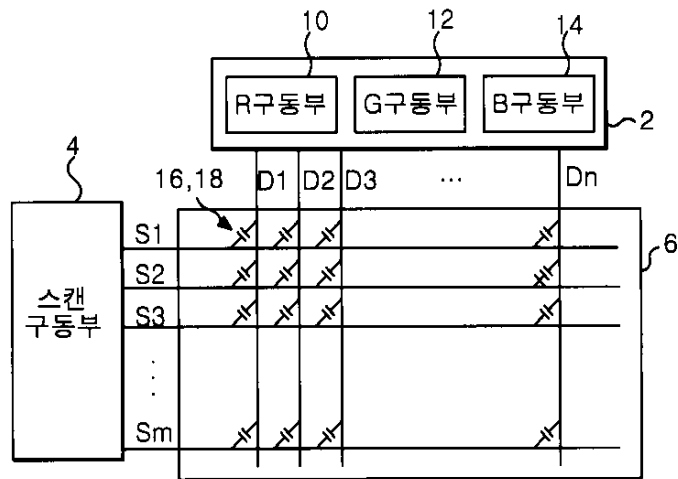




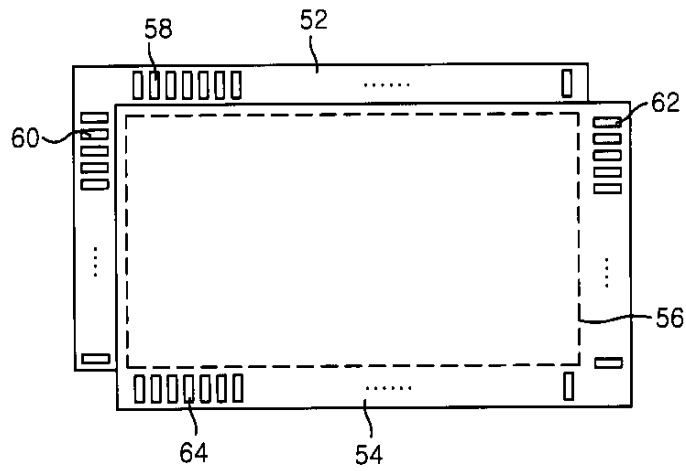
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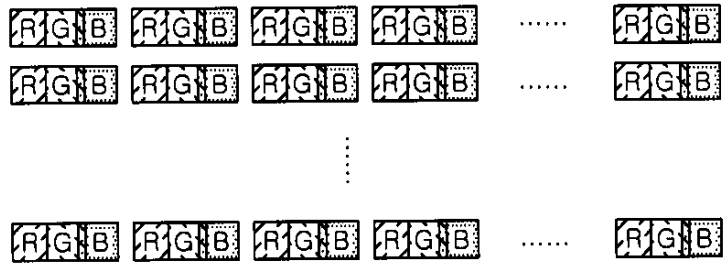
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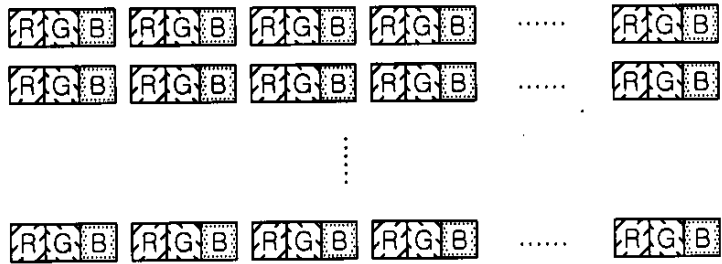
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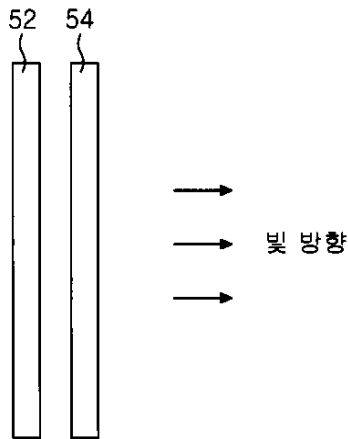
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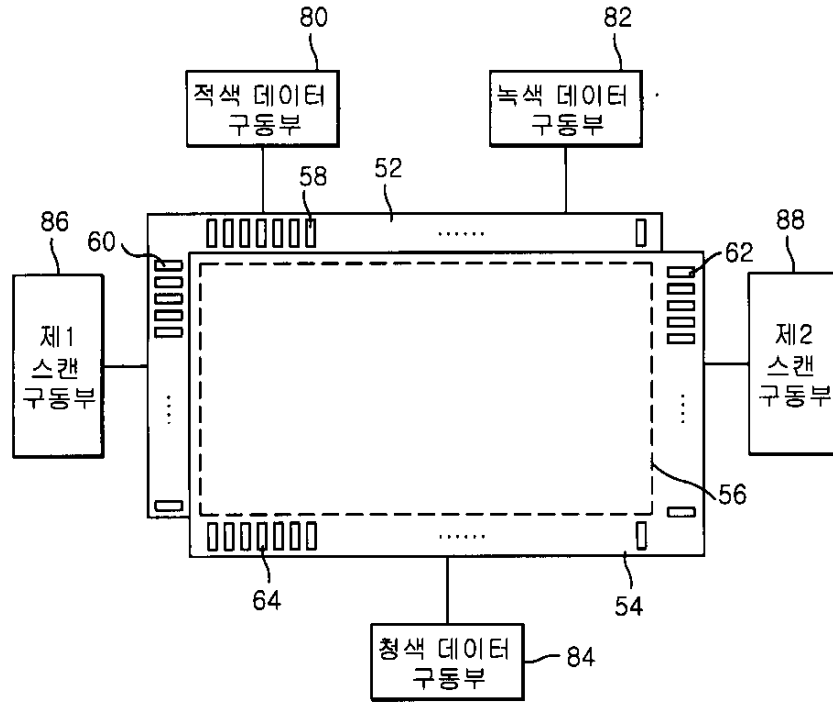
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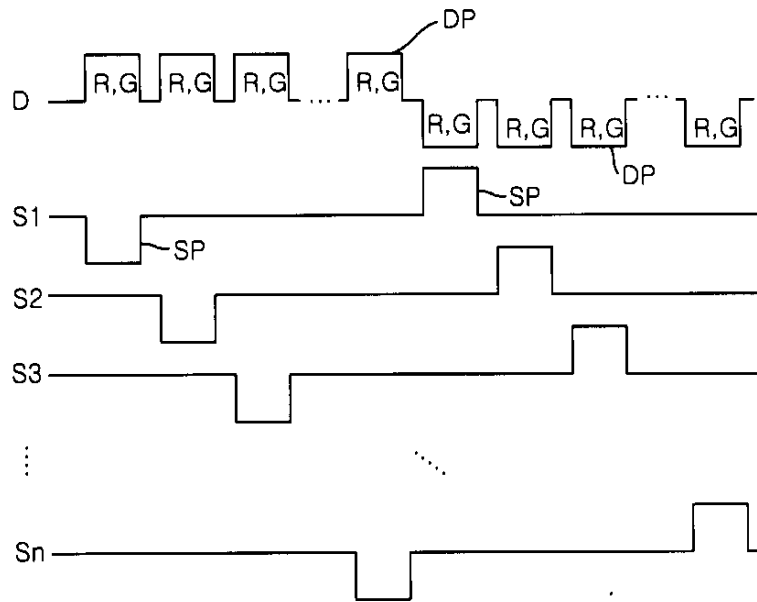
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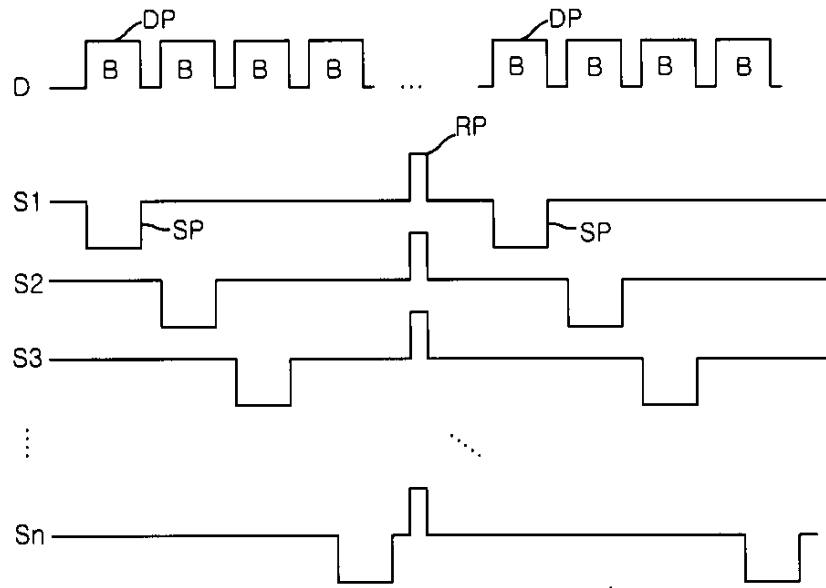
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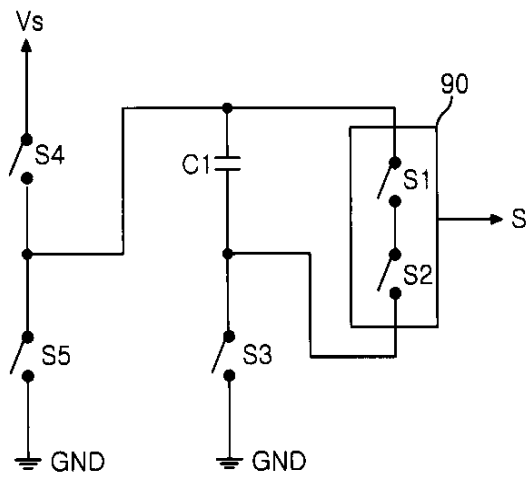
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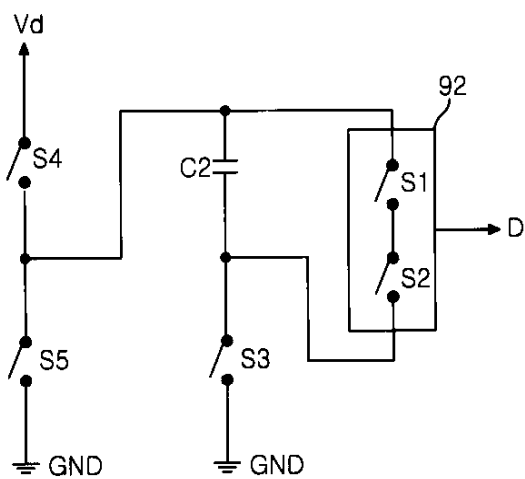
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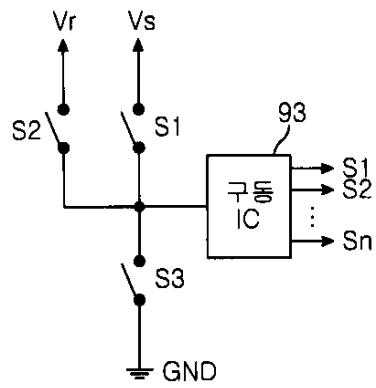
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